SPEC® CFP2006 Result

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Intel Corporation

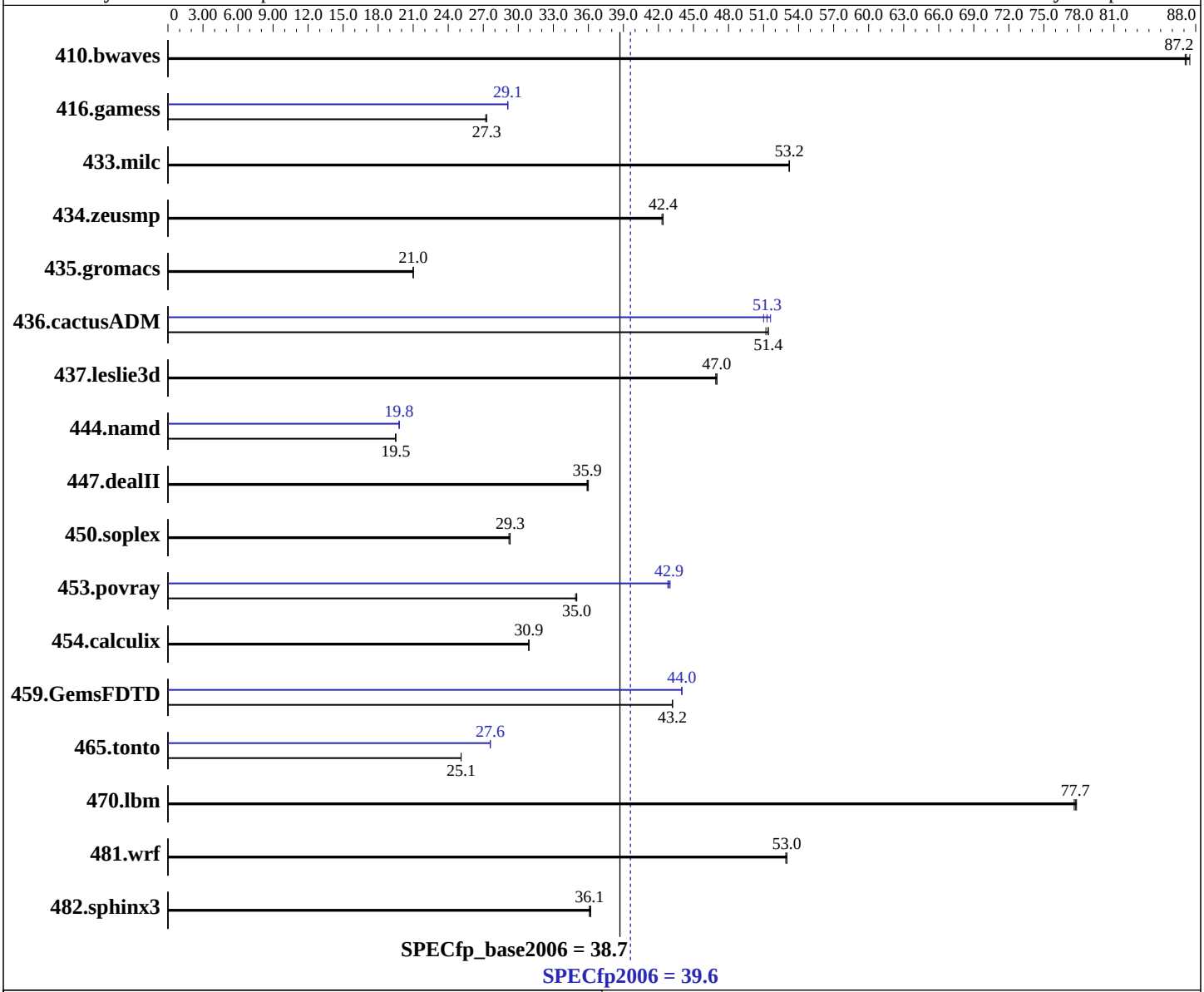
Intel DH61WW motherboard (Intel Pentium G850)

SPECfp®2006 = 39.6

SPECfp_base2006 = 38.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Oct-2011
Hardware Availability: May-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Pentium G850
CPU Characteristics:	
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate SP1 (64-bit)
Compiler:	C/C++: Version 12.0.3.176 of Intel C++ Studio XE for Windows; Fortran: Version 12.0.3.176 of Intel Visual Fortran Studio XE for Windows; Libraries: Version 15.00.30729.01 of Microsoft Visual Studio 2008 Professional SP1
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache:	3 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	Seagate 250GB HDD, 7200 rpm
Other Hardware:	None

System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**156**	**87.2**	156	87.1	155	87.5	**156**	**87.2**	156	87.1	155	87.5
416.gamess	719	27.2	718	27.3	**719**	**27.3**	673	29.1	673	29.1	**673**	**29.1**
433.milc	173	53.2	172	53.2	**172**	**53.2**	173	53.2	172	53.2	**172**	**53.2**
434.zeusmp	215	42.3	**215**	**42.4**	215	42.4	215	42.3	**215**	**42.4**	215	42.4
435.gromacs	**340**	**21.0**	340	21.0	340	21.0	**340**	**21.0**	340	21.0	340	21.0
436.cactusADM	**233**	**51.4**	233	51.2	233	51.4	234	51.0	**233**	**51.3**	232	51.6
437.leslie3d	200	47.0	201	46.9	**200**	**47.0**	200	47.0	201	46.9	**200**	**47.0**
444.namd	412	19.5	**412**	**19.5**	412	19.5	405	19.8	**405**	**19.8**	405	19.8
447.dealII	319	35.9	**318**	**35.9**	318	36.0	319	35.9	**318**	**35.9**	318	36.0
450.soplex	285	29.3	285	29.2	**285**	**29.3**	285	29.3	285	29.2	**285**	**29.3**
453.povray	152	35.0	152	34.9	**152**	**35.0**	124	42.8	**124**	**42.9**	124	43.0
454.calculix	267	30.9	**267**	**30.9**	267	30.9	267	30.9	**267**	**30.9**	267	30.9
459.GemsFDTD	**246**	**43.2**	246	43.2	245	43.2	241	44.0	241	44.0	**241**	**44.0**
465.tonto	**393**	**25.1**	392	25.1	393	25.1	357	27.6	**357**	**27.6**	357	27.6
470.lbm	**177**	**77.7**	177	77.6	177	77.8	**177**	**77.7**	177	77.6	177	77.8
481.wrf	**211**	**53.0**	211	53.0	211	52.9	**211**	**53.0**	211	53.0	211	52.9
482.sphinx3	538	36.2	**539**	**36.1**	539	36.1	538	36.2	**539**	**36.1**	539	36.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Component Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

General Notes

```
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qauto-ilp32 /F1000000000

C++ benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qcxx-features -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias
-Qopt-prefetch -Qauto-ilp32 /F1000000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
shlW64M.lib -link /FORCE:MULTIPLE

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias
-Qscalar-rep- /F1000000000

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel /F1000000000

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2 -Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.xml

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For other inquiries, please contact webmaster@spec.org.

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