



SPEC[®] CFP2006 Result

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Intel Corporation

SPECfp[®]_rate2006 = 57.4

Intel DH61WW motherboard (Intel Pentium G850)

SPECfp_rate_base2006 = 56.7

CPU2006 license: 13

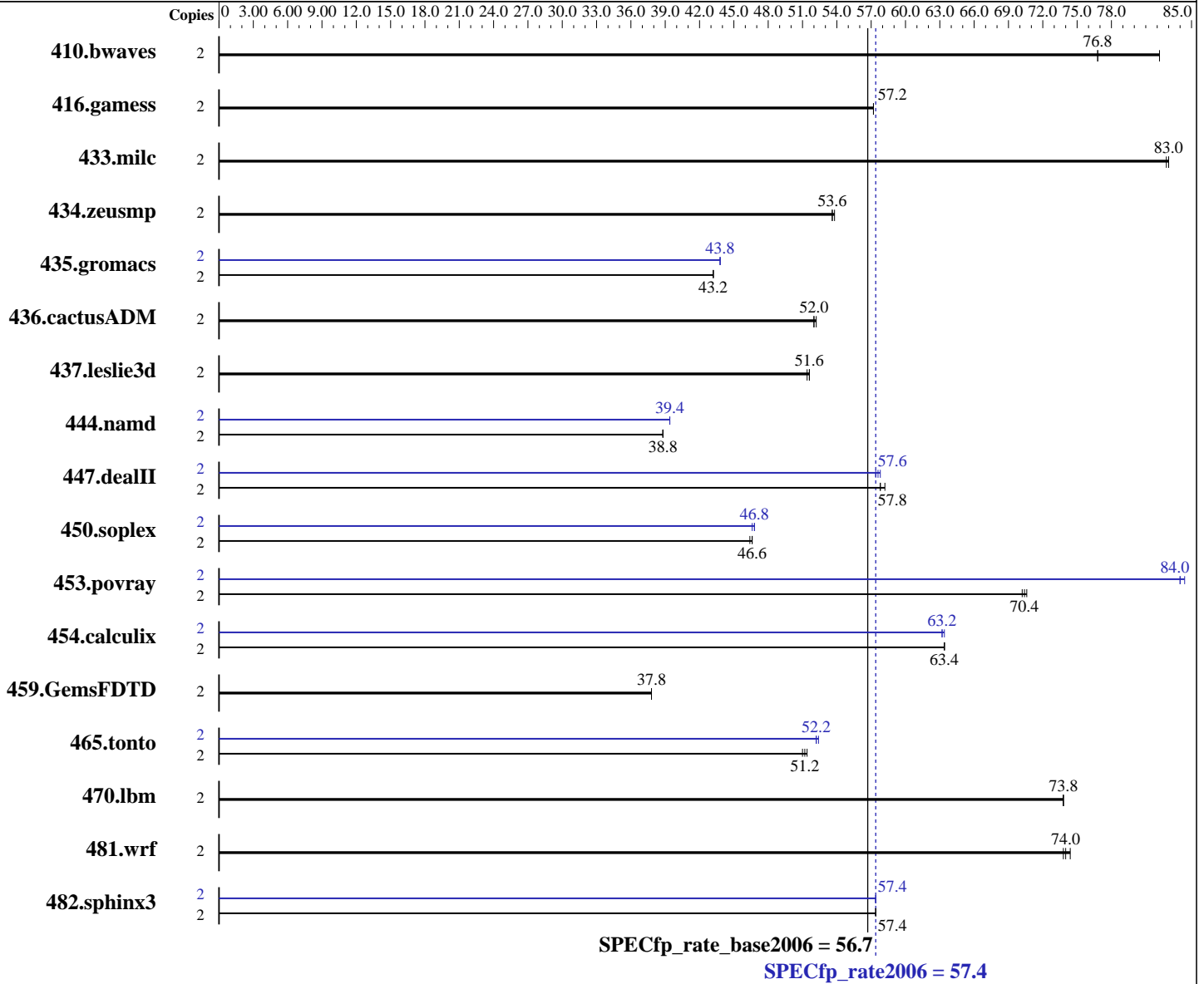
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Oct-2011

Hardware Availability: May-2011

Software Availability: Apr-2011



Hardware

CPU Name: Intel Pentium G850
 CPU Characteristics:
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Windows 7 Ultimate SP1 (64-bit)
 Compiler: C/C++: Version 12.0.3.176 of Intel C++ Studio XE for Windows;
 Fortran: Version 12.0.3.176 of Intel Visual Fortran Studio XE for Windows;
 Libraries: Version 15.00.30729.01 of Microsoft Visual Studio 2008 Professional SP1
 Auto Parallel: No
 File System: NTFS

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L3 Cache: 3 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 250GB HDD, 7200 rpm
 Other Hardware: None

System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	2	331	82.2	354	76.8	354	76.8	2	331	82.2	354	76.8	354	76.8		
416.gamess	2	685	57.2	685	57.2	686	57.2	2	685	57.2	685	57.2	686	57.2		
433.milc	2	222	82.8	221	83.0	221	83.0	2	222	82.8	221	83.0	221	83.0		
434.zeusmp	2	339	53.8	339	53.6	339	53.6	2	339	53.8	339	53.6	339	53.6		
435.gromacs	2	331	43.2	331	43.2	331	43.2	2	326	43.8	326	43.8	326	43.8		
436.cactusADM	2	460	52.0	458	52.2	459	52.0	2	460	52.0	458	52.2	459	52.0		
437.leslie3d	2	364	51.6	366	51.4	364	51.6	2	364	51.6	366	51.4	364	51.6		
444.namd	2	413	38.8	413	38.8	413	38.8	2	407	39.4	407	39.4	407	39.4		
447.dealII	2	396	57.8	395	57.8	394	58.2	2	396	57.8	399	57.4	397	57.6		
450.soplex	2	360	46.4	358	46.6	358	46.6	2	357	46.6	356	46.8	356	46.8		
453.povray	2	151	70.2	151	70.6	151	70.4	2	126	84.4	127	84.0	127	84.0		
454.calculix	2	260	63.4	260	63.4	260	63.4	2	261	63.2	261	63.2	261	63.4		
459.GemsFDTD	2	562	37.8	562	37.8	562	37.8	2	562	37.8	562	37.8	562	37.8		
465.tonto	2	383	51.4	385	51.0	385	51.2	2	376	52.2	376	52.4	377	52.2		
470.lbm	2	372	73.8	373	73.8	373	73.8	2	372	73.8	373	73.8	373	73.8		
481.wrf	2	303	73.8	301	74.4	302	74.0	2	303	73.8	301	74.4	302	74.0		
482.sphinx3	2	678	57.4	679	57.4	678	57.4	2	679	57.4	678	57.4	679	57.4		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 The start command with the /affinity switch was used to bind processes to cores

Component Notes

Tested systems can be used with Shin-G ATX case,
 PC Power and Cooling 1200W power supply

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qstd=c99

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Base Compiler Invocation (Continued)

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
 416.gamess: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
 434.zeusmp: -DSPEC_CPU_P64
 435.gromacs: -DSPEC_CPU_P64
 436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
 437.leslie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
 459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE



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Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
shlW64M.lib -link /FORCE:MULTIPLE

447.dealIII: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

450.soplex: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.20111012.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings-revC.xml>

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For other inquiries, please contact webmaster@spec.org.

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