SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

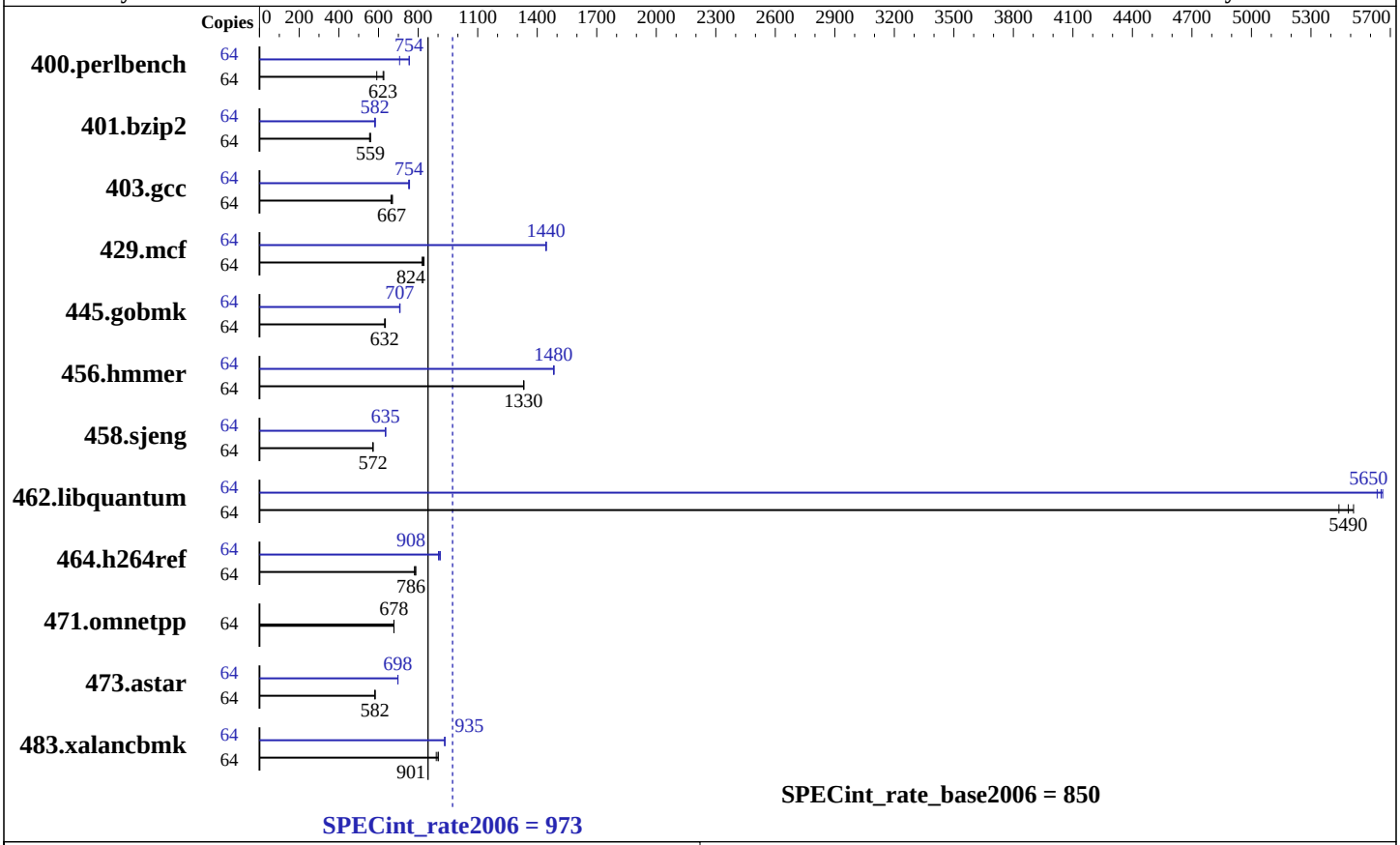
PowerEdge R815
(AMD Opteron 6276, 2.30 GHz)

SPECint®_rate2006 = 973

SPECint_rate_base2006 = 850

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2011
Hardware Availability: Nov-2011
Software Availability: Jul-2011



Hardware

CPU Name:	AMD Opteron 6276
CPU Characteristics:	AMD Turbo CORE technology up to 3.20 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	64 cores, 4 chips, 16 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	512 KB I on chip per chip, 64 KB I shared / 2 cores; 16 KB D on chip per core
Secondary Cache:	16 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache:	16 MB I+D on chip per chip, 8 MB shared / 8 cores
Other Cache:	None
Memory:	256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	2 x 160 GB SAS, 15000 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 4.2.5.2 of x86 Open64 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap 10.0 32-bit Library for Linux

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge R815
(AMD Opteron 6276, 2.30 GHz)

SPECint_rate2006 = 973

SPECint_rate_base2006 = 850

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2011
Hardware Availability: Nov-2011
Software Availability: Jul-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	1058	591	**1003**	**623**	998	627	64	885	706	**829**	**754**	828	755
401.bzip2	64	**1105**	**559**	1113	555	1104	559	64	1062	581	**1061**	**582**	1058	584
403.gcc	64	777	663	**772**	**667**	768	671	64	685	752	**683**	**754**	681	756
429.mcf	64	704	829	712	820	**709**	**824**	64	404	1450	**404**	**1440**	404	1440
445.gobmk	64	1058	635	1063	632	**1063**	**632**	64	948	708	**949**	**707**	950	706
456.hmmer	64	449	1330	448	1330	**448**	**1330**	64	**403**	**1480**	402	1480	403	1480
458.sjeng	64	1354	572	**1354**	**572**	1353	572	64	1217	636	**1219**	**635**	1219	635
462.libquantum	64	244	5440	**242**	**5490**	240	5510	64	235	5630	**235**	**5650**	234	5660
464.h264ref	64	1813	781	**1801**	**786**	1798	788	64	**1560**	**908**	1570	902	1554	912
471.omnetpp	64	590	678	590	678	**590**	**678**	64	590	678	590	678	**590**	**678**
473.astar	64	772	582	**772**	**582**	771	582	64	644	698	**644**	**698**	644	698
483.xalancbmk	64	496	891	**490**	**901**	490	901	64	473	933	473	935	**473**	**935**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Binaries were compiled on a system with 2x AMD Opteron 6276 chips + 128GB Memory using RHEL 6.1
Huge pages, transparent Huge pages, and space randomization controlled with the following settings:
echo 57344 > /proc/sys/vm/nr_hugepages
mount -t hugetlbfs nodev /mnt/hugepages
echo never > /sys/kernel/mm/redhat_transparent_hugepage/enabled
echo 0 > /proc/sys/kernel/randomize_va_space=0
```

Platform Notes

```
'Power Management' set to 'Maximum Performance' in BIOS
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd1104-rate-libs-revA/32:/root/cpu2006-1.1/amd1104-rate-libs-revA/64"
The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
```

Continued on next page

General Notes (Continued)

http://developer.amd.com/cpu/open64

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-march=bdver1 -Ofast -CG:local_sched_alg=1 -INLINE:aggressive=on
-IPA:plimit=8000 -IPA:small_pu=100 -HP:bd=2m:heap=2m -mso
-LNO:prefetch=2

C++ benchmarks:
-march=bdver1 -Ofast -m32 -INLINE:aggressive=on -CG:cmp_peep=on
-D__OPEN64_FAST_SET -L/root/work/libraries/SmartHeap-10/lib -lsmartheap

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=2 -LNO:opt=0
-IPA:plimit=20000 -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
-WOPT:if_conv=0 -WOPT:sib=on -CG:local_sched_alg=1
-CG:unroll_fb_req=on -CG:movext_icmp=off -HP:bd=2m:heap=2m

401.bzip2: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:prefetch=2 -LNO:pf2=0
-OPT:alias=disjoint -OPT:goto=off -CG:local_sched_alg=1
-HP:bdt=2m:heap=2m

403.gcc: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
-CG:cmp_peep=on -CG:pre_minreg_level=2 -m32
-HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on

429.mcf: -march=bdver1 -O3 -OPT:unroll_times_max=5 -ipa
-INLINE:aggressive=on -CG:gcm=off
-GRA:prioritize_by_density=on -m32 -HP:bdt=2m:heap=2m -mso

445.gobmk: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:unroll_size=256
-OPT:unroll_times_max=8 -OPT:keep_ext=on -IPA:plimit=750
-IPA:min_hotness=300 -IPA:pu_reorder=1
-LNO:ignore_feedback=off -WOPT:if_conv=2 -HP:bd=2m:heap=2m

456.hmmer: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=2
-OPT:alias=disjoint -OPT:unroll_times_max=16
-OPT:unroll_size=512 -OPT:unroll_level=2 -OPT:keep_ext=on
-CG:cflow=0 -CG:cmp_peep=on -CG:pre_local_sched=off
-HP:bdt=2m:heap=2m

Continued on next page

Peak Optimization Flags (Continued)

458.sjeng: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -CG:ptr_load_use=0
-CG:divrem_opt=on -CG:movext_icmp=off -CG:locs_best=on
-LNO:full_unroll=10 -IPA:pu_reorder=2 -HP:bd=2m:heap=2m
-WOPT:sib=on

462.libquantum: -march=bdver1 -Ofast -mso -OPT:unroll_size=512
-OPT:unroll_times_max=16 -LNO:prefetch=2
-LNO:prefetch_ahead=4 -LNO:pf2=0 -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=15000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m,limit=300

464.h264ref: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:unroll_size=256
-OPT:unroll_times_max=2 -IPA:plimit=20000
-OPT:alias=disjoint -CG:ptr_load_use=0
-CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -march=bdver1 -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off
-WOPT:if_conv=0 -WOPT:sib=on -CG:divrem_opt=on
-GRA:optimize_boundary=on -OPT:alias=disjoint
-INLINE:aggressive=on -IPA:small_pu=3000 -IPA:plimit=3000
-m32 -HP:bdt=2m:heap=2m

483.xalancbmk: -march=bdver1 -Ofast -LNO:prefetch=2 -OPT:unroll_size=512
-OPT:unroll_times_max=8 -D__OPEN64_FAST_SET
-INLINE:aggressive=on -m32 -CG:cmp_peep=on
-CG:local_sched=off -GRA:unspill=on -TENV:frame_pointer=off
-fno-emit-exceptions
-L/root/work/libraries/SmartHeap-10/lib -lsmartheap

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/amd1104-platform-rate-revA.20111122.html
http://www.spec.org/cpu2006/flags/x86-open64-425-flags-rate-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/amd1104-platform-rate-revA.20111122.xml
http://www.spec.org/cpu2006/flags/x86-open64-425-flags-rate-revA.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Thu Jul 24 01:31:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 November 2011.