SPEC® CINT2006 Result
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Intel Corporation

Intel DZ68DB motherboard (Intel Core i7-2600K)

SPECint®_rate2006 = 170

SPECint_rate_base2006 = 164
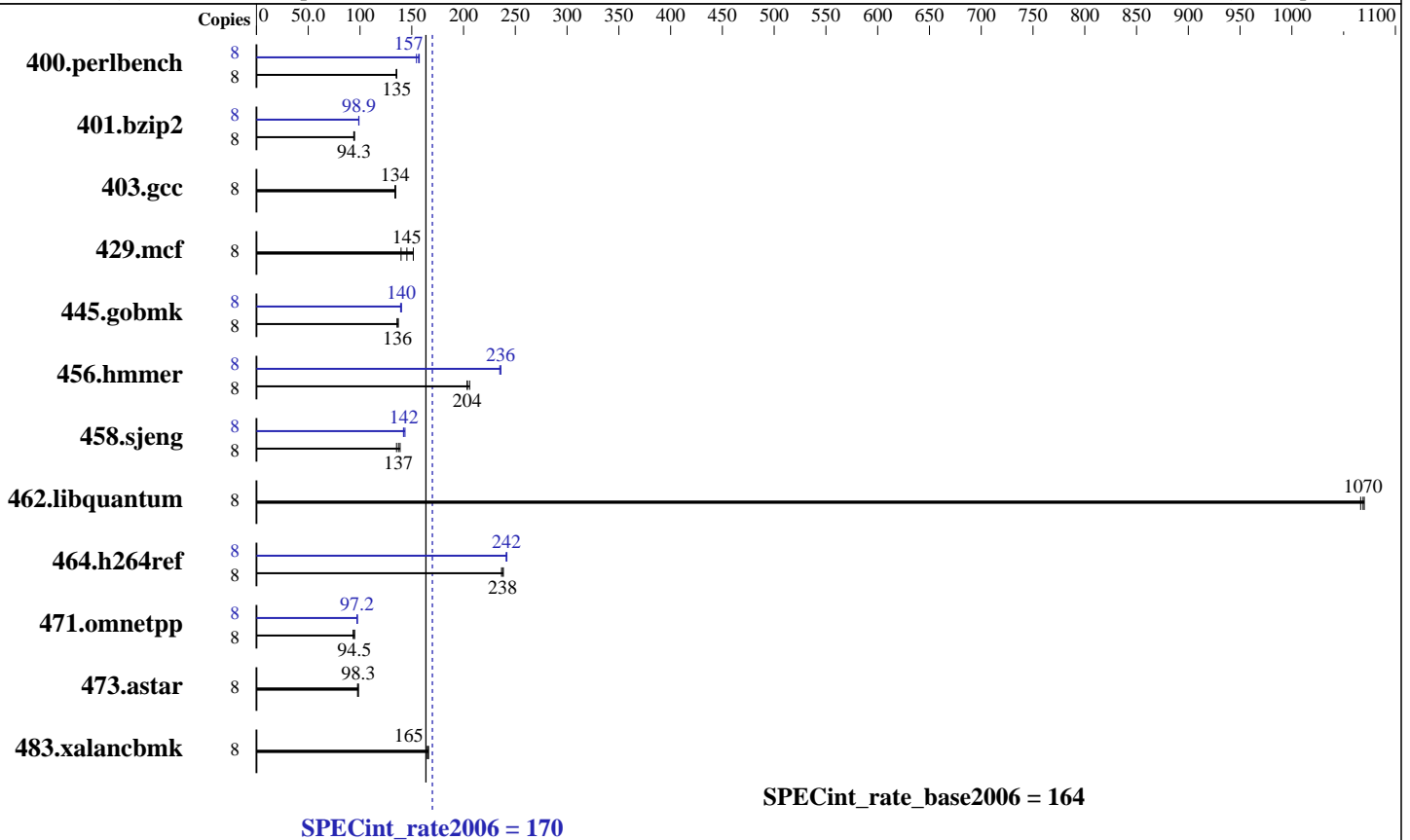
CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Aug-2011

Hardware Availability: May-2011

Software Availability: Sep-2011

	Copies	value (blue)	value (base)
400.perlbench	8 / 8	157	135
401.bzip2	8 / 8	98.9	94.3
403.gcc	8	134	
429.mcf	8	145	
445.gobmk	8 / 8	140	136
456.hmmer	8 / 8	236	204
458.sjeng	8 / 8	142	137
462.libquantum	8	1070	
464.h264ref	8 / 8	242	238
471.omnetpp	8 / 8	97.2	94.5
473.astar	8	98.3	
483.xalancbmk	8	165	

SPECint_rate_base2006 = 164

SPECint_rate2006 = 170

Hardware

CPU Name:	Intel Core i7-2600K
CPU Characteristics:	Intel Turbo Boost Technology up to 3.8 GHz
CPU MHz:	3401
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	1 TB Seagate SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 5 (X11)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	577	135	**577**	**135**	579	135	8	506	154	497	157	**499**	**157**
401.bzip2	8	822	94.0	**819**	**94.3**	815	94.7	8	780	99.0	**781**	**98.9**	782	98.8
403.gcc	8	479	134	481	134	**480**	**134**	8	479	134	481	134	**480**	**134**
429.mcf	8	522	140	**502**	**145**	482	151	8	522	140	**502**	**145**	482	151
445.gobmk	8	**616**	**136**	612	137	618	136	8	603	139	**600**	**140**	600	140
456.hmmer	8	**367**	**204**	363	206	367	204	8	**316**	**236**	318	235	316	236
458.sjeng	8	716	135	**706**	**137**	698	139	8	682	142	**680**	**142**	675	143
462.libquantum	8	155	1070	**155**	**1070**	155	1070	8	155	1070	**155**	**1070**	155	1070
464.h264ref	8	742	238	748	237	**745**	**238**	8	**733**	**242**	734	241	732	242
471.omnetpp	8	536	93.3	528	94.7	**529**	**94.5**	8	515	97.1	**514**	**97.2**	513	97.5
473.astar	8	575	97.6	571	98.4	**571**	**98.3**	8	575	97.6	571	98.4	**571**	**98.3**
483.xalancbmk	8	334	165	**334**	**165**	332	166	8	334	165	**334**	**165**	332	166

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /home/spec/cpu2006.1.2/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ 8787f7622badcf24e01c368b1db4377c
running on rhel61-rahul.sc.intel.com Fri Aug 26 22:23:16 2011

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Core(TM) i7-2600K CPU @ 3.40GHz
      1 "physical id"s (chips)
      8 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 4
      siblings : 8

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Platform Notes (Continued)

```
      physical 0: cores 0 1 2 3
   cache size : 8192 KB

 From /proc/meminfo
    MemTotal:        7966960 kB
    HugePages_Total:       0
    Hugepagesize:       2048 kB

 /usr/bin/lsb_release -d
    Red Hat Enterprise Linux Server release 6.1 (Santiago)

 From /etc/*release* /etc/*version*
    redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
    system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

 uname -a:
    Linux rhel61-rahul.sc.intel.com 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
    15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

 run-level 5 Aug 24 19:39

 SPEC is set to: /home/spec/cpu2006.1.2
    Filesystem     Type    Size  Used Avail Use% Mounted on
    /dev/mapper/vg_rhel61rahul-lv_home
                   ext4    862G   28G  791G   4% /home

 (End of data from sysinfo program)
```

Component Notes

```
 Tested systems can be used with Shin-G ATX case,
 PC Power and Cooling 1200W power supply
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 LD_LIBRARY_PATH = "/home/spec/cpu2006.1.2/smartheap:/home/spec/cpu2006.1.2/ic12.1-libs/ia32:/home/spec/cpu2006.1.2/ic12.1-libs/intel64"

 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
```

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Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
```

C++ benchmarks:
```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:
```
icpc -m32
```

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Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
    400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

        401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
                   -auto-ilp32 -ansi-alias

          403.gcc: basepeak = yes

          429.mcf: basepeak = yes

        445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                   -ansi-alias

       456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

        458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
                   -auto-ilp32

    462.libquantum: basepeak = yes

      464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                   -ansi-alias
```

C++ benchmarks:

```
      471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                   -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
                   -opt-ra-region-strategy=block -Wl,-z,muldefs
                   -L/smartheap -lsmartheap

        473.astar: basepeak = yes

    483.xalancbmk: basepeak = yes
```

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.html`
`http://www.spec.org/cpu2006/flags/Intel-Platform-Settings-V1.2-revA.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.xml`
`http://www.spec.org/cpu2006/flags/Intel-Platform-Settings-V1.2-revA.xml`

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 01:42:24 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 October 2011.