SPEC® CFP2006 Result

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Fujitsu

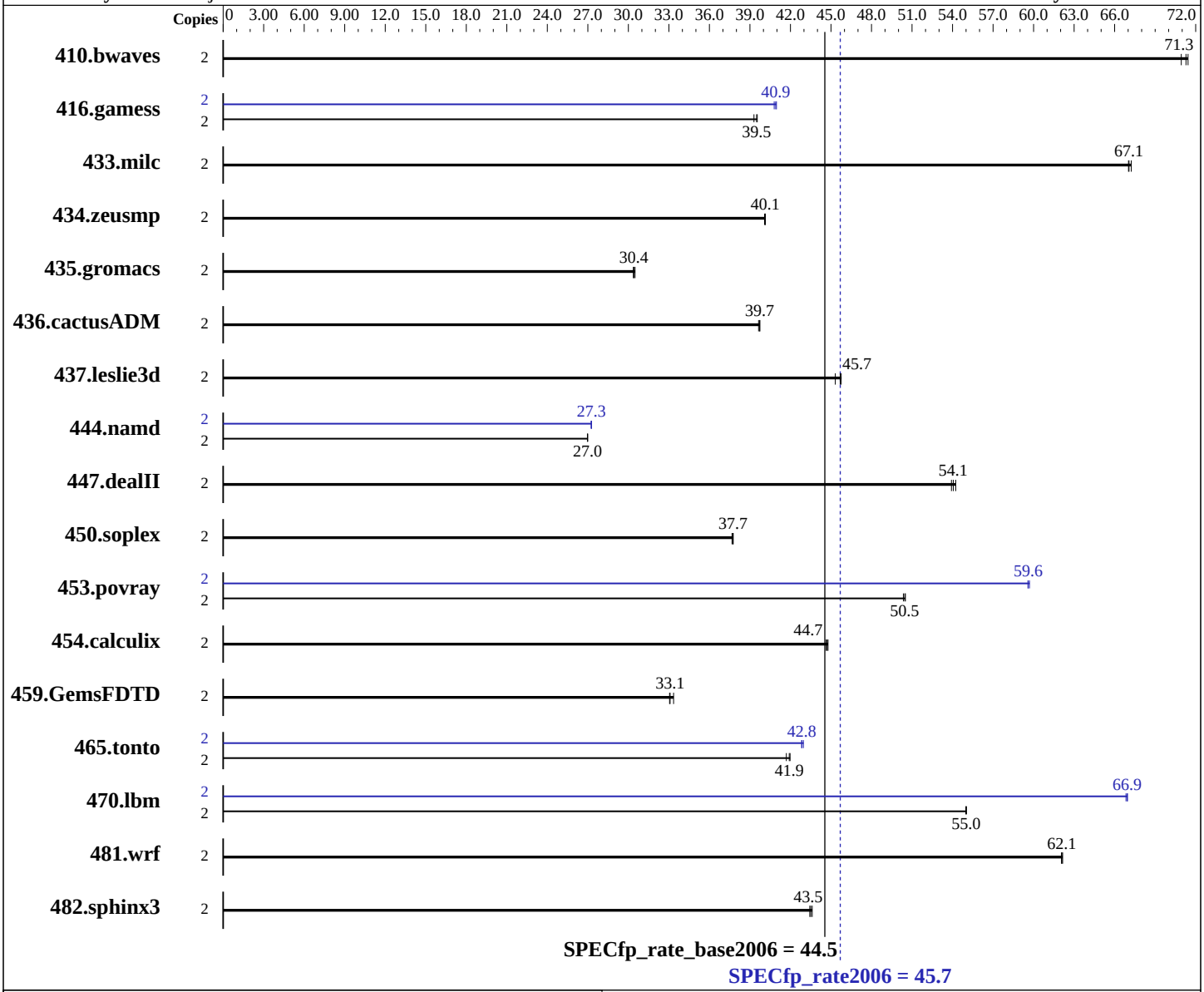
PRIMERGY TX120 S3, Intel Celeron G530T, 2.0 GHz

SPECfp®_rate2006 = 45.7

SPECfp_rate_base2006 = 44.5

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Sep-2011
Hardware Availability: Sep-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Celeron G530T
CPU Characteristics:	
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1067 MHz and CL7)
Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	**381**	**71.3**	383	70.9	380	71.4	2	**381**	**71.3**	383	70.9	380	71.4
416.gamess	2	990	39.5	**992**	**39.5**	996	39.3	2	960	40.8	956	41.0	**957**	**40.9**
433.milc	2	274	67.0	**274**	**67.1**	273	67.2	2	274	67.0	**274**	**67.1**	273	67.2
434.zeusmp	2	454	40.1	453	40.1	**454**	**40.1**	2	454	40.1	453	40.1	**454**	**40.1**
435.gromacs	2	470	30.4	468	30.5	**470**	**30.4**	2	470	30.4	468	30.5	**470**	**30.4**
436.cactusADM	2	603	39.6	**602**	**39.7**	602	39.7	2	603	39.6	**602**	**39.7**	602	39.7
437.leslie3d	2	411	45.8	415	45.3	**412**	**45.7**	2	411	45.8	415	45.3	**412**	**45.7**
444.namd	2	595	27.0	**594**	**27.0**	594	27.0	2	588	27.3	**588**	**27.3**	589	27.2
447.dealII	2	422	54.2	**423**	**54.1**	424	53.9	2	422	54.2	**423**	**54.1**	424	53.9
450.soplex	2	442	37.8	**442**	**37.7**	443	37.7	2	442	37.8	**442**	**37.7**	443	37.7
453.povray	2	**211**	**50.5**	211	50.5	211	50.4	2	**179**	**59.6**	179	59.6	178	59.7
454.calculix	2	**369**	**44.7**	370	44.7	368	44.8	2	**369**	**44.7**	370	44.7	368	44.8
459.GemsFDTD	2	636	33.3	642	33.1	**641**	**33.1**	2	636	33.3	642	33.1	**641**	**33.1**
465.tonto	2	472	41.7	469	42.0	**470**	**41.9**	2	458	43.0	460	42.8	**459**	**42.8**
470.lbm	2	500	55.0	499	55.0	**500**	**55.0**	2	**411**	**66.9**	411	66.8	410	67.0
481.wrf	2	359	62.2	**360**	**62.1**	360	62.1	2	359	62.2	**360**	**62.1**	360	62.1
482.sphinx3	2	894	43.6	898	43.4	**896**	**43.5**	2	894	43.6	898	43.4	**896**	**43.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 Large pages were not enabled for this run
```

General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on RHEL5.5
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: basepeak = yes

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Peak Optimization Flags (Continued)

416.gamess: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2` `-inline-level=0 -scalar-rep- -static`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html`
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml`

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For other inquiries, please contact webmaster@spec.org.

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