SPEC® CFP2006 Result

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Fujitsu

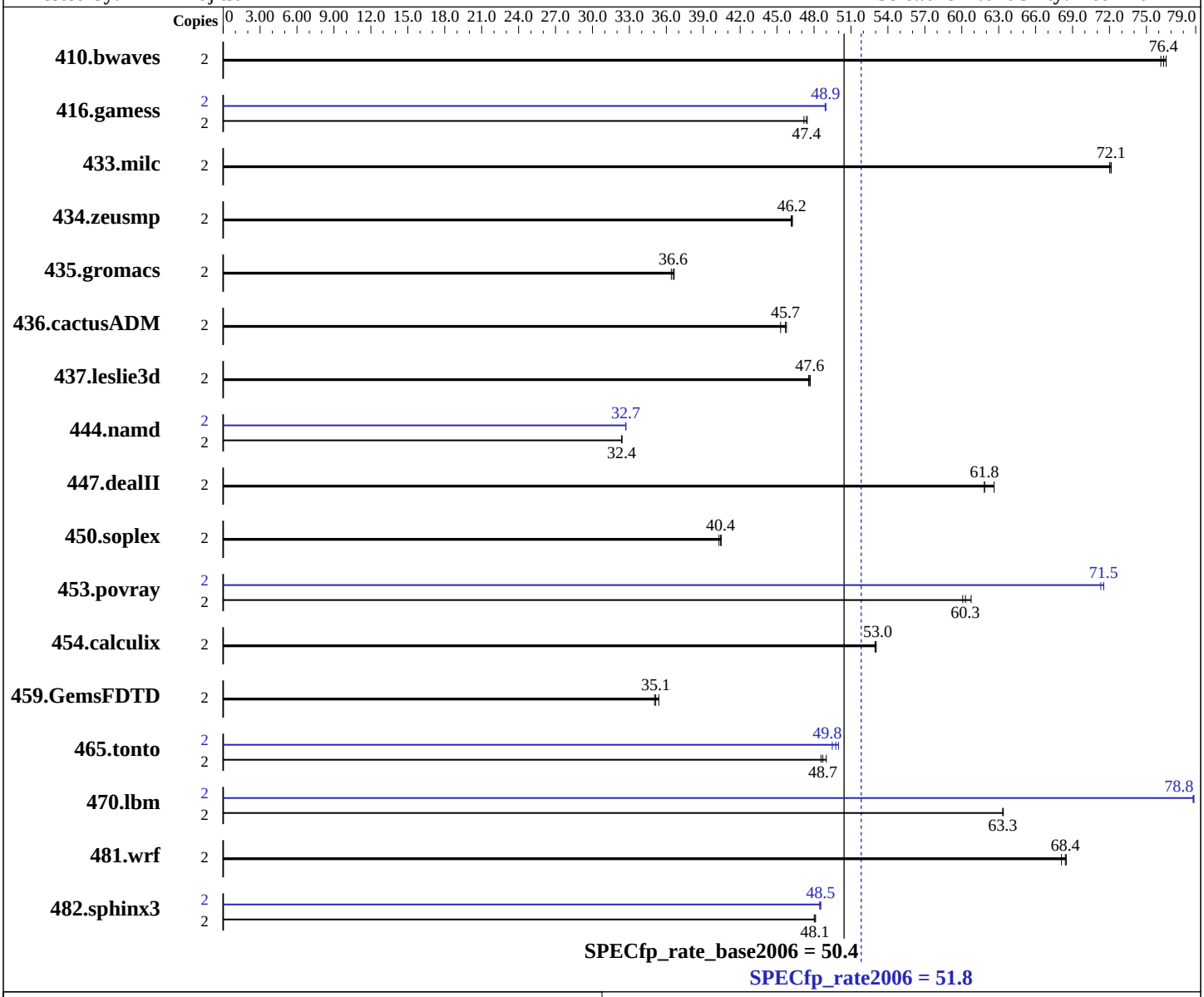
PRIMERGY TX100 S3, Intel Celeron G530, 2.40 GHz

SPECfp®_rate2006 = 51.8

SPECfp_rate_base2006 = 50.4

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Aug-2011
Hardware Availability: Sep-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Celeron G530
CPU Characteristics:	
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 2 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1067 MHz and CL7)
Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	355	76.6	357	76.2	**356**	**76.4**	2	355	76.6	357	76.2	**356**	**76.4**
416.gamess	2	825	47.4	**826**	**47.4**	830	47.2	2	800	48.9	801	48.9	**800**	**48.9**
433.milc	2	**255**	**72.1**	255	72.1	255	72.0	2	**255**	**72.1**	255	72.1	255	72.0
434.zeusmp	2	**394**	**46.2**	394	46.1	394	46.2	2	**394**	**46.2**	394	46.1	394	46.2
435.gromacs	2	**391**	**36.6**	392	36.4	390	36.6	2	**391**	**36.6**	392	36.4	390	36.6
436.cactusADM	2	522	45.8	**523**	**45.7**	528	45.3	2	522	45.8	**523**	**45.7**	528	45.3
437.leslie3d	2	**395**	**47.6**	394	47.7	395	47.6	2	**395**	**47.6**	394	47.7	395	47.6
444.namd	2	**495**	**32.4**	495	32.4	495	32.4	2	490	32.7	**490**	**32.7**	490	32.7
447.dealII	2	365	62.6	370	61.8	**370**	**61.8**	2	365	62.6	370	61.8	**370**	**61.8**
450.soplex	2	**413**	**40.4**	412	40.5	414	40.3	2	**413**	**40.4**	412	40.5	414	40.3
453.povray	2	**176**	**60.3**	177	60.1	175	60.8	2	149	71.5	149	71.3	**149**	**71.5**
454.calculix	2	311	53.1	**311**	**53.0**	312	53.0	2	311	53.1	**311**	**53.0**	312	53.0
459.GemsFDTD	2	606	35.0	600	35.4	**604**	**35.1**	2	606	35.0	600	35.4	**604**	**35.1**
465.tonto	2	402	49.0	**404**	**48.7**	405	48.6	2	**395**	**49.8**	394	50.0	398	49.5
470.lbm	2	434	63.4	**434**	**63.3**	434	63.3	2	349	78.8	**349**	**78.8**	348	78.9
481.wrf	2	328	68.1	326	68.5	**327**	**68.4**	2	328	68.1	326	68.5	**327**	**68.4**
482.sphinx3	2	812	48.0	810	48.1	**811**	**48.1**	2	803	48.5	805	48.4	**803**	**48.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 Large pages were not enabled for this run
```

General Notes

```
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on RHEL5.5
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

482.sphinx3: icc -m32

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias` `-auto-ilp32`

447.dealII: `basepeak = yes`

450.soplex: `basepeak = yes`

453.povray: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -ansi-alias` `-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll2` `-inline-level=0 -scalar-rep- -static`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto` `-inline-calloc -opt-malloc-options=3` `-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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