# SPEC® CFP2006 Result

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## Fujitsu

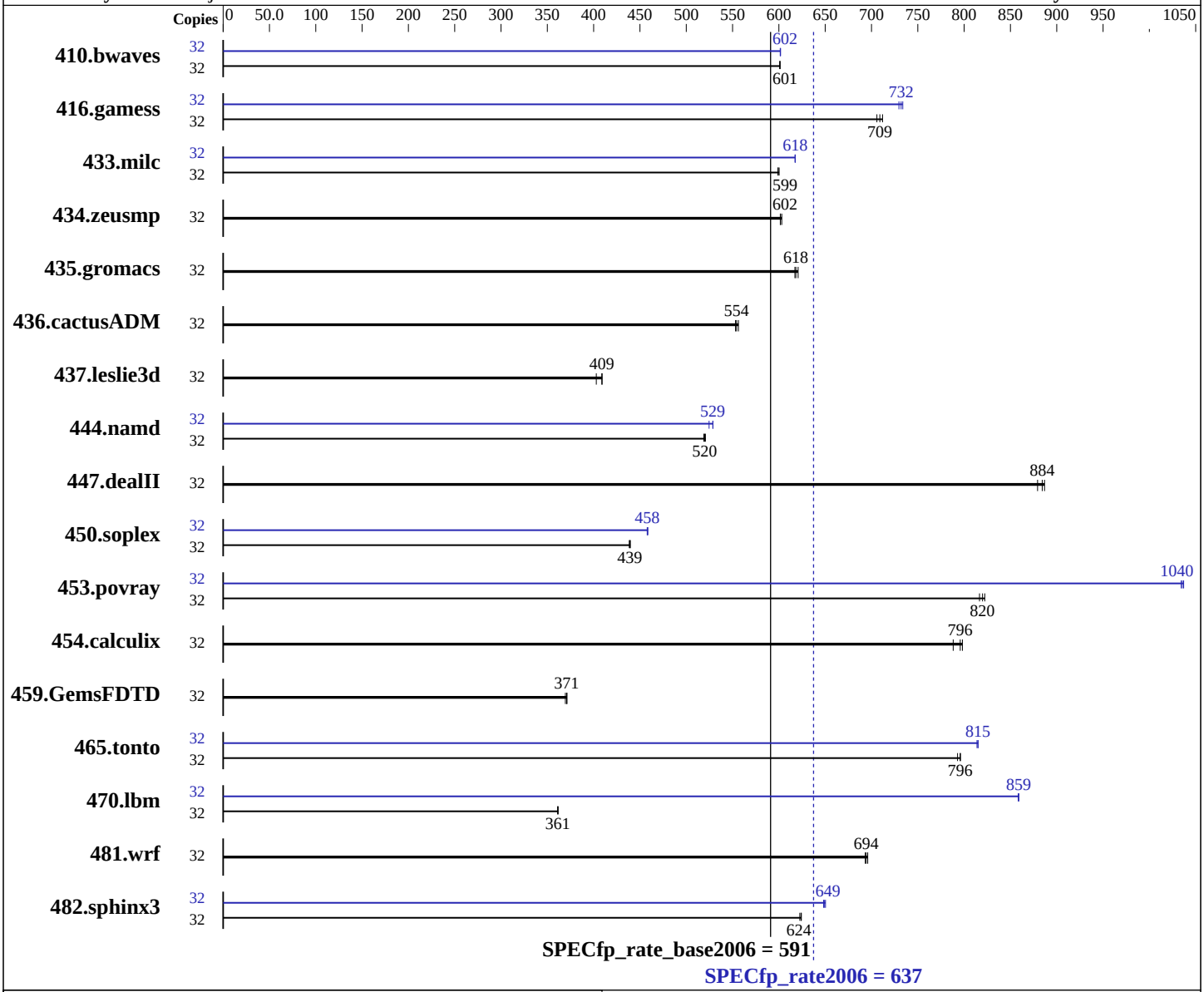
PRIMERGY RX600 S6, Intel Xeon E7-8837, 2.67 GHz

SPECfp®_rate2006 = 637

SPECfp_rate_base2006 = 591

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Jul-2011
**Hardware Availability:** Jul-2011
**Software Availability:** Jan-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E7-8837 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.80 GHz |
| CPU MHz: | 2667 |
| FPU: | Integrated |
| CPU(s) enabled: | 32 cores, 4 chips, 8 cores/chip |
| CPU(s) orderable: | 2,3,4 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

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L3 Cache: 24 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (64 x 8 GB 4Rx8 PC3L-8500R-7, ECC)
Disk Subsystem: 1 x SAS, 600 GB, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 32 | 723 | 601 | **723** | **601** | 724 | 601 | 32 | 723 | 601 | **723** | **602** | 723 | 602 |
| 416.gamess | 32 | 888 | 706 | 880 | 712 | **884** | **709** | 32 | **856** | **732** | 859 | 730 | 854 | 734 |
| 433.milc | 32 | **490** | **599** | 490 | 599 | 489 | 600 | 32 | 476 | 618 | 476 | 617 | **476** | **618** |
| 434.zeusmp | 32 | 484 | 602 | **484** | **602** | 483 | 603 | 32 | 484 | 602 | **484** | **602** | 483 | 603 |
| 435.gromacs | 32 | **370** | **618** | 368 | 621 | 370 | 617 | 32 | **370** | **618** | 368 | 621 | 370 | 617 |
| 436.cactusADM | 32 | 687 | 556 | 691 | 553 | **690** | **554** | 32 | 687 | 556 | 691 | 553 | **690** | **554** |
| 437.leslie3d | 32 | 735 | 409 | **736** | **409** | 747 | 403 | 32 | 735 | 409 | **736** | **409** | 747 | 403 |
| 444.namd | 32 | 494 | 519 | 493 | 521 | **494** | **520** | 32 | 485 | 529 | 489 | 525 | **485** | **529** |
| 447.dealII | 32 | 413 | 887 | **414** | **884** | 416 | 879 | 32 | 413 | 887 | **414** | **884** | 416 | 879 |
| 450.soplex | 32 | 609 | 438 | 607 | 440 | **608** | **439** | 32 | **583** | **458** | 583 | 458 | 582 | 459 |
| 453.povray | 32 | **208** | **820** | 207 | 822 | 208 | 817 | 32 | 165 | 1030 | 164 | 1040 | **164** | **1040** |
| 454.calculix | 32 | 331 | 798 | **332** | **796** | 335 | 788 | 32 | 331 | 798 | **332** | **796** | 335 | 788 |
| 459.GemsFDTD | 32 | **916** | **371** | 919 | 369 | 914 | 371 | 32 | **916** | **371** | 919 | 369 | 914 | 371 |
| 465.tonto | 32 | 396 | 796 | **396** | **796** | 397 | 793 | 32 | **386** | **815** | 387 | 814 | 386 | 815 |
| 470.lbm | 32 | 1218 | 361 | 1216 | 362 | **1217** | **361** | 32 | 512 | 858 | **512** | **859** | 512 | 859 |
| 481.wrf | 32 | **515** | **694** | 516 | 693 | 514 | 696 | 32 | **515** | **694** | 516 | 693 | 514 | 696 |
| 482.sphinx3 | 32 | **1000** | **624** | 1002 | 623 | 999 | 624 | 32 | 962 | 648 | **961** | **649** | 959 | 650 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 28800 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional
```

## General Notes

```
Binaries were compiled on RHEL5.5
For information about Fujitsu please visit: http://www.fujitsu.com
```

## Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml`
`http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml`


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