Acer Incorporated

Acer AW2000ht-AW170ht F1 (Intel Xeon L5630, 2.13GHz)

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated
Test date: Aug-2011
Hardware Availability: Aug-2010
Software Availability: Jan-2011

SPECint®_rate2006 = 195
SPECint_rate_base2006 = 184

CPU Name: Intel Xeon L5630
CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz: 2133
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 1000 GB SATA 7200RPM
Other Hardware: None

Hardware

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ Compiler XE for applications running on IA-32
Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ReiserFS
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

Software
## SPEC CINT2006 Result

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### Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>400.perlbench</td>
<td>16</td>
<td>1089</td>
<td>143</td>
<td>1087</td>
<td>144</td>
<td>1095</td>
<td>143</td>
<td>16</td>
<td>885</td>
<td>177</td>
<td>887</td>
<td>176</td>
<td>889</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>16</td>
<td>1409</td>
<td>110</td>
<td>1412</td>
<td>109</td>
<td>1412</td>
<td>109</td>
<td>16</td>
<td>1310</td>
<td>118</td>
<td>1306</td>
<td>118</td>
<td>1313</td>
</tr>
<tr>
<td>403.gcc</td>
<td>16</td>
<td>912</td>
<td>141</td>
<td>912</td>
<td>141</td>
<td>915</td>
<td>141</td>
<td>16</td>
<td>907</td>
<td>142</td>
<td>891</td>
<td>145</td>
<td>902</td>
</tr>
<tr>
<td>429.mcf</td>
<td>16</td>
<td>730</td>
<td>200</td>
<td>729</td>
<td>200</td>
<td>730</td>
<td>200</td>
<td>8</td>
<td>320</td>
<td>228</td>
<td>322</td>
<td>227</td>
<td>314</td>
</tr>
<tr>
<td>445.gobmk</td>
<td>16</td>
<td>1029</td>
<td>163</td>
<td>1031</td>
<td>163</td>
<td>1028</td>
<td>163</td>
<td>16</td>
<td>977</td>
<td>172</td>
<td>977</td>
<td>172</td>
<td>976</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>16</td>
<td>675</td>
<td>221</td>
<td>670</td>
<td>223</td>
<td>658</td>
<td>227</td>
<td>8</td>
<td>299</td>
<td>250</td>
<td>299</td>
<td>250</td>
<td>299</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>16</td>
<td>1223</td>
<td>158</td>
<td>1223</td>
<td>158</td>
<td>1221</td>
<td>159</td>
<td>16</td>
<td>1154</td>
<td>168</td>
<td>1158</td>
<td>167</td>
<td>1155</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>16</td>
<td>371</td>
<td>894</td>
<td>367</td>
<td>903</td>
<td>371</td>
<td>893</td>
<td>16</td>
<td>371</td>
<td>894</td>
<td>367</td>
<td>903</td>
<td>371</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>16</td>
<td>1628</td>
<td>218</td>
<td>1573</td>
<td>225</td>
<td>1627</td>
<td>218</td>
<td>16</td>
<td>1612</td>
<td>220</td>
<td>1611</td>
<td>220</td>
<td>1611</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>16</td>
<td>711</td>
<td>141</td>
<td>711</td>
<td>141</td>
<td>707</td>
<td>141</td>
<td>16</td>
<td>658</td>
<td>152</td>
<td>658</td>
<td>152</td>
<td>658</td>
</tr>
<tr>
<td>473.astar</td>
<td>16</td>
<td>979</td>
<td>115</td>
<td>983</td>
<td>114</td>
<td>984</td>
<td>114</td>
<td>16</td>
<td>979</td>
<td>115</td>
<td>983</td>
<td>114</td>
<td>984</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>16</td>
<td>592</td>
<td>186</td>
<td>593</td>
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<td>592</td>
<td>186</td>
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</tr>
</tbody>
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

### Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

### Platform Notes

BIOS Settings:
Fan speed = full speed (Default = Balanced)
Data Reuse = Disabled (Default = Enabled)

### General Notes

Binaries compiled on RHEL5.5
The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

### Base Compiler Invocation

C benchmarks:

```bash
icc -m32
```

Continued on next page
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### Base Compiler Invocation (Continued)

C++ benchmarks:
- **icpc -m32**

### Base Portability Flags

- 400.perlbench: `-DSPEC_CPU_LINUX_IA32`
- 462.libquantum: `-DSPEC_CPU_LINUX`
- 483.xalancbmk: `-DSPEC_CPU_LINUX`

### Base Optimization Flags

**C benchmarks:**
- `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch`
- `-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

**C++ benchmarks:**
- `-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
- `-L/smarrtheap -lsmarrtheap`
- `-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

### Base Other Flags

**C benchmarks:**
- 403.gcc: `-Dalloca=_alloca`

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icc -m32

**400.perlbench:** icc -m64

**401.bzip2:** icc -m64

**456.hmmer:** icc -m64

**458.sjeng:** icc -m64

**C++ benchmarks:**
- icpc -m32
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Peak Portability Flags

-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

Peak Optimization Flags

C benchmarks:

-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

C++ benchmarks:
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**Peak Optimization Flags (Continued)**

```
471.omnetpp (continued):
   -L/smartheap -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes
```

---

**Peak Other Flags**

```
C benchmarks:

403.gcc: -Dalloca=_alloca
```

---

The flags files that were used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html  
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml  
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml

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For questions about this result, please contact the tester.  
For other inquiries, please contact webmaster@spec.org.

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