



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint[®]_rate2006 = 131

PRIMERGY TX100 S3, Intel Xeon E3-1220, 3.10 GHz

SPECint_rate_base2006 = 126

CPU2006 license: 19

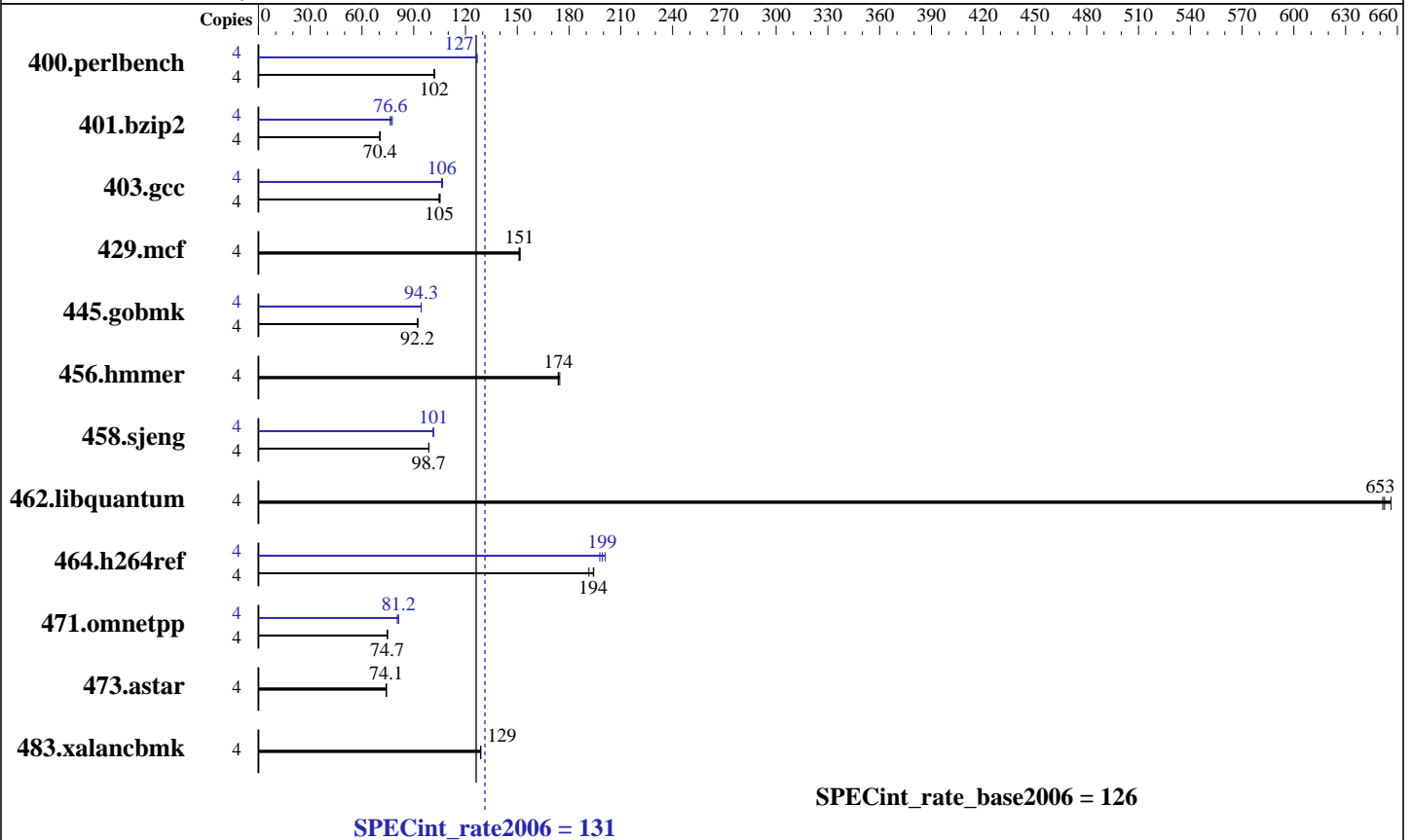
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2011

Hardware Availability: Sep-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E3-1220
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 131

PRIMERGY TX100 S3, Intel Xeon E3-1220, 3.10 GHz

SPECint_rate_base2006 = 126

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jun-2011
Hardware Availability: Sep-2011
Software Availability: Jan-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	383	102	384	102	<u>384</u>	<u>102</u>	4	309	127	308	127	<u>309</u>	<u>127</u>
401.bzip2	4	547	70.5	<u>548</u>	<u>70.4</u>	549	70.3	4	498	77.5	505	76.5	<u>504</u>	<u>76.6</u>
403.gcc	4	<u>307</u>	<u>105</u>	307	105	306	105	4	302	107	303	106	<u>303</u>	<u>106</u>
429.mcf	4	242	151	<u>241</u>	<u>151</u>	241	152	4	242	151	<u>241</u>	<u>151</u>	241	152
445.gobmk	4	455	92.2	454	92.3	<u>455</u>	<u>92.2</u>	4	445	94.3	<u>445</u>	<u>94.3</u>	445	94.3
456.hammer	4	<u>215</u>	<u>174</u>	215	174	214	175	4	<u>215</u>	<u>174</u>	215	174	214	175
458.sjeng	4	490	98.8	<u>490</u>	<u>98.7</u>	490	98.7	4	<u>477</u>	<u>101</u>	477	101	478	101
462.libquantum	4	<u>127</u>	<u>653</u>	126	656	127	652	4	<u>127</u>	<u>653</u>	126	656	127	652
464.h264ref	4	462	192	<u>456</u>	<u>194</u>	456	194	4	447	198	441	201	<u>444</u>	<u>199</u>
471.omnetpp	4	334	74.8	335	74.7	<u>334</u>	<u>74.7</u>	4	308	81.2	<u>308</u>	<u>81.2</u>	311	80.4
473.astar	4	379	74.2	<u>379</u>	<u>74.1</u>	379	74.0	4	379	74.2	<u>379</u>	<u>74.1</u>	379	74.0
483.xalancbmk	4	214	129	<u>214</u>	<u>129</u>	214	129	4	214	129	<u>214</u>	<u>129</u>	214	129

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run
echo 1800 > /proc/sys/vm/nr_hugepages

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 131

PRIMERGY TX100 S3, Intel Xeon E3-1220, 3.10 GHz

SPECint_rate_base2006 = 126

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jun-2011
Hardware Availability: Sep-2011
Software Availability: Jan-2011

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 131

PRIMERGY TX100 S3, Intel Xeon E3-1220, 3.10 GHz

SPECint_rate_base2006 = 126

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2011

Hardware Availability: Sep-2011

Software Availability: Jan-2011

Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: basepeak = yes

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 131

PRIMERGY TX100 S3, Intel Xeon E3-1220, 3.10 GHz

SPECint_rate_base2006 = 126

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2011

Hardware Availability: Sep-2011

Software Availability: Jan-2011

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 22:40:29 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 13 September 2011.

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>