



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 157

PRIMERGY TX100 S3, Intel Xeon E3-1230, 3.20 GHz

SPECint_rate_base2006 = 151

CPU2006 license: 19

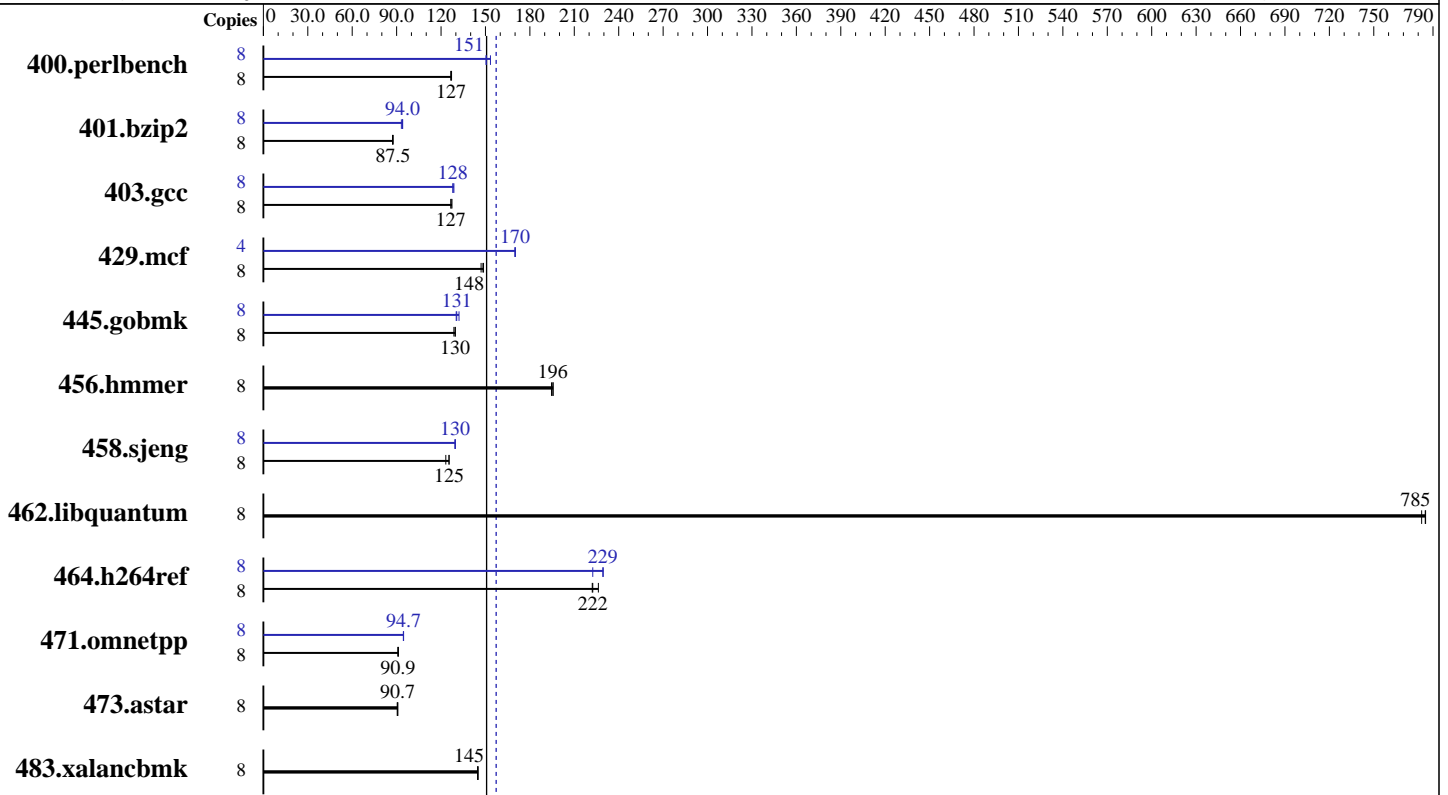
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2011

Hardware Availability: Sep-2011

Software Availability: Jan-2011



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Hardware

CPU Name: Intel Xeon E3-1230
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|------------|------------|-------------|------------|-------------|--------|------------|------------|------------|-------------|------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 617 | 127 | 617 | 127 | 615 | 127 | 8 | 510 | 153 | 520 | 150 | 519 | 151 |
| 401.bzip2 | 8 | 881 | 87.6 | 882 | 87.5 | 885 | 87.2 | 8 | 821 | 94.1 | 822 | 94.0 | 828 | 93.2 |
| 403.gcc | 8 | 506 | 127 | 508 | 127 | 508 | 127 | 8 | 503 | 128 | 503 | 128 | 500 | 129 |
| 429.mcf | 8 | 492 | 148 | 491 | 149 | 496 | 147 | 4 | 215 | 170 | 214 | 170 | 215 | 170 |
| 445.gobmk | 8 | 648 | 130 | 647 | 130 | 652 | 129 | 8 | 644 | 130 | 643 | 131 | 635 | 132 |
| 456.hammer | 8 | 381 | 196 | 384 | 195 | 382 | 196 | 8 | 381 | 196 | 384 | 195 | 382 | 196 |
| 458.sjeng | 8 | 786 | 123 | 772 | 125 | 772 | 125 | 8 | 748 | 129 | 747 | 130 | 746 | 130 |
| 462.libquantum | 8 | 211 | 785 | 212 | 782 | 211 | 785 | 8 | 211 | 785 | 212 | 782 | 211 | 785 |
| 464.h264ref | 8 | 782 | 226 | 797 | 222 | 796 | 222 | 8 | 771 | 230 | 796 | 222 | 772 | 229 |
| 471.omnetpp | 8 | 548 | 91.2 | 550 | 90.9 | 550 | 90.9 | 8 | 529 | 94.5 | 528 | 94.7 | 528 | 94.7 |
| 473.astar | 8 | 619 | 90.7 | 619 | 90.7 | 621 | 90.4 | 8 | 619 | 90.7 | 619 | 90.7 | 621 | 90.4 |
| 483.xalancbmk | 8 | 382 | 145 | 381 | 145 | 381 | 145 | 8 | 382 | 145 | 381 | 145 | 381 | 145 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

Continued on next page



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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmmer: basepeak = yes

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4
-auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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