SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

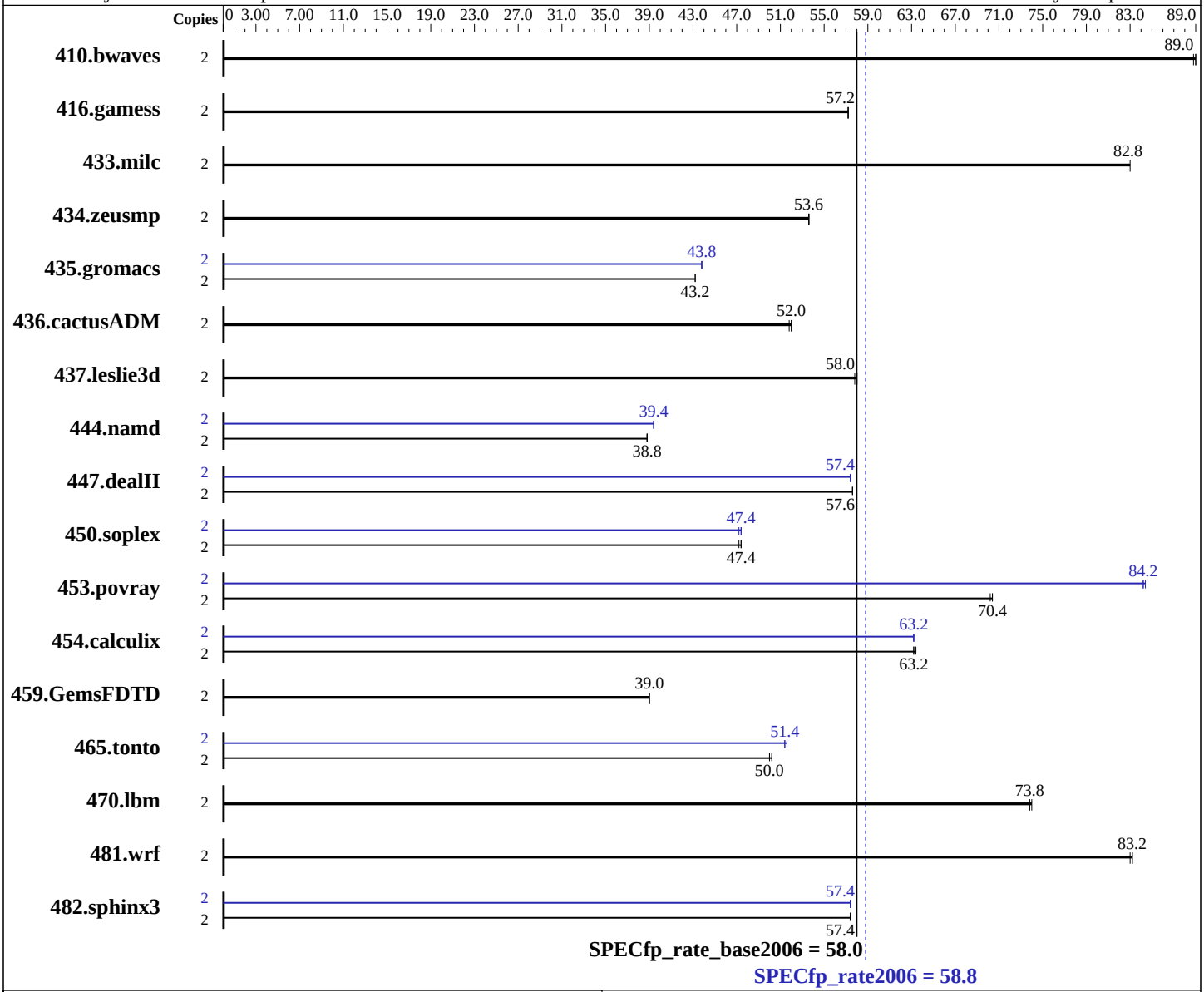
Intel DH67BLB3 Motherboard (Intel Pentium G850)

SPECfp®_rate2006 = 58.8

SPECfp_rate_base2006 = 58.0

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2011
Hardware Availability: May-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Pentium G850
CPU Characteristics:	
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: 3 MB I+D on chip per chip
Other Cache: None
Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	306	88.8	**306**	**89.0**	306	89.0	2	306	88.8	**306**	**89.0**	306	89.0
416.gamess	2	685	57.2	685	57.2	**685**	**57.2**	2	685	57.2	685	57.2	**685**	**57.2**
433.milc	2	222	82.8	**222**	**82.8**	221	83.0	2	222	82.8	**222**	**82.8**	221	83.0
434.zeusmp	2	339	53.6	340	53.6	**339**	**53.6**	2	339	53.6	340	53.6	**339**	**53.6**
435.gromacs	2	332	43.0	**331**	**43.2**	331	43.2	2	326	43.8	**326**	**43.8**	326	43.8
436.cactusADM	2	461	51.8	**460**	**52.0**	460	52.0	2	461	51.8	**460**	**52.0**	460	52.0
437.leslie3d	2	**324**	**58.0**	324	58.0	325	57.8	2	**324**	**58.0**	324	58.0	325	57.8
444.namd	2	413	38.8	413	38.8	**413**	**38.8**	2	406	39.4	406	39.4	**406**	**39.4**
447.dealII	2	398	57.6	397	57.6	**397**	**57.6**	2	399	57.4	399	57.4	**399**	**57.4**
450.soplex	2	353	47.2	**352**	**47.4**	352	47.4	2	353	47.2	352	47.4	**352**	**47.4**
453.povray	2	152	70.2	151	70.4	**151**	**70.4**	2	**126**	**84.2**	126	84.4	126	84.2
454.calculix	2	261	63.2	**261**	**63.2**	261	63.4	2	261	63.2	**261**	**63.2**	261	63.2
459.GemsFDTD	2	544	39.0	**543**	**39.0**	543	39.0	2	544	39.0	**543**	**39.0**	543	39.0
465.tonto	2	**393**	**50.0**	394	50.0	393	50.2	2	383	51.4	382	51.6	**382**	**51.4**
470.lbm	2	371	74.0	**372**	**73.8**	372	73.8	2	371	74.0	**372**	**73.8**	372	73.8
481.wrf	2	269	83.0	268	83.2	**268**	**83.2**	2	269	83.0	268	83.2	**268**	**83.2**
482.sphinx3	2	**679**	**57.4**	678	57.4	680	57.4	2	**679**	**57.4**	679	57.4	680	57.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 The start command with the /affinity switch was used to bind processes to cores
```

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE`

C++ benchmarks:

444.namd: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

447.dealII: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

450.soplex: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

453.povray: `-QxSSE4.2`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

Continued on next page

Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml

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For other inquiries, please contact webmaster@spec.org.

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