SPEC® CFP2006 Result

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ASUSTeK Computer Inc.

(Test Sponsor: Intel Corporation)

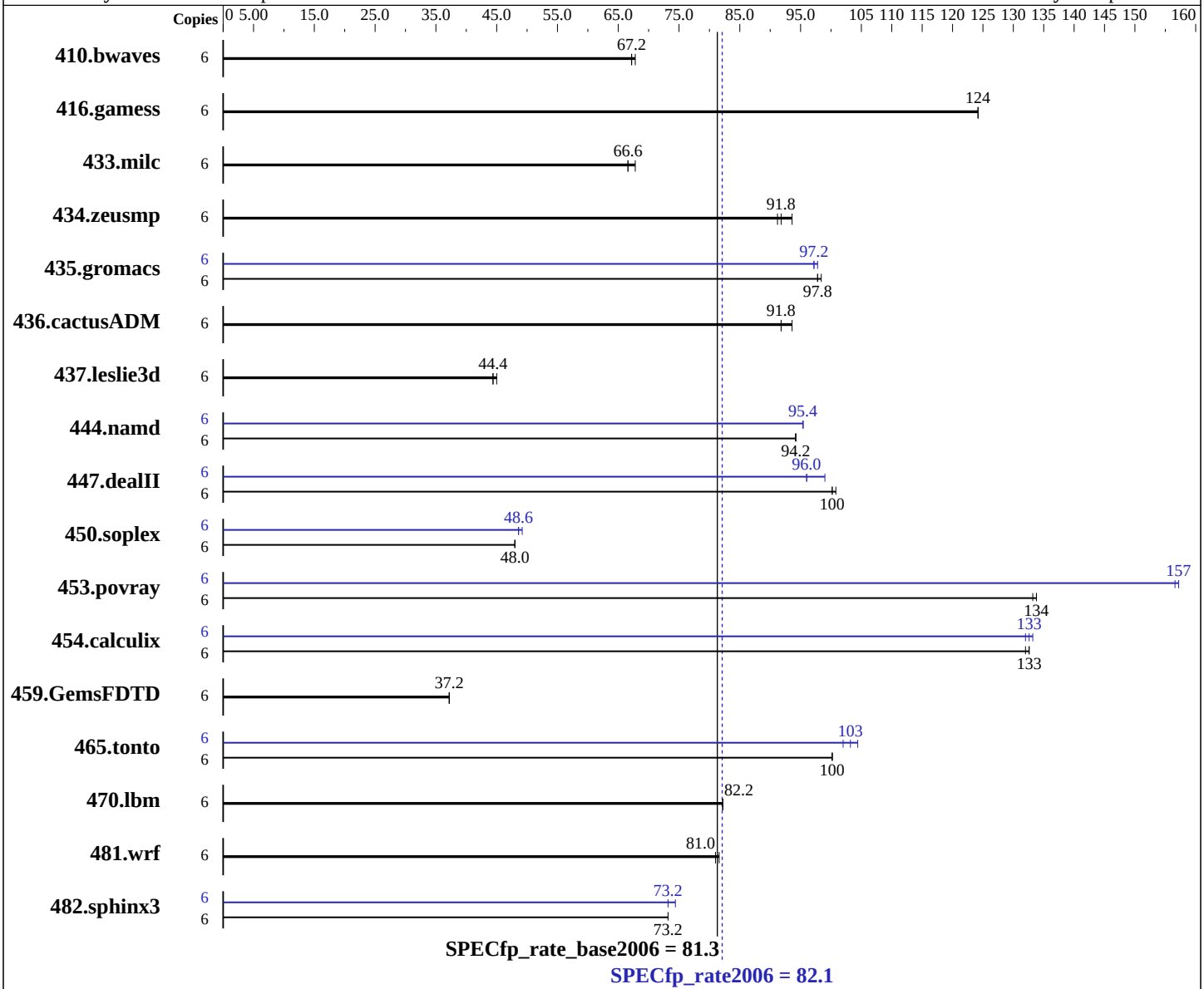
ASUSTek M4A89GTD PRO/USB3 (Phenom II X6 1075T)

SPECfp®_rate2006 = 82.1

SPECfp_rate_base2006 = 81.3

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2011
Hardware Availability: Feb-2011
Software Availability: Apr-2011



Hardware

CPU Name:	AMD Phenom II X6 1075T
CPU Characteristics:	AMD Turbo CORE technology up to 3.5 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx4 PC3-10600U-9)
Disk Subsystem: 1 TB Seagate SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	6	1206	67.8	**1214**	**67.2**	1218	67.2	6	1206	67.8	**1214**	**67.2**	1218	67.2
416.gamess	6	945	124	**945**	**124**	946	124	6	945	124	**945**	**124**	946	124
433.milc	6	811	67.8	**825**	**66.6**	827	66.6	6	811	67.8	**825**	**66.6**	827	66.6
434.zeusmp	6	582	93.6	598	91.2	**596**	**91.8**	6	582	93.6	598	91.2	**596**	**91.8**
435.gromacs	6	435	98.4	**439**	**97.8**	439	97.8	6	440	97.2	**439**	**97.2**	437	97.8
436.cactusADM	6	768	93.6	**781**	**91.8**	782	91.8	6	768	93.6	**781**	**91.8**	782	91.8
437.leslie3d	6	1251	45.0	**1269**	**44.4**	1271	44.4	6	1251	45.0	**1269**	**44.4**	1271	44.4
444.namd	6	510	94.2	**511**	**94.2**	511	94.2	6	505	95.4	**505**	**95.4**	504	95.4
447.dealII	6	687	100	**683**	**100**	682	101	6	**713**	**96.0**	716	96.0	692	99.0
450.soplex	6	1043	48.0	1046	48.0	**1045**	**48.0**	6	1032	48.6	**1029**	**48.6**	1016	49.2
453.povray	6	239	134	240	133	**239**	**134**	6	203	157	204	157	**203**	**157**
454.calculix	6	374	133	**374**	**133**	375	132	6	376	132	**374**	**133**	372	133
459.GemsFDTD	6	1715	37.2	**1717**	**37.2**	1718	37.2	6	1715	37.2	**1717**	**37.2**	1718	37.2
465.tonto	6	591	100	**588**	**100**	588	100	6	578	102	**572**	**103**	566	104
470.lbm	6	1002	82.2	1003	82.2	**1003**	**82.2**	6	1002	82.2	1003	82.2	**1003**	**82.2**
481.wrf	6	824	81.6	828	81.0	**828**	**81.0**	6	824	81.6	828	81.0	**828**	**81.0**
482.sphinx3	6	1593	73.2	**1594**	**73.2**	1594	73.2	6	**1597**	**73.2**	1597	73.2	1578	74.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 The start command with the /affinity switch was used to bind processes to cores
```

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
/arch:SSE3 -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `/arch:SSE3 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE`

C++ benchmarks:

444.namd: `/arch:SSE3`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

447.dealII: `/arch:SSE3`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

450.soplex: `/arch:SSE3`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

453.povray: `/arch:SSE3`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: /arch:SSE3(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

436.cactusADM: basepeak = yes

454.calculix: /arch:SSE3 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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