



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 39.3

PowerEdge T710 (Intel Xeon E5606, 2.13 GHz)

SPECfp_base2006 = 36.8

CPU2006 license: 55

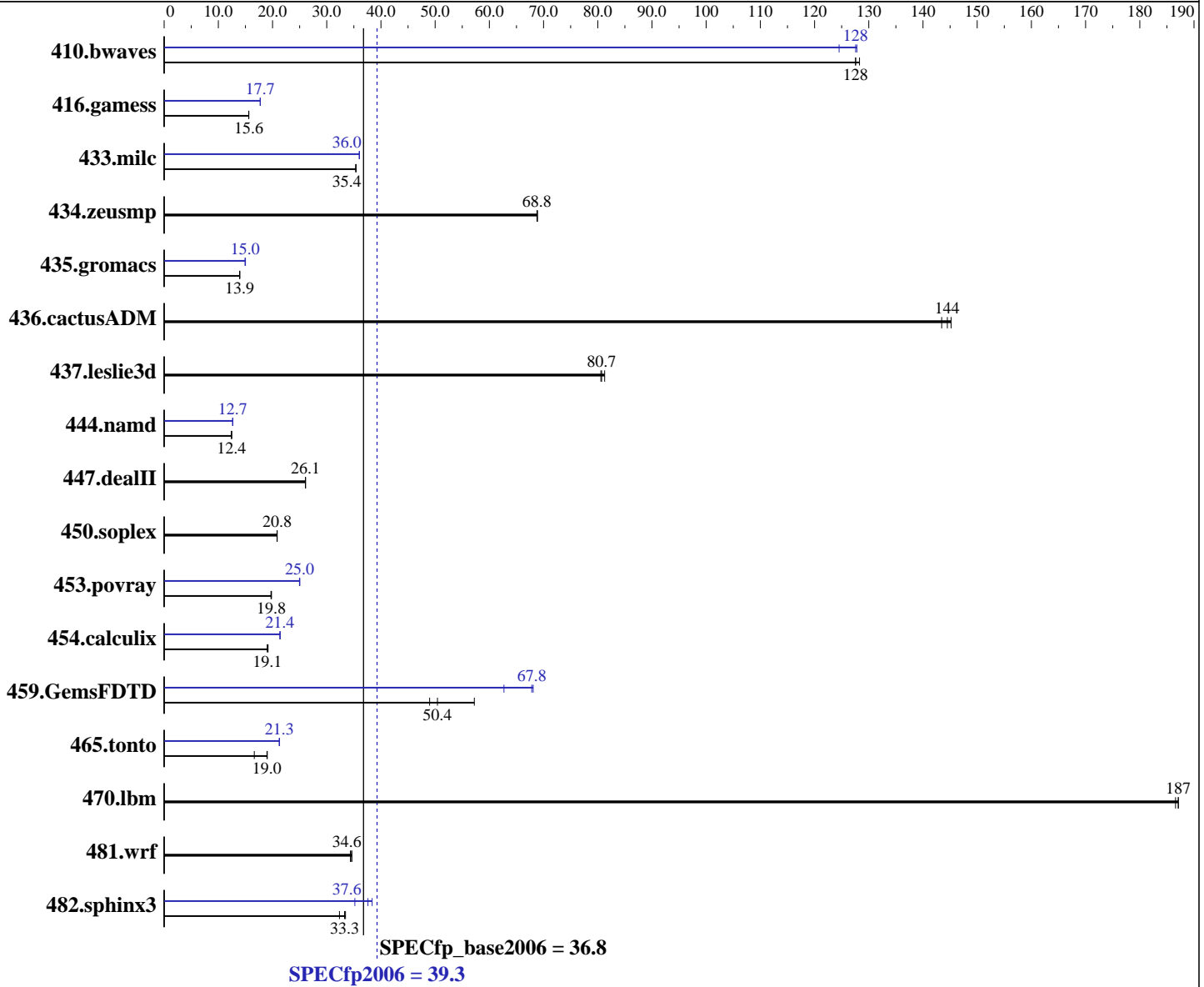
Test date: May-2011

Test sponsor: Dell Inc.

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon E5606
 CPU Characteristics:
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem: 1 x 146 GB 15000 RPM SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|------------|-------------|-------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 107 | 128 | 107 | 128 | 106 | 128 | 109 | 125 | 106 | 128 | 107 | 128 |
| 416.gamess | 1254 | 15.6 | 1254 | 15.6 | 1255 | 15.6 | 1104 | 17.7 | 1105 | 17.7 | 1104 | 17.7 |
| 433.milc | 259 | 35.4 | 260 | 35.3 | 259 | 35.4 | 255 | 36.0 | 255 | 36.0 | 255 | 36.0 |
| 434.zeusmp | 132 | 68.9 | 132 | 68.8 | 132 | 68.8 | 132 | 68.9 | 132 | 68.8 | 132 | 68.8 |
| 435.gromacs | 513 | 13.9 | 511 | 14.0 | 513 | 13.9 | 476 | 15.0 | 478 | 14.9 | 478 | 15.0 |
| 436.cactusADM | 82.3 | 145 | 83.3 | 143 | 82.7 | 144 | 82.3 | 145 | 83.3 | 143 | 82.7 | 144 |
| 437.leslie3d | 116 | 80.7 | 117 | 80.6 | 116 | 81.3 | 116 | 80.7 | 117 | 80.6 | 116 | 81.3 |
| 444.namd | 644 | 12.4 | 645 | 12.4 | 645 | 12.4 | 635 | 12.6 | 633 | 12.7 | 633 | 12.7 |
| 447.dealII | 439 | 26.1 | 439 | 26.1 | 439 | 26.1 | 439 | 26.1 | 439 | 26.1 | 439 | 26.1 |
| 450.soplex | 400 | 20.9 | 401 | 20.8 | 400 | 20.8 | 400 | 20.9 | 401 | 20.8 | 400 | 20.8 |
| 453.povray | 269 | 19.8 | 269 | 19.8 | 270 | 19.7 | 213 | 25.0 | 213 | 25.0 | 213 | 25.0 |
| 454.calculix | 432 | 19.1 | 430 | 19.2 | 433 | 19.0 | 386 | 21.4 | 386 | 21.4 | 386 | 21.4 |
| 459.GemsFDTD | 217 | 49.0 | 210 | 50.4 | 185 | 57.2 | 156 | 67.8 | 156 | 68.1 | 169 | 62.7 |
| 465.tonto | 592 | 16.6 | 517 | 19.0 | 519 | 19.0 | 464 | 21.2 | 463 | 21.3 | 462 | 21.3 |
| 470.lbm | 73.4 | 187 | 73.6 | 187 | 73.4 | 187 | 73.4 | 187 | 73.6 | 187 | 73.4 | 187 |
| 481.wrf | 323 | 34.6 | 323 | 34.6 | 325 | 34.4 | 323 | 34.6 | 323 | 34.6 | 325 | 34.4 |
| 482.sphinx3 | 582 | 33.5 | 586 | 33.3 | 603 | 32.3 | 518 | 37.6 | 554 | 35.2 | 508 | 38.4 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Data Reuse = Disabled (Default = Enabled)



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General Notes

OMP_NUM_THREADS set to number of cores
Binaries were compiled on RHEL5.5
The Dell PowerEdge T710 and
the Bull NovaScale T860 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge T710 model

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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