



SPEC[®] CFP2006 Result

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NEC Corporation

SPECfp[®]_rate2006 = 103

Express5800/E110d-1 (Intel Xeon E3-1260L)

SPECfp_rate_base2006 = 100

CPU2006 license: 9006

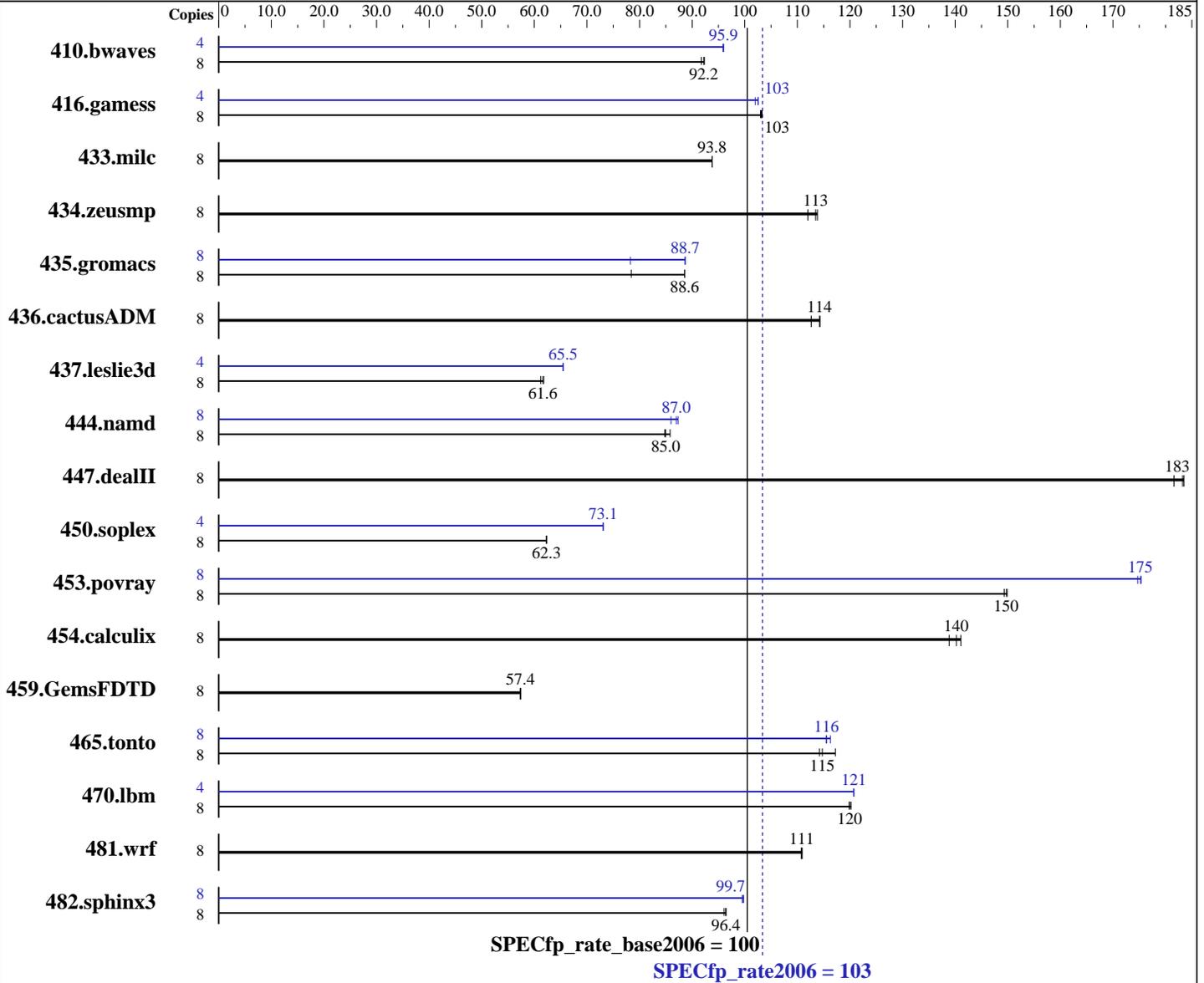
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2011

Hardware Availability: Jun-2011

Software Availability: Mar-2011



Hardware

CPU Name: Intel Xeon E3-1260L
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.3.174 Build 20110309
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 160 GB SATA, 7200 RPM
 Other Hardware: None

Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	8	1185	91.7	1178	92.3	<u>1179</u>	<u>92.2</u>	4	567	95.8	<u>567</u>	<u>95.9</u>	566	96.0		
416.gamess	8	1516	103	1521	103	<u>1519</u>	<u>103</u>	4	768	102	764	103	<u>764</u>	<u>103</u>		
433.milc	8	<u>783</u>	<u>93.8</u>	783	93.8	783	93.8	8	<u>783</u>	<u>93.8</u>	783	93.8	783	93.8		
434.zeusmp	8	640	114	<u>642</u>	<u>113</u>	650	112	8	640	114	<u>642</u>	<u>113</u>	650	112		
435.gromacs	8	728	78.4	<u>645</u>	<u>88.6</u>	645	88.6	8	644	88.7	730	78.2	<u>644</u>	<u>88.7</u>		
436.cactusADM	8	849	113	<u>837</u>	<u>114</u>	836	114	8	849	113	<u>837</u>	<u>114</u>	836	114		
437.leslie3d	8	1217	61.8	1229	61.2	<u>1220</u>	<u>61.6</u>	4	<u>574</u>	<u>65.5</u>	575	65.4	574	65.5		
444.namd	8	748	85.8	<u>755</u>	<u>85.0</u>	757	84.8	8	<u>738</u>	<u>87.0</u>	735	87.3	746	86.0		
447.dealII	8	499	183	<u>499</u>	<u>183</u>	504	182	8	499	183	<u>499</u>	<u>183</u>	504	182		
450.soplex	8	1071	62.3	<u>1071</u>	<u>62.3</u>	1070	62.4	4	457	73.0	456	73.1	<u>456</u>	<u>73.1</u>		
453.povray	8	284	150	285	149	<u>284</u>	<u>150</u>	8	243	175	244	175	<u>243</u>	<u>175</u>		
454.calculix	8	475	139	468	141	<u>471</u>	<u>140</u>	8	475	139	468	141	<u>471</u>	<u>140</u>		
459.GemsFDTD	8	1479	57.4	1480	57.4	<u>1479</u>	<u>57.4</u>	8	1479	57.4	1480	57.4	<u>1479</u>	<u>57.4</u>		
465.tonto	8	<u>686</u>	<u>115</u>	689	114	671	117	8	<u>681</u>	<u>116</u>	682	115	677	116		
470.lbm	8	915	120	<u>916</u>	<u>120</u>	917	120	4	<u>455</u>	<u>121</u>	455	121	455	121		
481.wrf	8	<u>806</u>	<u>111</u>	806	111	807	111	8	<u>806</u>	<u>111</u>	806	111	807	111		
482.sphinx3	8	1617	96.4	<u>1617</u>	<u>96.4</u>	1623	96.1	8	<u>1564</u>	<u>99.7</u>	1566	99.5	1563	99.8		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Huge pages were not configured for this run

Platform Notes

Default BIOS settings were used.



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.lelie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -ansi-alias



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
 -ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.xml>

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