SPEC® CFP2006 Result

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Intel Corporation

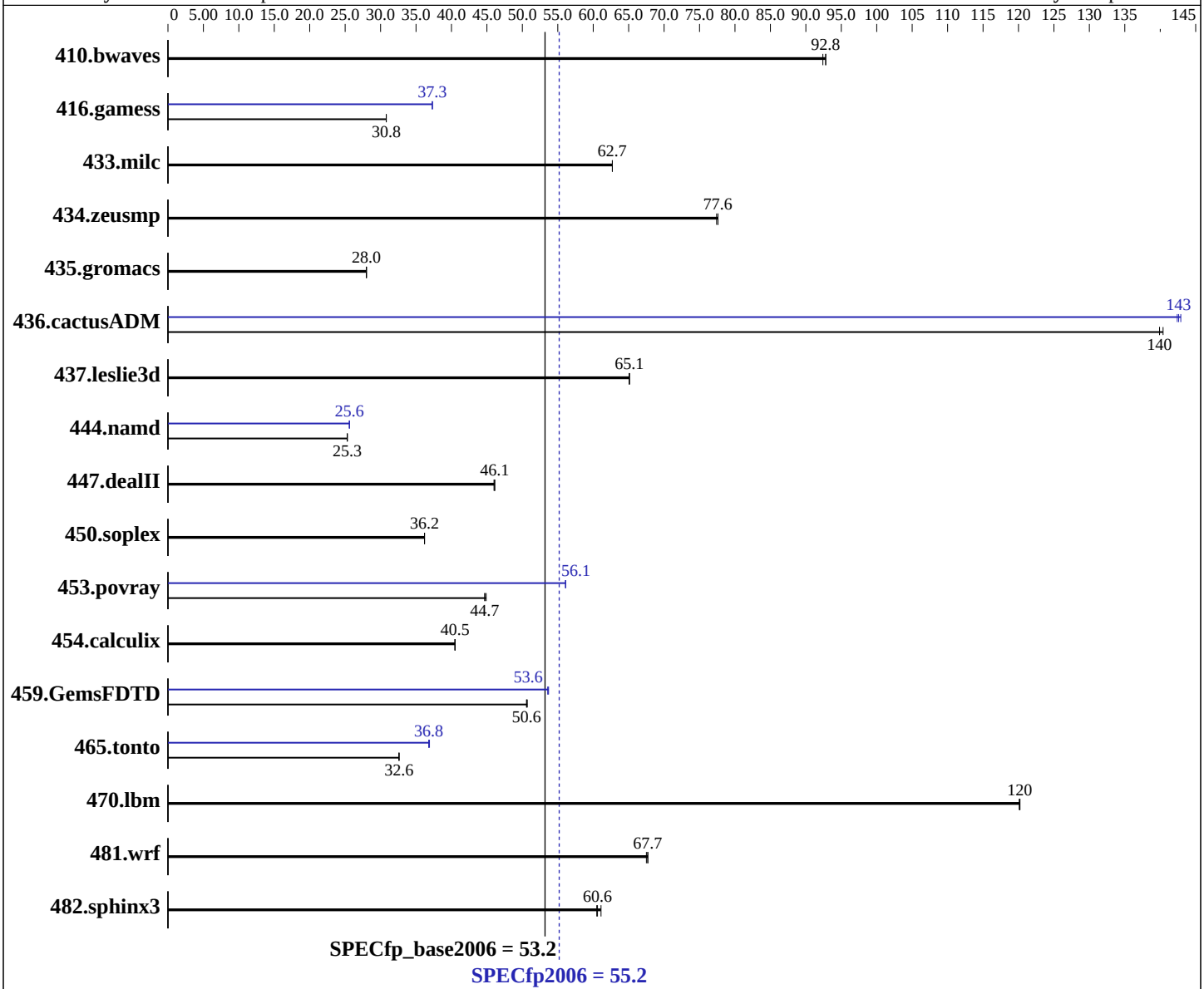
Intel DH67BLB3 Motherboard (Intel Core i5-2500)

SPECfp®2006 = 55.2

SPECfp_base2006 = 53.2

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2011
Hardware Availability: Mar-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Core i5-2500
CPU Characteristics:	Intel Turbo Boost Technology up to 3.7 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Intel Visual Fortran Compiler XE for Intel 64 Version 12.0.3.176 Build 20110309 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	Yes
File System:	NTFS

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L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM
Other Hardware:	None

System State:	Default
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	147	92.4	146	92.8	**147**	**92.8**	147	92.4	146	92.8	**147**	**92.8**
416.gamess	**636**	**30.8**	635	30.8	636	30.8	526	37.3	**526**	**37.3**	526	37.3
433.milc	**146**	**62.7**	146	62.7	146	62.7	**146**	**62.7**	146	62.7	146	62.7
434.zeusmp	118	77.4	117	77.6	**117**	**77.6**	118	77.4	117	77.6	**117**	**77.6**
435.gromacs	255	28.0	**255**	**28.0**	255	28.0	255	28.0	**255**	**28.0**	255	28.0
436.cactusADM	85.1	140	**85.4**	**140**	85.4	140	83.6	143	83.9	142	**83.8**	**143**
437.leslie3d	144	65.1	**144**	**65.1**	144	65.1	144	65.1	**144**	**65.1**	144	65.1
444.namd	317	25.3	**317**	**25.3**	317	25.3	313	25.6	313	25.6	**313**	**25.6**
447.dealII	248	46.1	249	46.0	**248**	**46.1**	248	46.1	249	46.0	**248**	**46.1**
450.soplex	**230**	**36.2**	230	36.2	230	36.2	**230**	**36.2**	230	36.2	230	36.2
453.povray	**119**	**44.7**	118	44.9	119	44.7	94.8	56.1	**94.9**	**56.1**	94.9	56.1
454.calculix	**204**	**40.5**	204	40.5	204	40.5	**204**	**40.5**	204	40.5	204	40.5
459.GemsFDTD	**210**	**50.6**	209	50.7	210	50.6	198	53.6	**198**	**53.6**	198	53.7
465.tonto	302	32.6	302	32.6	**302**	**32.6**	**267**	**36.8**	267	36.8	267	36.9
470.lbm	114	120	114	120	**114**	**120**	114	120	114	120	**114**	**120**
481.wrf	166	67.5	165	67.7	**165**	**67.7**	166	67.5	165	67.7	**165**	**67.7**
482.sphinx3	**322**	**60.6**	319	61.1	322	60.5	**322**	**60.6**	319	61.1	322	60.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

C++ benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qcxx-features -Qauto-ilp32 /F1000000000 shlW64M.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
/F1000000000

Benchmarks using both Fortran and C:
-QxAVX -Qipo -O3 -Qprec-div- -Qparallel -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F1000000000

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

447.dealII: `basepeak = yes`

450.soplex: `basepeak = yes`

453.povray: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000`

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Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qopt-prefetch -Qparallel /F1000000000

465.tonto: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto -Qinline-calloc /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qunroll2 -Qauto-ilp32 /F1000000000

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Thu Jul 24 00:23:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 August 2011.