# SPEC® CINT2006 Result

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## Intel Corporation

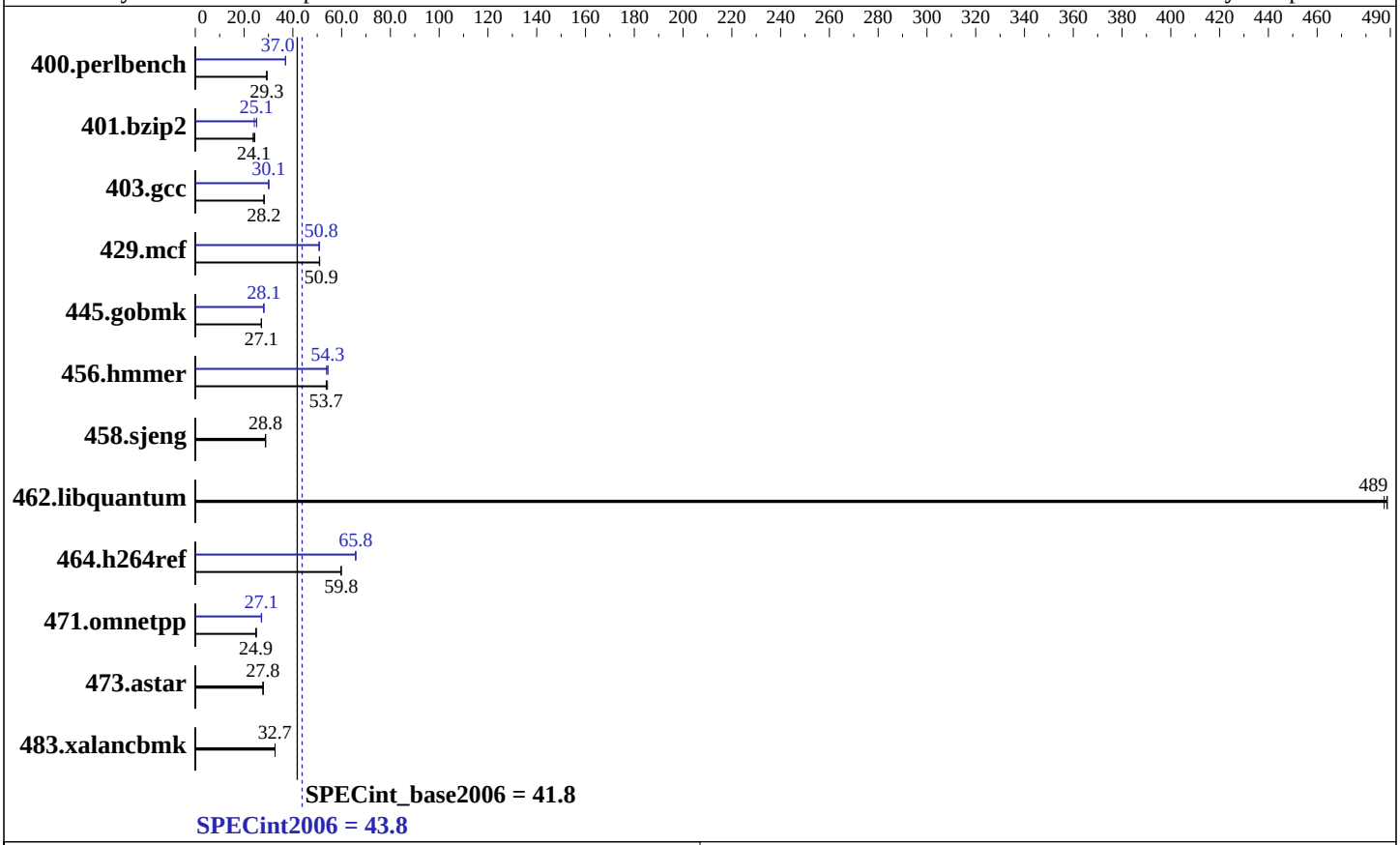
Intel DH67BLB3 Motherboard (Intel Core i5-2500S)

SPECint®2006 = 43.8

SPECint_base2006 = 41.8

**CPU2006 license:** 13
**Test sponsor:** Intel Corporation
**Tested by:** Intel Corporation

**Test date:** Jul-2011
**Hardware Availability:** Mar-2011
**Software Availability:** Apr-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Core i5-2500S |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.7 GHz |
| CPU MHz: | 2700 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 6 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9) |
| Disk Subsystem: | Seagate 1 TB SATA, 7200 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | Windows 7 Ultimate (64-bit) |
| Compiler: | Intel C++ Compiler XE for IA32 and Intel 64 Version 12.0.3.176 Build 20110309<br>Microsoft Visual Studio 2008 Professional SP1 (for libraries) |
| Auto Parallel: | Yes |
| File System: | NTFS |
| System State: | Default |
| Base Pointers: | 32/64-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | SmartHeap Library Version 9.01 from http://www.microquill.com/ |

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## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 334 | 29.3 | **333** | **29.3** | 333 | 29.4 | 264 | 37.0 | 264 | 37.0 | **264** | **37.0** |
| 401.bzip2 | **401** | **24.1** | 407 | 23.7 | 396 | 24.4 | 382 | 25.2 | 398 | 24.2 | **385** | **25.1** |
| 403.gcc | **285** | **28.2** | 285 | 28.2 | 285 | 28.2 | 268 | 30.1 | **268** | **30.1** | 268 | 30.1 |
| 429.mcf | **179** | **50.9** | 179 | 50.9 | 179 | 51.0 | 180 | 50.8 | **180** | **50.8** | 180 | 50.8 |
| 445.gobmk | 387 | 27.1 | **387** | **27.1** | 387 | 27.1 | 373 | 28.1 | **373** | **28.1** | 374 | 28.1 |
| 456.hmmer | 172 | 54.2 | **174** | **53.7** | 174 | 53.7 | **172** | **54.3** | 172 | 54.4 | 173 | 53.8 |
| 458.sjeng | **420** | **28.8** | 420 | 28.8 | 420 | 28.8 | **420** | **28.8** | 420 | 28.8 | 420 | 28.8 |
| 462.libquantum | 42.4 | 489 | 42.5 | 488 | **42.4** | **489** | 42.4 | 489 | 42.5 | 488 | **42.4** | **489** |
| 464.h264ref | **370** | **59.8** | 370 | 59.8 | 370 | 59.8 | 337 | 65.7 | 336 | 65.8 | **337** | **65.8** |
| 471.omnetpp | 251 | 25.0 | **251** | **24.9** | 251 | 24.9 | 230 | 27.2 | **230** | **27.1** | 231 | 27.1 |
| 473.astar | 252 | 27.8 | **252** | **27.8** | 253 | 27.8 | 252 | 27.8 | **252** | **27.8** | 253 | 27.8 |
| 483.xalancbmk | 211 | 32.7 | 211 | 32.7 | **211** | **32.7** | 211 | 32.7 | 211 | 32.7 | **211** | **32.7** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter
```

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

## Base Portability Flags

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
-DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword

## Base Optimization Flags

C benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32 /F512000000

C++ benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32 /F512000000 shlW64M.lib -link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml`


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For other inquiries, please contact webmaster@spec.org.

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