SPEC® CFP2006 Result

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IBM Corporation

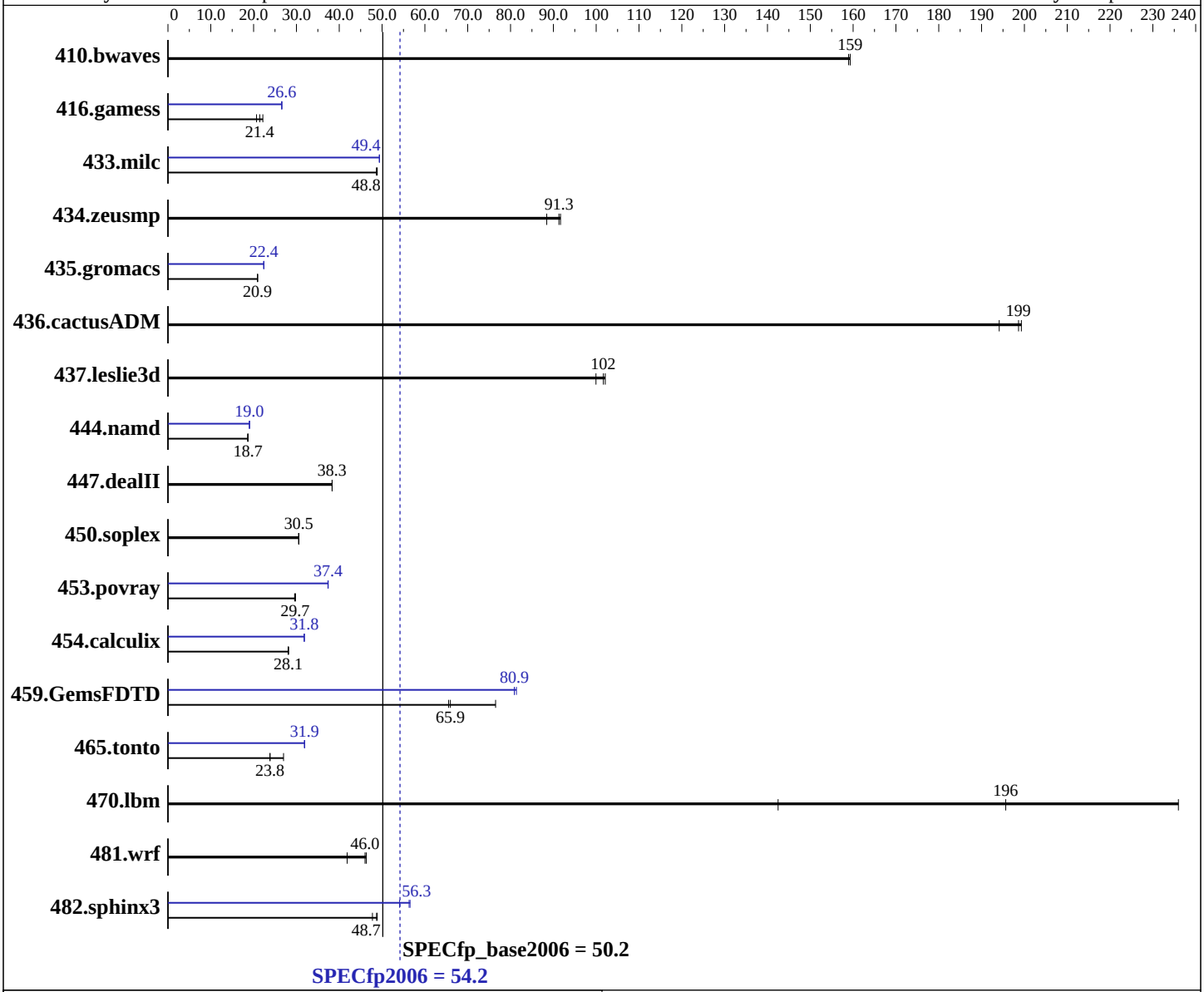
IBM BladeCenter HS22 (Intel Xeon X5647)

SPECfp®2006 = 54.2

SPECfp_base2006 = 50.2

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jun-2011
Hardware Availability: Feb-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Xeon X5647
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem: 1 x 73 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	**85.3**	**159**	85.5	159	85.3	159	**85.3**	**159**	85.5	159	85.3	159
416.gamess	947	20.7	884	22.2	**913**	**21.4**	737	26.6	736	26.6	**737**	**26.6**
433.milc	189	48.7	188	48.9	**188**	**48.8**	186	49.4	**186**	**49.4**	186	49.4
434.zeusmp	**99.7**	**91.3**	103	88.5	99.3	91.7	**99.7**	**91.3**	103	88.5	99.3	91.7
435.gromacs	341	20.9	341	20.9	**341**	**20.9**	**319**	**22.4**	318	22.4	319	22.4
436.cactusADM	**60.2**	**199**	61.6	194	60.0	199	**60.2**	**199**	61.6	194	60.0	199
437.leslie3d	92.0	102	94.0	100	**92.4**	**102**	92.0	102	94.0	100	**92.4**	**102**
444.namd	431	18.6	**429**	**18.7**	429	18.7	**421**	**19.0**	421	19.0	421	19.0
447.dealII	**298**	**38.3**	298	38.3	298	38.3	**298**	**38.3**	298	38.3	298	38.3
450.soplex	273	30.5	273	30.6	**273**	**30.5**	273	30.5	273	30.6	**273**	**30.5**
453.povray	180	29.6	179	29.8	**179**	**29.7**	142	37.4	142	37.4	**142**	**37.4**
454.calculix	**294**	**28.1**	294	28.1	293	28.2	259	31.8	**259**	**31.8**	259	31.8
459.GemsFDTD	139	76.5	**161**	**65.9**	162	65.5	130	81.4	**131**	**80.9**	131	80.9
465.tonto	413	23.8	**413**	**23.8**	365	27.0	**309**	**31.9**	308	31.9	309	31.9
470.lbm	58.2	236	96.4	142	**70.2**	**196**	58.2	236	96.4	142	**70.2**	**196**
481.wrf	267	41.8	241	46.3	**243**	**46.0**	267	41.8	241	46.3	**243**	**46.0**
482.sphinx3	**400**	**48.7**	399	48.9	408	47.8	**346**	**56.3**	360	54.1	345	56.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodev   /mnt/hugepages   hugetlbfs   defaults   0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
Load Default BIOS Settings and then change the following
  Turbo Mode enabled
  Turbo Boost set to Traditional
  Power C-states enabled
  Demand Scrub disabled
```

General Notes

```
Binaries compiled on RHEL5.5
OMP_NUM_THREADS set to number of cores
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml

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