# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Itautec

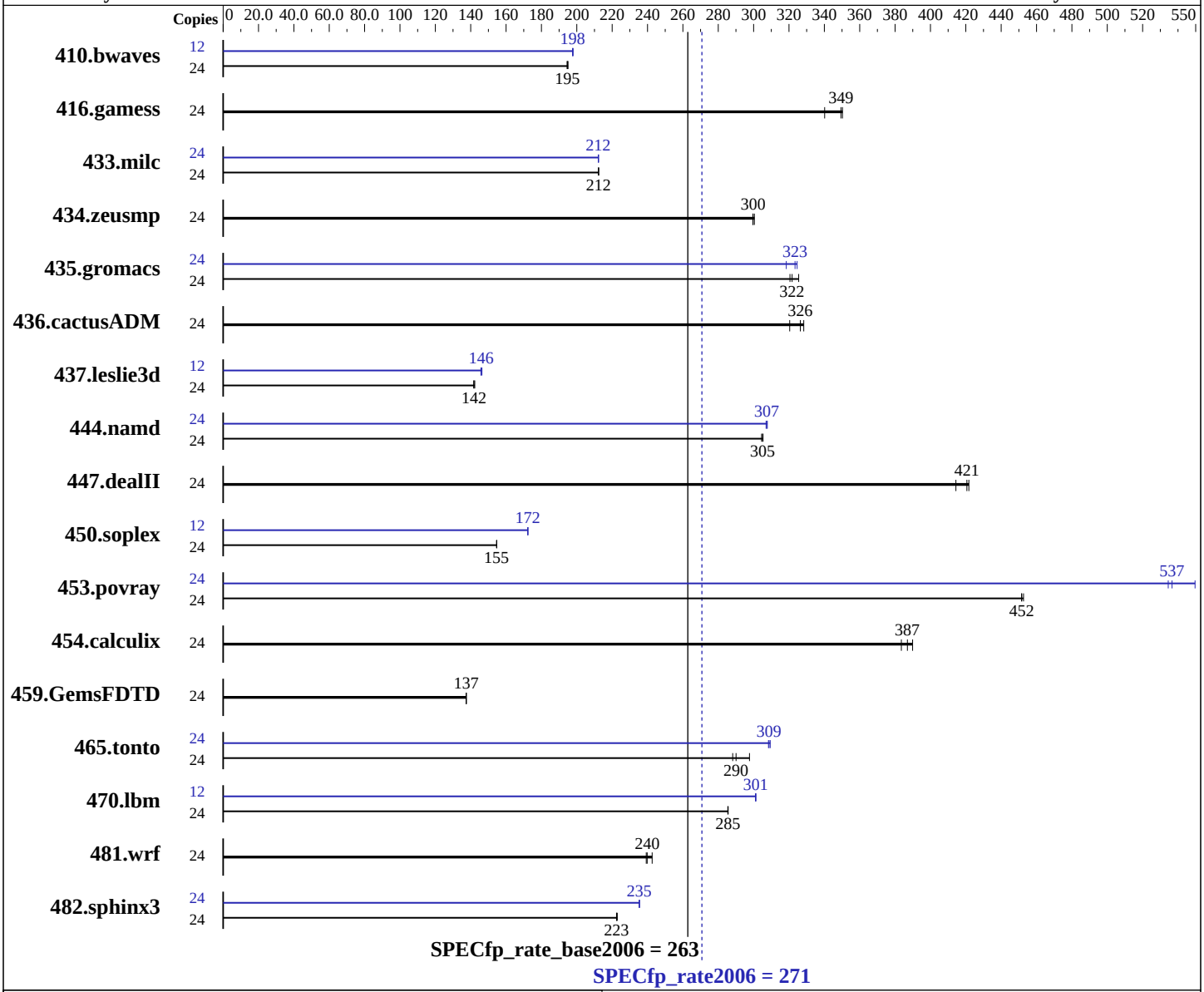
## Servidor Itautec MX214 (Intel Xeon X5680)

**SPECfp®_rate2006 = 271**

**SPECfp_rate_base2006 = 263**

**CPU2006 license:** 9001
**Test sponsor:** Itautec
**Tested by:** Itautec

**Test date:** Jul-2011
**Hardware Availability:** Apr-2011
**Software Availability:** Jan-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5680 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.60 GHz |
| CPU MHz: | 3333 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

Continued on next page

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.2 Build 20110112 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Itautec

## Servidor Itautec MX214 (Intel Xeon X5680)

SPECfp_rate2006 = 271

SPECfp_rate_base2006 = 263

**CPU2006 license:** 9001
**Test sponsor:** Itautec
**Tested by:** Itautec

**Test date:** Jul-2011
**Hardware Availability:** Apr-2011
**Software Availability:** Jan-2011

L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 500 GB SATA-2, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 24 | 1672 | 195 | **1675** | **195** | 1677 | 194 | 12 | 824 | 198 | 825 | 198 | **825** | **198** |
| 416.gamess | 24 | **1345** | **349** | 1342 | 350 | 1381 | 340 | 24 | **1345** | **349** | 1342 | 350 | 1381 | 340 |
| 433.milc | 24 | **1038** | **212** | 1037 | 212 | 1038 | 212 | 24 | 1038 | 212 | **1038** | **212** | 1038 | 212 |
| 434.zeusmp | 24 | 729 | 300 | 727 | 300 | **729** | **300** | 24 | 729 | 300 | 727 | 300 | **729** | **300** |
| 435.gromacs | 24 | 526 | 325 | 535 | 321 | **533** | **322** | 24 | 528 | 325 | **530** | **323** | 538 | 318 |
| 436.cactusADM | 24 | 895 | 320 | 873 | 328 | **879** | **326** | 24 | 895 | 320 | 873 | 328 | **879** | **326** |
| 437.leslie3d | 24 | 1592 | 142 | 1585 | 142 | **1590** | **142** | 12 | **773** | **146** | 774 | 146 | 771 | 146 |
| 444.namd | 24 | **631** | **305** | 630 | 305 | 632 | 305 | 24 | 627 | 307 | **626** | **307** | 625 | 308 |
| 447.dealII | 24 | 651 | 422 | 663 | 414 | **653** | **421** | 24 | 651 | 422 | 663 | 414 | **653** | **421** |
| 450.soplex | 24 | 1294 | 155 | 1294 | 155 | **1294** | **155** | 12 | 581 | 172 | **581** | **172** | 581 | 172 |
| 453.povray | 24 | 282 | 453 | **283** | **452** | 283 | 452 | 24 | 239 | 534 | **238** | **537** | 232 | 550 |
| 454.calculix | 24 | 508 | 390 | **512** | **387** | 516 | 384 | 24 | 508 | 390 | **512** | **387** | 516 | 384 |
| 459.GemsFDTD | 24 | 1853 | 137 | **1853** | **137** | 1852 | 138 | 24 | 1853 | 137 | **1853** | **137** | 1852 | 138 |
| 465.tonto | 24 | **814** | **290** | 793 | 298 | 820 | 288 | 24 | 766 | 308 | **765** | **309** | 763 | 309 |
| 470.lbm | 24 | 1155 | 286 | 1155 | 285 | **1155** | **285** | 12 | 547 | 301 | **547** | **301** | 548 | 301 |
| 481.wrf | 24 | 1105 | 243 | **1118** | **240** | 1120 | 239 | 24 | 1105 | 243 | **1118** | **240** | 1120 | 239 |
| 482.sphinx3 | 24 | 2099 | 223 | **2102** | **223** | 2103 | 222 | 24 | **1987** | **235** | 1988 | 235 | 1986 | 235 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run
```

## Platform Notes

```
Data Reuse disabled in BIOS.
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32`

470.lbm: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-malloc-options=3`
`-ansi-alias -opt-prefetch -static -auto-ilp32`

Continued on next page

## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -static -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml


SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 23:55:32 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 August 2011.