SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS100-E7(P8B-M) Server System
(Intel Xeon E3-1280, 3.50 GHz)

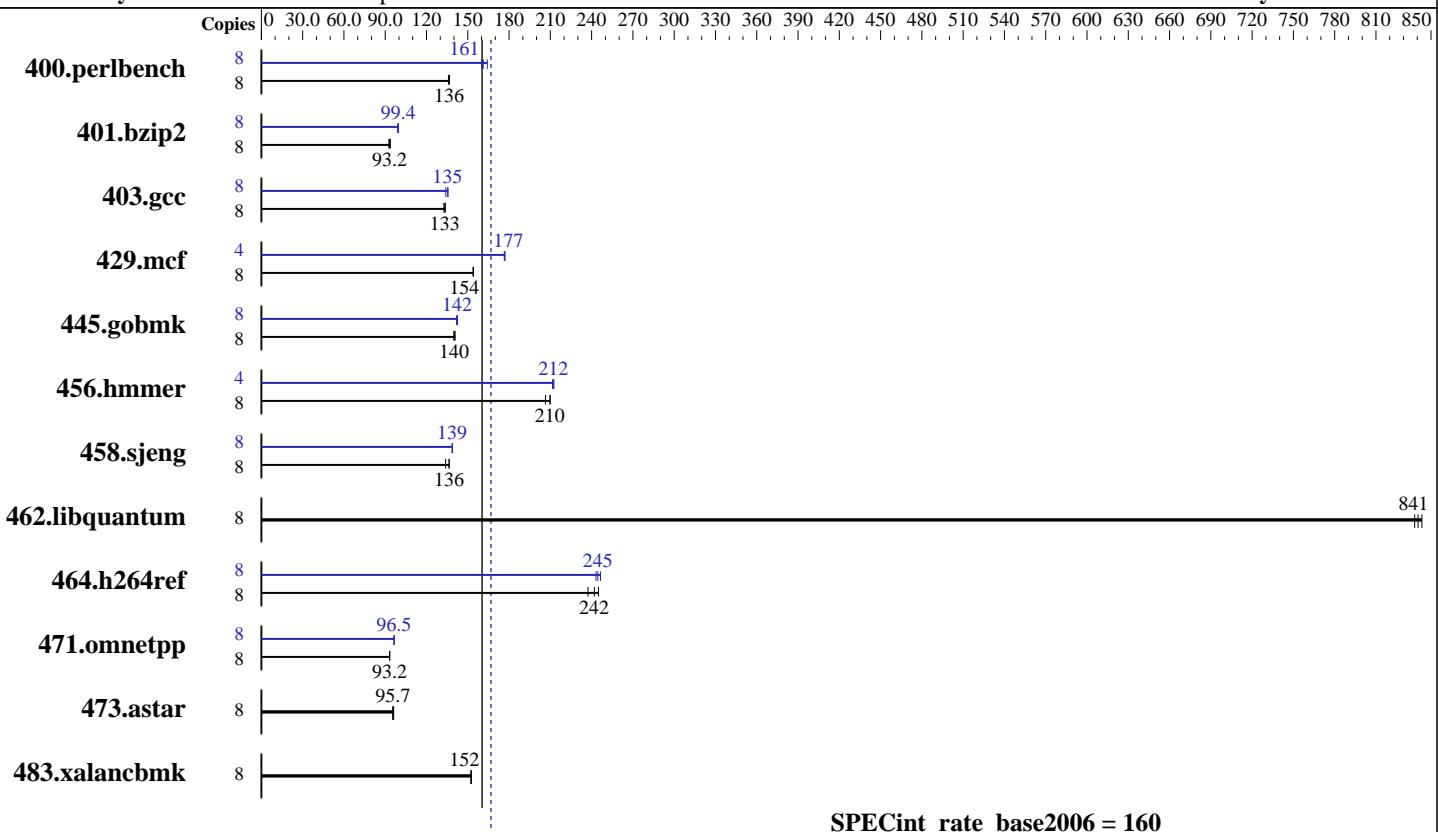$SPECint®\_rate2006 = \quad 167$

$SPECint\_rate\_base2006 = \quad 160$

SPECint_rate_base2006 = 160

SPECint_rate2006 = 167

400.perlbench	8	161
	8	136
401.bzip2	8	99.4
	8	93.2
403.gcc	8	135
	8	133
429.mcf	4	177
	8	154
445.gobmk	8	142
	8	140
456.hmmer	4	212
	8	210
458.sjeng	8	139
	8	136
462.libquantum	8	841
464.h264ref	8	245
	8	242
471.omnetpp	8	96.5
	8	93.2
473.astar	8	95.7
483.xalancbmk	8	152

Hardware

CPU Name:	Intel Xeon E3-1280
CPU Characteristics:	Intel Turbo Boost Technology up to 3.9 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3L-10600E-9, ECC)
Disk Subsystem:	Seagate ST3500320AS 1 x 500 GB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS100-E7(P8B-M) Server System
(Intel Xeon E3-1280, 3.50 GHz)

$SPECint\_rate2006 =$ 167

$SPECint\_rate\_base2006 =$ 160

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	572	137	575	136	**573**	**136**	8	476	164	488	160	**485**	**161**
401.bzip2	8	833	92.7	824	93.7	**828**	**93.2**	8	**776**	**99.4**	779	99.1	776	99.5
403.gcc	8	**483**	**133**	486	133	482	134	8	**476**	**135**	475	136	481	134
429.mcf	8	474	154	473	154	**474**	**154**	4	**206**	**177**	206	177	207	177
445.gobmk	8	600	140	596	141	**599**	**140**	8	592	142	**590**	**142**	588	143
456.hmmer	8	**356**	**210**	362	206	356	210	4	176	212	176	213	**176**	**212**
458.sjeng	8	723	134	709	136	**710**	**136**	8	698	139	697	139	**698**	**139**
462.libquantum	8	197	843	**197**	**841**	198	838	8	197	843	**197**	**841**	198	838
464.h264ref	8	723	245	**732**	**242**	746	237	8	718	246	**724**	**245**	728	243
471.omnetpp	8	536	93.3	**537**	**93.2**	537	93.1	8	518	96.5	518	96.5	**518**	**96.5**
473.astar	8	584	96.1	**587**	**95.7**	588	95.5	8	584	96.1	**587**	**95.7**	588	95.5
483.xalancbmk	8	362	152	362	153	**362**	**152**	8	362	152	362	153	**362**	**152**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     taskset was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
    Hugepages was not enabled

General Notes

    Binaries compiled on RHEL5.5 with
    binutils-2.17.50.0.6-14.el5

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

ASUSTeK Computer Inc.

ASUS RS100-E7(P8B-M) Server System
(Intel Xeon E3-1280, 3.50 GHz)

SPECint_rate2006 =   167

SPECint_rate_base2006 = 160

CPU2006 license: 9016
Test sponsor: ASUSTeK Computer Inc.
Tested by: ASUSTeK Computer Inc.

Test date: Jun-2011
Hardware Availability: Apr-2011
Software Availability: Jan-2011

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:
```
icpc -m32
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
```

Continued on next page

ASUSTeK Computer Inc.

ASUS RS100-E7(P8B-M) Server System
(Intel Xeon E3-1280, 3.50 GHz)

SPECint_rate2006 = 167

SPECint_rate_base2006 = 160

CPU2006 license: 9016		**Test date:**	Jun-2011
Test sponsor: ASUSTeK Computer Inc.		**Hardware Availability:**	Apr-2011
Tested by: ASUSTeK Computer Inc.		**Software Availability:**	Jan-2011

Peak Portability Flags (Continued)

```
  456.hmmer: -DSPEC_CPU_LP64
  458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2)
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -auto-ilp32 -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

      403.gcc: -xAVX -ipo -O3 -no-prec-div
               -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

      429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
               -auto-ilp32

    445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

    456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
               -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

   464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
               -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/smartheap -lsmartheap
```

SPEC CINT2006 Result

ASUSTeK Computer Inc.

ASUS RS100-E7(P8B-M) Server System
(Intel Xeon E3-1280, 3.50 GHz)

SPECint_rate2006 =   167

SPECint_rate_base2006 = 160

CPU2006 license: 9016

Test sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test date: Jun-2011

Hardware Availability: Apr-2011

Software Availability: Jan-2011

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/ASUSTekPlatform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/ASUSTekPlatform.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 21:27:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 July 2011.