Acer Incorporated

Acer AW2000ht-AW170ht F1 (Intel Xeon X5670, 2.93GHz)

**SPECint\_rate2006 = 386**

**SPECint\_rate_base2006 = 362**

**CPU2006 license:** 97

**Test date:** Jun-2011

**Test sponsor:** Acer Incorporated

**Hardware Availability:** Aug-2010

**Tested by:** Acer Incorporated

**Software Availability:** Jan-2011

**400.perlbench**

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**401.bzip2**

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**445.gobmk**

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SPEC CINT2006 Result

Acer Incorporated

Acer AW2000ht-AW170ht F1 (Intel Xeon X5670, 2.93GHz)

SPECint_rate2006 = 386
SPECint_rate_base2006 = 362

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2011
Hardware Availability: Aug-2010
Software Availability: Jan-2011

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

Platform Notes

BIOS Settings:
Fan speed = full speed (Default = Energy Saving)
Data Reuse = Disabled (Default = Enabled)

General Notes

Binaries compiled on RHEL5.5
The Acer AW2000ht-AW170ht F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page
Acer Incorporated
Acer AW2000ht-AW170ht F1 (Intel Xeon X5670, 2.93GHz)

**SPECint_rate2006 = 386**
**SPECint_rate_base2006 = 362**

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### Base Compiler Invocation (Continued)

**C++ benchmarks:**
- icpc -m32

---

### Base Portability Flags

- 400.perlbench: -DSPEC_CPU_LINUX_IA32
- 462.libquantum: -DSPEC_CPU_LINUX
- 483.xalancbmk: -DSPEC_CPU_LINUX

### Base Optimization Flags

**C benchmarks:**
- -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
- -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

**C++ benchmarks:**
- -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
- -L/smartheap -lsmartheap
- -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

### Base Other Flags

**C benchmarks:**
- 403.gcc: -Dalloca=_alloca

### Peak Compiler Invocation

**C benchmarks (except as noted below):**
- icc -m32

**400.perlbench:**
- icc -m64

**401.bzip2:**
- icc -m64

**456.hmmer:**
- icc -m64

**458.sjeng:**
- icc -m64

**C++ benchmarks:**
- icpc -m32
Acer Incorporated
Acer AW2000ht-AW170ht F1 (Intel Xeon X5670, 2.93GHz)

SPECint_rate2006 = 386
SPECint_rate_base2006 = 362

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2011
Hardware Availability: Aug-2010
Software Availability: Jan-2011

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
                  -O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
           -O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
           -opt-prefetch -auto-ilp32 -ansi-alias
           -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403gcc: -xSSE4.2 -ipo -O3 -no-prec-div
       -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
         -O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
         -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2 (pass 2) -prof-gen (pass 1) -prof-use (pass 2)
            -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
           -O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
           -unroll14 -auto-ilp32
           -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
            -O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
            -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2 (pass 2) -prof-gen (pass 1) -ipo (pass 2)
           -O3 (pass 2) -no-prec-div (pass 2) -prof-use (pass 2)
           -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page
Acer Incorporated

Acer AW2000ht-AW170ht F1 (Intel Xeon X5670, 2.93GHz)

SPECint_rate2006 = 386
SPECint_rate_base2006 = 362

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2011
Hardware Availability: Aug-2010
Software Availability: Jan-2011

Peak Optimization Flags (Continued)

471.omnetpp (continued):
   -L/smartheap -lsmartheap

473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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