# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

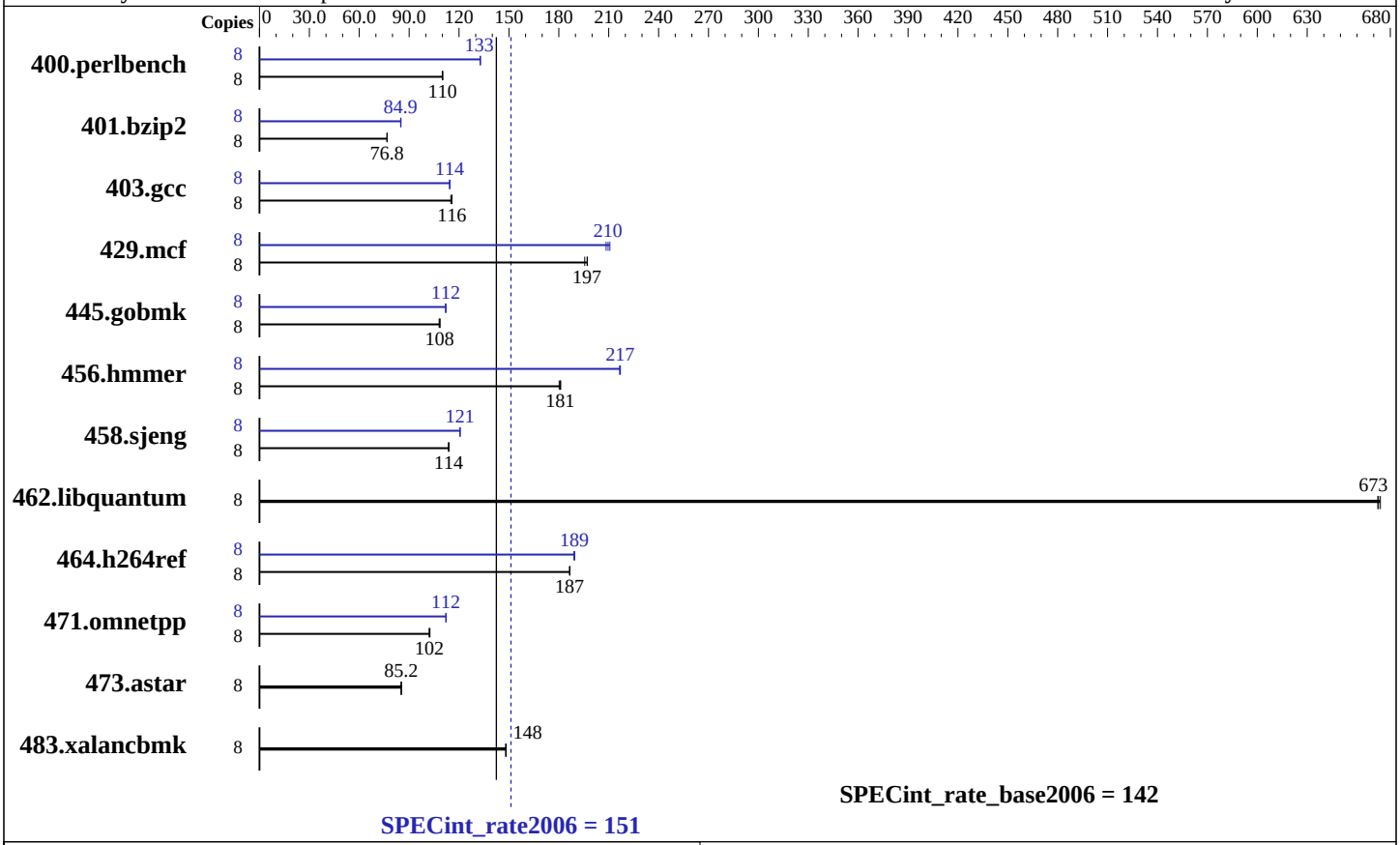
## Acer AR380 F1 (Intel Xeon L5609)

SPECint®_rate2006 = 151

SPECint_rate_base2006 = 142

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** Jun-2011
**Hardware Availability:** Feb-2011
**Software Availability:** Jan-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon L5609 |
| CPU Characteristics: | |
| CPU MHz: | 1866 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC, running at 1066 MHz) |
| Disk Subsystem: | 1 x 300 GB SATA, 10000 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |

# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

## Acer AR380 F1 (Intel Xeon L5609)

SPECint_rate2006 = 151

SPECint_rate_base2006 = 142

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** Jun-2011
**Hardware Availability:** Feb-2011
**Software Availability:** Jan-2011

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | **710** | **110** | 709 | 110 | 711 | 110 | 8 | 588 | 133 | **589** | **133** | 589 | 133 |
| 401.bzip2 | 8 | 1005 | 76.8 | **1005** | **76.8** | 1008 | 76.6 | 8 | 907 | 85.1 | 909 | 84.9 | **909** | **84.9** |
| 403.gcc | 8 | 560 | 115 | 556 | 116 | **556** | **116** | 8 | **563** | **114** | 563 | 114 | 562 | 115 |
| 429.mcf | 8 | 373 | 196 | **370** | **197** | 370 | 197 | 8 | 350 | 208 | 346 | 211 | **348** | **210** |
| 445.gobmk | 8 | **774** | **108** | 777 | 108 | 773 | 109 | 8 | **749** | **112** | 750 | 112 | 749 | 112 |
| 456.hmmer | 8 | 414 | 180 | 412 | 181 | **413** | **181** | 8 | **344** | **217** | 345 | 217 | 344 | 217 |
| 458.sjeng | 8 | 850 | 114 | 851 | 114 | **851** | **114** | 8 | 802 | 121 | **803** | **121** | 803 | 121 |
| 462.libquantum | 8 | **246** | **673** | 246 | 672 | 246 | 674 | 8 | **246** | **673** | 246 | 672 | 246 | 674 |
| 464.h264ref | 8 | 949 | 187 | 949 | 187 | **949** | **187** | 8 | 934 | 190 | 936 | 189 | **935** | **189** |
| 471.omnetpp | 8 | 488 | 103 | **489** | **102** | 490 | 102 | 8 | 446 | 112 | **446** | **112** | 445 | 112 |
| 473.astar | 8 | 658 | 85.3 | 659 | 85.2 | **659** | **85.2** | 8 | 658 | 85.3 | 659 | 85.2 | **659** | **85.2** |
| 483.xalancbmk | 8 | 373 | 148 | **373** | **148** | 372 | 148 | 8 | 373 | 148 | **373** | **148** | 372 | 148 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run
```

## Platform Notes

```
 BIOS Settings:
 Fan speed = full speed (Default = Balanced)
 Data Reuse = Disabled (Default = Enabled)
```

## General Notes

```
 Binaries compiled on RHEL 5.5
 This result was measured on the Gateway GR380 F1.
 The Acer AR380 F1, AR360 F1, Gateway GR360 F1 are electronically equivalent.
```

# Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

Continued on next page

## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page

## Peak Optimization Flags (Continued)

471.omnetpp (continued):
`-L/smartheap -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`
`http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`
`http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.xml`


SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 21:44:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 July 2011.