



SPEC[®] CINT2006 Result

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Dell Inc.

SPECint[®]_rate2006 = Not Run

PowerEdge R810 (Intel Xeon E7-8837, 2.66 GHz)

SPECint_rate_base2006 = 368

CPU2006 license: 55

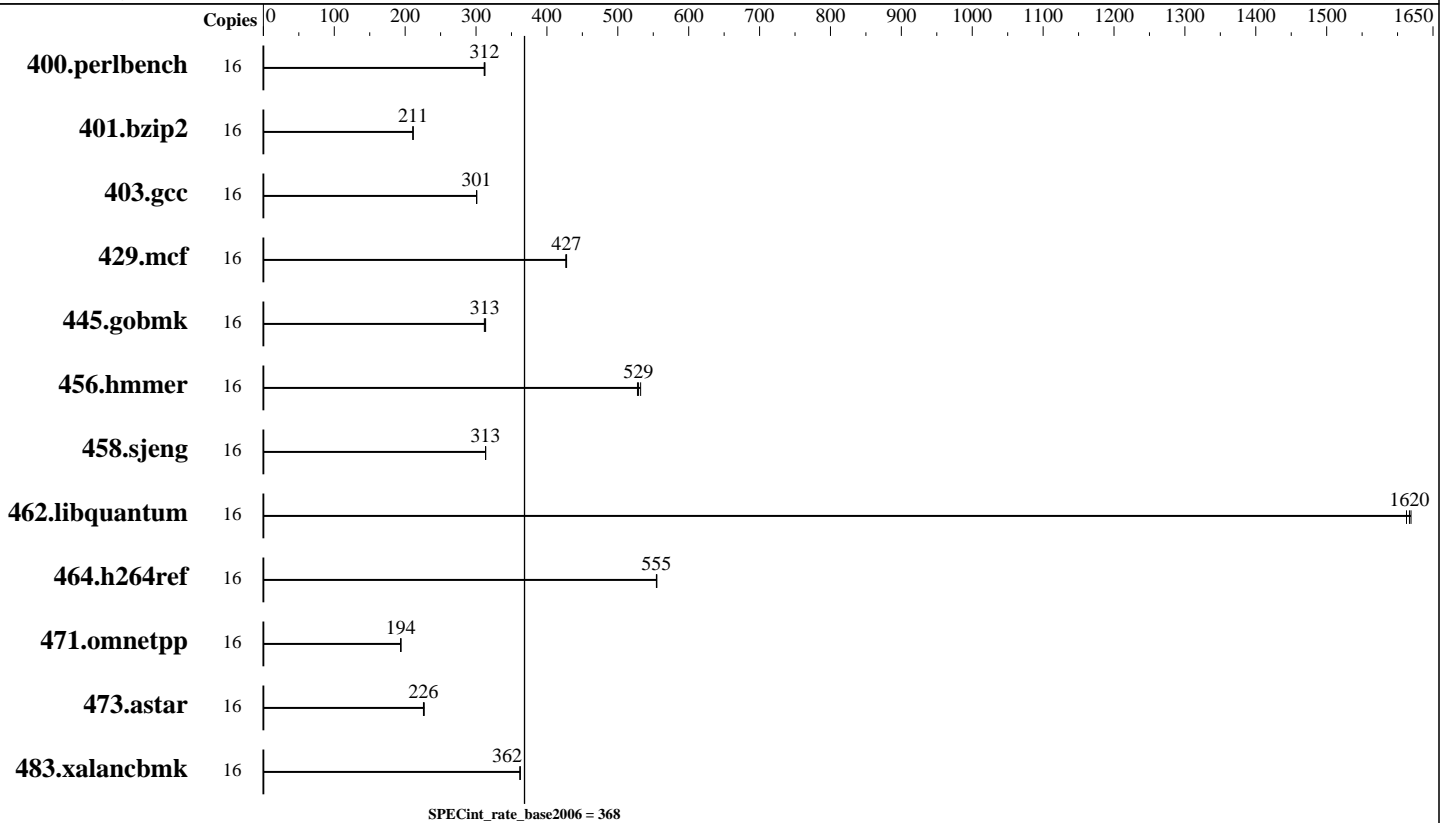
Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E7-8837
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3L-8500R-7, ECC)
 Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID0
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	502	311	500	313	<u>501</u>	<u>312</u>							
401.bzip2	16	732	211	<u>731</u>	<u>211</u>	731	211							
403.gcc	16	428	301	428	301	<u>428</u>	<u>301</u>							
429.mcf	16	342	427	<u>341</u>	<u>427</u>	341	428							
445.gobmk	16	538	312	<u>537</u>	<u>313</u>	535	314							
456.hammer	16	281	532	283	528	<u>282</u>	<u>529</u>							
458.sjeng	16	<u>618</u>	<u>313</u>	617	314	618	313							
462.libquantum	16	206	1610	205	1620	<u>205</u>	<u>1620</u>							
464.h264ref	16	<u>638</u>	<u>555</u>	638	555	639	554							
471.omnetpp	16	<u>516</u>	<u>194</u>	516	194	516	194							
473.astar	16	<u>497</u>	<u>226</u>	495	227	497	226							
483.xalancbmk	16	<u>305</u>	<u>362</u>	305	362	305	362							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

Binaries were compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
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For other inquiries, please contact webmaster@spec.org.

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