SPEC® CFP2006 Result

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IBM Corporation

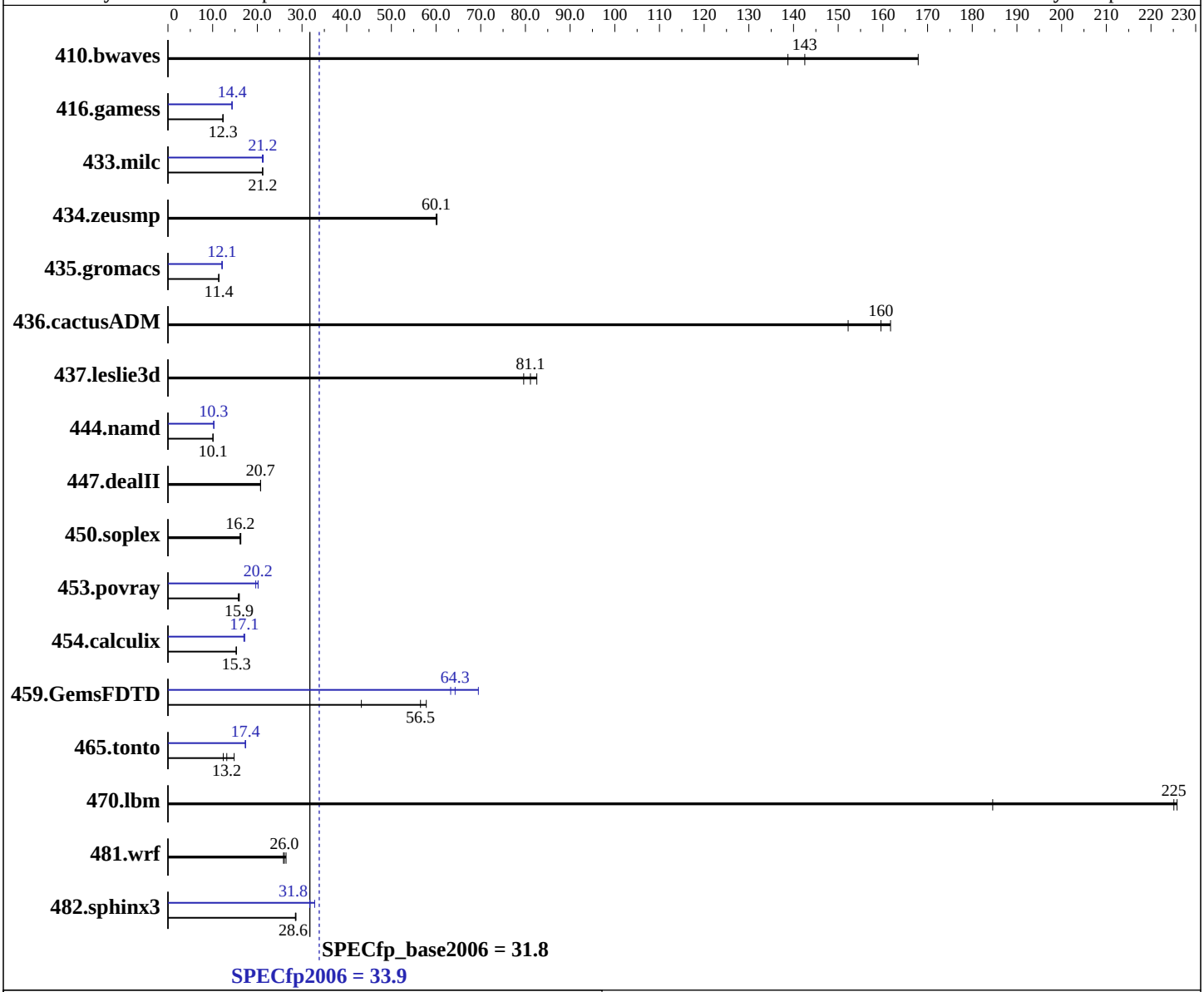
IBM System x3690 X5 (Intel Xeon E7-2803)

SPECfp®2006 = 33.9

SPECfp_base2006 = 31.8

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: May-2011
Hardware Availability: May-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Xeon E7-2803
CPU Characteristics:	
CPU MHz:	1733
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 18 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (32 x 8 GB 4Rx8 PC3-8500R-7, ECC, running at 800 MHz)
Disk Subsystem: 1 x 300 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	97.9	139	**95.4**	**143**	80.9	168	97.9	139	**95.4**	**143**	80.9	168
416.gamess	1583	12.4	1597	12.3	**1586**	**12.3**	**1363**	**14.4**	1363	14.4	1369	14.3
433.milc	433	21.2	433	21.2	**433**	**21.2**	435	21.1	**433**	**21.2**	430	21.3
434.zeusmp	**152**	**60.1**	151	60.2	152	60.1	**152**	**60.1**	151	60.2	152	60.1
435.gromacs	626	11.4	**626**	**11.4**	630	11.3	**590**	**12.1**	591	12.1	586	12.2
436.cactusADM	73.9	162	**74.9**	**160**	78.5	152	73.9	162	**74.9**	**160**	78.5	152
437.leslie3d	**116**	**81.1**	118	79.6	114	82.6	**116**	**81.1**	118	79.6	114	82.6
444.namd	**795**	**10.1**	796	10.1	795	10.1	781	10.3	782	10.3	**782**	**10.3**
447.dealII	551	20.7	552	20.7	**552**	**20.7**	551	20.7	552	20.7	**552**	**20.7**
450.soplex	516	16.2	511	16.3	**516**	**16.2**	516	16.2	511	16.3	**516**	**16.2**
453.povray	**334**	**15.9**	338	15.7	333	16.0	271	19.6	264	20.2	**264**	**20.2**
454.calculix	540	15.3	**539**	**15.3**	539	15.3	486	17.0	**482**	**17.1**	481	17.2
459.GemsFDTD	245	43.3	184	57.8	**188**	**56.5**	153	69.5	168	63.3	**165**	**64.3**
465.tonto	664	14.8	795	12.4	**747**	**13.2**	567	17.4	568	17.3	**567**	**17.4**
470.lbm	74.4	185	60.8	226	**61.0**	**225**	74.4	185	60.8	226	**61.0**	**225**
481.wrf	423	26.4	**429**	**26.0**	432	25.9	423	26.4	**429**	**26.0**	432	25.9
482.sphinx3	680	28.6	**682**	**28.6**	683	28.5	**614**	**31.8**	593	32.8	614	31.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited
'nodev   /mnt/hugepages   hugetlbfs   defaults   0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

General Notes

```
OMP_NUM_THREADS set to number of cores
Binaries compiled on RHEL 5.5
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 17:30:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 June 2011.