



# SPEC<sup>®</sup> CFP2006 Result

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## Acer Incorporated

SPECfp<sup>®</sup>\_rate2006 = **605**

## Gateway GR585 F1 (AMD Opteron 6172)

SPECfp\_rate\_base2006 = **558**

CPU2006 license: 97

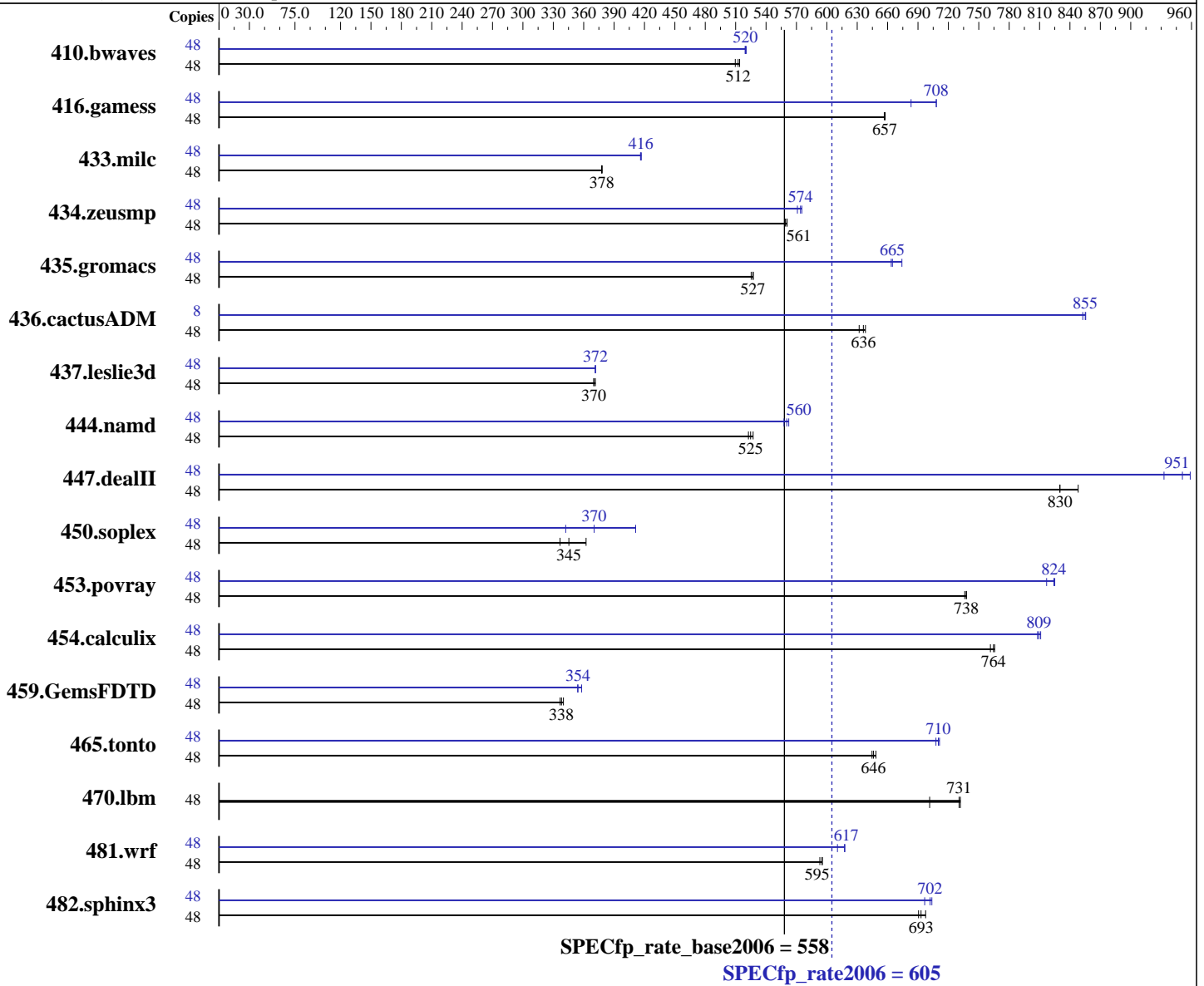
Test date: May-2011

Test sponsor: Acer Incorporated

Hardware Availability: Jul-2010

Tested by: Acer Incorporated

Software Availability: Jul-2010



### Hardware

CPU Name: AMD Opteron 6172  
 CPU Characteristics:  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 48 cores, 4 chips, 12 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 512 KB I+D on chip per core

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

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L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 6 cores  
 Other Cache: None  
 Memory: 128 GB (32 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	1280	510	<u>1273</u>	<u>512</u>	1269	514	48	<u>1255</u>	<u>520</u>	1256	519	1253	520
416.gamess	48	<u>1430</u>	<u>657</u>	1431	657	1429	658	48	1376	683	1327	708	<u>1327</u>	<u>708</u>
433.milc	48	1167	378	<u>1166</u>	<u>378</u>	1165	378	48	1059	416	1057	417	<u>1058</u>	<u>416</u>
434.zeusmp	48	781	559	779	561	<u>779</u>	<u>561</u>	48	765	571	759	575	<u>761</u>	<u>574</u>
435.gromacs	48	650	527	<u>650</u>	<u>527</u>	652	525	48	516	664	<u>516</u>	<u>665</u>	508	674
436.cactusADM	48	<u>902</u>	<u>636</u>	908	632	899	638	8	112	853	112	856	<u>112</u>	<u>855</u>
437.leslie3d	48	1220	370	<u>1219</u>	<u>370</u>	1214	372	48	1215	371	1214	372	<u>1214</u>	<u>372</u>
444.namd	48	737	523	<u>734</u>	<u>525</u>	730	527	48	690	558	<u>687</u>	<u>560</u>	684	562
447.dealII	48	662	830	647	848	<u>661</u>	<u>830</u>	48	<u>577</u>	<u>951</u>	573	959	589	933
450.soplex	48	1189	337	<u>1159</u>	<u>345</u>	1105	362	48	1170	342	<u>1081</u>	<u>370</u>	973	411
453.povray	48	346	738	347	736	<u>346</u>	<u>738</u>	48	313	817	310	825	<u>310</u>	<u>824</u>
454.calculix	48	517	766	<u>518</u>	<u>764</u>	520	761	48	488	811	<u>489</u>	<u>809</u>	490	808
459.GemsFDTD	48	1512	337	<u>1506</u>	<u>338</u>	1498	340	48	1439	354	1423	358	<u>1437</u>	<u>354</u>
465.tonto	48	<u>731</u>	<u>646</u>	728	648	733	645	48	667	708	664	711	<u>665</u>	<u>710</u>
470.lbm	48	940	701	901	732	<u>903</u>	<u>731</u>	48	940	701	901	732	<u>903</u>	<u>731</u>
481.wrf	48	900	596	<u>901</u>	<u>595</u>	904	593	48	878	611	868	618	<u>868</u>	<u>617</u>
482.sphinx3	48	1355	691	1341	698	<u>1350</u>	<u>693</u>	48	1343	697	1329	704	<u>1333</u>	<u>702</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
 'numactl' was used to bind copies to the cores.  
 See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
 'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr\_hugepages=21600 in /etc/sysctl.conf  
 mount -t hugetlbfs nodev /mnt/hugepages



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## General Notes

Environment variables set by runspec before the start of the run:

HUGETLB\_LIMIT = "450"

LD\_LIBRARY\_PATH = "/usr/cpu2006/amd1002-rate-libs-revC/64:/usr/cpu2006/amd1002-rate-libs-revC/32"

OMP\_NUM\_THREADS = "6"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at

<http://developer.amd.com/cpu/open64>

Binaries were compiled on SLES10 SP2 with binutils 2.18

This result was measured on the Gateway GR585 F1.

The Acer AR585 F1 and Gateway GR585 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64  
 436.cactusADM: -DSPEC\_CPU\_LP64 -fno-second-underscore  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX -DSPEC\_CPU\_CASE\_FLAG  
 -fno-second-underscore  
 482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

`-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m`

C++ benchmarks:

`-march=barcelona -mso -Ofast -static -INLINE:aggressive=on  
-OPT:malloc_alg=1 -HP:bdt=2m`

Fortran benchmarks:

`-march=barcelona -mso -Ofast -HP`

Benchmarks using both Fortran and C:

`-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP`

## Peak Compiler Invocation

C benchmarks:

`opencc`

C++ benchmarks:

`openCC`

Fortran benchmarks:

`openf95`

Benchmarks using both Fortran and C:

`opencc openf95`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`  
416.gamess: `-DSPEC_CPU_LP64`  
433.milc: `-DSPEC_CPU_LP64`  
434.zeusmp: `-DSPEC_CPU_LP64`  
435.gromacs: `-DSPEC_CPU_LP64`  
436.cactusADM: `-DSPEC_CPU_LP64 -fno-second-underscore`  
437.leslie3d: `-DSPEC_CPU_LP64`  
444.namd: `-DSPEC_CPU_LP64`  
453.povray: `-DSPEC_CPU_LP64`  
454.calculix: `-DSPEC_CPU_LP64`  
459.GemsFDTD: `-DSPEC_CPU_LP64`  
465.tonto: `-DSPEC_CPU_LP64`  
470.lbm: `-DSPEC_CPU_LP64`  
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG  
-fno-second-underscore`  
482.sphinx3: `-DSPEC_CPU_LP64`



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## Peak Optimization Flags

### C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1  
-CG:local\_sched\_alg=1 -CG:locs\_shallow\_depth=1  
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -Ofast -OPT:malloc\_alg=2  
-CG:sse\_cse\_regs=0 -CG:locs\_shallow\_depth=1 -CG:cmp\_peep=on  
-CG:local\_sched\_alg=1 -INLINE:aggressive=on

### C++ benchmarks:

444.namd: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -Ofast -LNO:ignore\_feedback=off  
-CG:local\_sched\_alg=2 -CG:load\_exe=0 -CG:compute\_to=on  
-OPT:unroll\_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.deallI: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on  
-LNO:opt=0 -fno-emit-exceptions -m32  
-OPT:unroll\_times\_max=8 -OPT:unroll\_size=256  
-OPT:unroll\_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on  
-CG:cmp\_peep=on -TENV:frame\_pointer=off

450.soplex: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -O3 -INLINE:aggressive=on  
-OPT:IEEE\_arith=3 -OPT:IEEE\_NaN\_Inf=off  
-OPT:fold\_unsigned\_relops=on -OPT:malloc\_alg=1  
-CG:load\_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

### Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on  
-LNO:blocking=off -LNO:prefetch\_ahead=5  
-LNO:ignore\_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m  
-CG:cmp\_peep=on

416.gamess: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0  
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll\_size=256  
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off  
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll\_size=256  
-CG:cmp\_peep=on -GRA:prioritize\_by\_density=on -HP

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## Peak Optimization Flags (Continued)

437.leslie3d: -march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m

459.GemsFDTD: -march=barcelona -mso -Ofast -LNO:fission=2  
-LNO:prefetch\_ahead=1 -CG:load\_exe=0 -CG:local\_sched\_alg=1  
-HP

465.tonto: -march=barcelona -mso -Ofast  
-OPT:alias=no\_f90\_pointer\_alias -LNO:blocking=off  
-CG:load\_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -mso -Ofast -OPT:rsqrt=2  
-HP:bdt=2m:heap=2m

436.cactusADM: -march=barcelona -mso -fb\_create fbdata(pass 1)  
-fb\_opt fbdata(pass 2) -Ofast -apo -LNO:prefetch\_ahead=1  
-HP:bdt=2m:heap=2m -LANG:heap\_allocation\_threshold=100

454.calculix: -march=barcelona -mso -Ofast -CG:load\_exe=0  
-CG:ptr\_load\_use=0 -CG:local\_sched\_alg=2 -CG:compute\_to=on  
-LNO:prefetch\_ahead=30 -WOPT:unroll=2  
-GRA:optimize\_boundary=on -HP:bdt=2m:heap=2m

481.wrf: -march=barcelona -mso -Ofast -LNO:blocking=off  
-LNO:prefetch\_ahead=10 -LANG:copyinout=off  
-IPA:callee\_limit=5000 -GRA:prioritize\_by\_density=on -m3dnow  
-HP

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 17:23:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 10 June 2011.