SPEC® CFP2006 Result

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Dell Inc.

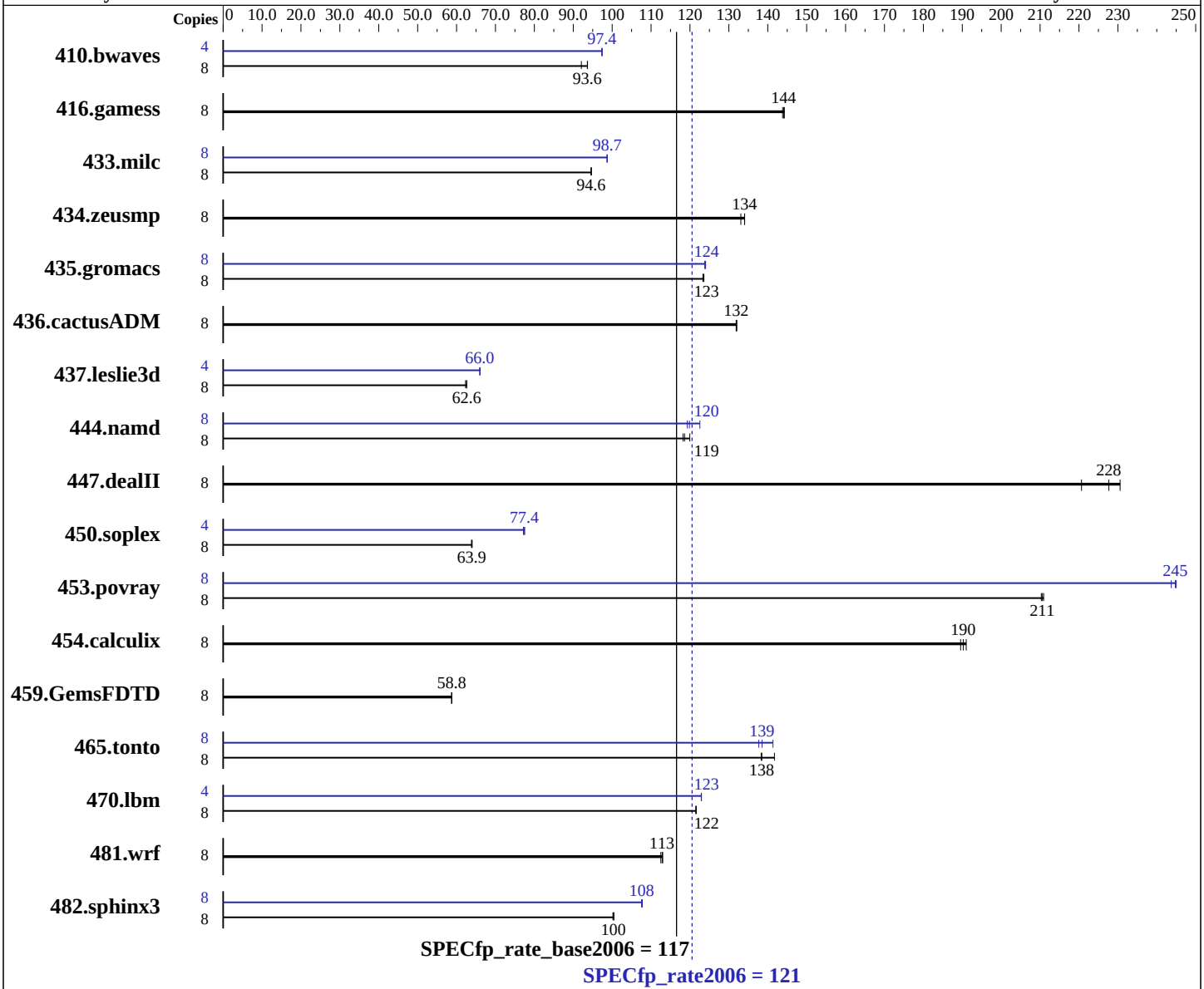
PowerEdge R210 II (Intel Xeon E3-1280, 3.50 GHz)

SPECfp®_rate2006 = 121

SPECfp_rate_base2006 = 117

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Apr-2011
Hardware Availability: May-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon E3-1280
CPU Characteristics:	Intel Turbo Boost Technology up to 3.90 GHz
CPU MHz:	3500
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (4 x 2 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 146 GB 15000 RPM SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	1181	92.1	1161	93.7	**1161**	**93.6**	4	**558**	**97.4**	558	97.4	558	97.3
416.gamess	8	**1087**	**144**	1089	144	1086	144	8	**1087**	**144**	1089	144	1086	144
433.milc	8	**776**	**94.6**	776	94.6	776	94.6	8	744	98.7	**744**	**98.7**	744	98.7
434.zeusmp	8	**543**	**134**	547	133	543	134	8	**543**	**134**	547	133	543	134
435.gromacs	8	463	123	**463**	**123**	462	124	8	**461**	**124**	461	124	461	124
436.cactusADM	8	724	132	**725**	**132**	725	132	8	724	132	**725**	**132**	725	132
437.leslie3d	8	**1201**	**62.6**	1201	62.6	1207	62.3	4	570	66.0	569	66.1	**570**	**66.0**
444.namd	8	**541**	**119**	535	120	543	118	8	524	123	**535**	**120**	538	119
447.dealII	8	397	231	**402**	**228**	415	221	8	397	231	**402**	**228**	415	221
450.soplex	8	1042	64.0	**1044**	**63.9**	1045	63.9	4	430	77.5	432	77.2	**431**	**77.4**
453.povray	8	**202**	**211**	202	211	202	210	8	175	244	**174**	**245**	174	245
454.calculix	8	**347**	**190**	348	190	346	191	8	**347**	**190**	348	190	346	191
459.GemsFDTD	8	**1444**	**58.8**	1444	58.8	1445	58.7	8	**1444**	**58.8**	1444	58.8	1445	58.7
465.tonto	8	555	142	569	138	**568**	**138**	8	557	141	**568**	**139**	572	138
470.lbm	8	**904**	**122**	904	122	904	122	4	447	123	447	123	**447**	**123**
481.wrf	8	**792**	**113**	794	113	791	113	8	**792**	**113**	794	113	791	113
482.sphinx3	8	**1553**	**100**	1556	100	1552	100	8	1447	108	**1449**	**108**	1449	108

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 3600> /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
```

General Notes

```
Binaries were compiled on RHEL5.5
The Dell PowerEdge R210 II and
the Bull NovaScale R410B F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R210 II model
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:
`-xAVX -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xAVX -ipo -O3 -no-prec-div -static -ansi-alias`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3

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Peak Optimization Flags (Continued)

465.tonto (continued):
`-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

435.gromacs: `-xAVX`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml`

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