SPEC® CINT2006 Result

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Fujitsu

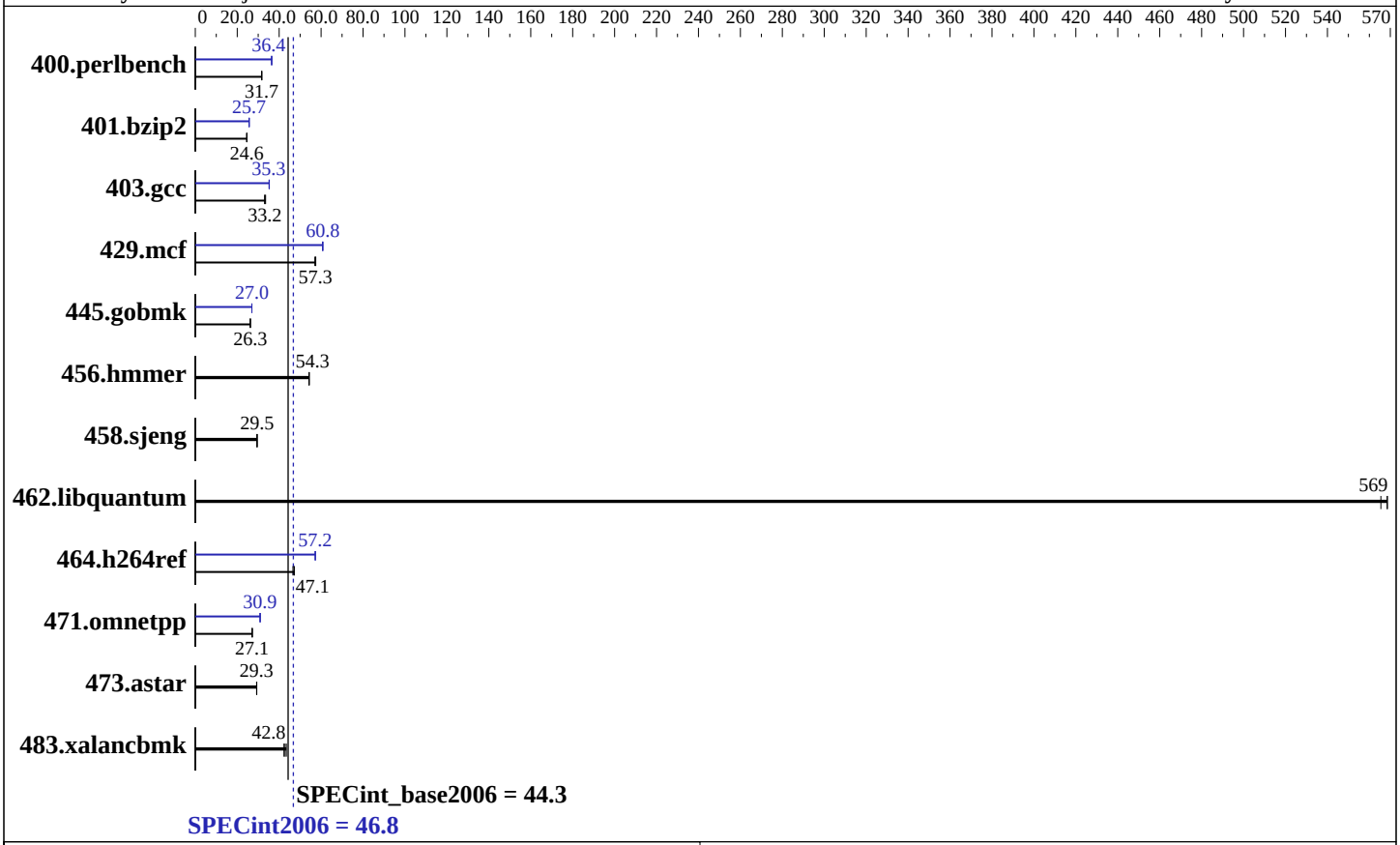
PRIMERGY TX120 S3, Intel Xeon E3-1230, 3.20 GHz

SPECint®2006 = 46.8

SPECint_base2006 = 44.3

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2011
Hardware Availability: Jun-2011
Software Availability: Jan-2011



Hardware

CPU Name:	Intel Xeon E3-1230
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
Disk Subsystem:	1 x SATA, 300 GB, 7200 RPM
Other Hardware:	--

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**308**	**31.7**	309	31.6	308	31.7	268	36.5	**268**	**36.4**	269	36.4
401.bzip2	393	24.5	392	24.6	**393**	**24.6**	377	25.6	375	25.7	**375**	**25.7**
403.gcc	244	33.1	**242**	**33.2**	240	33.5	229	35.2	228	35.3	**228**	**35.3**
429.mcf	159	57.5	160	57.0	**159**	**57.3**	**150**	**60.8**	150	60.7	150	60.9
445.gobmk	**400**	**26.3**	400	26.3	400	26.2	389	27.0	**389**	**27.0**	389	27.0
456.hmmer	172	54.3	172	54.2	**172**	**54.3**	172	54.3	172	54.2	**172**	**54.3**
458.sjeng	**410**	**29.5**	412	29.4	410	29.5	**410**	**29.5**	412	29.4	410	29.5
462.libquantum	36.4	569	**36.4**	**569**	36.6	565	36.4	569	**36.4**	**569**	36.6	565
464.h264ref	**470**	**47.1**	469	47.2	475	46.6	387	57.2	**387**	**57.2**	387	57.3
471.omnetpp	230	27.2	**230**	**27.1**	231	27.1	202	31.0	**202**	**30.9**	203	30.8
473.astar	239	29.3	240	29.2	**240**	**29.3**	239	29.3	240	29.2	**240**	**29.3**
483.xalancbmk	**161**	**42.8**	163	42.3	159	43.4	**161**	**42.8**	163	42.3	159	43.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
BIOS configuration:
Intel HT Technology = Disable
```

General Notes

```
OMP_NUM_THREADS set to number of cores
This result was measured on the PRIMERGY TX120 S3. The PRIMERGY TX120 S3
and the PRIMERGY TX140 S1 are electronically equivalent.
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:

```
icc  -m64
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

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Peak Compiler Invocation (Continued)

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

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Peak Optimization Flags (Continued)

456.hmmer: basepeak = yes

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Intel-Linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Intel-Linux64.xml

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For other inquiries, please contact webmaster@spec.org.

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