SPEC® CINT2006 Result

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GIGA-BYTE Technology Co. Ltd.

(Test Sponsor: Advanced Micro Devices)

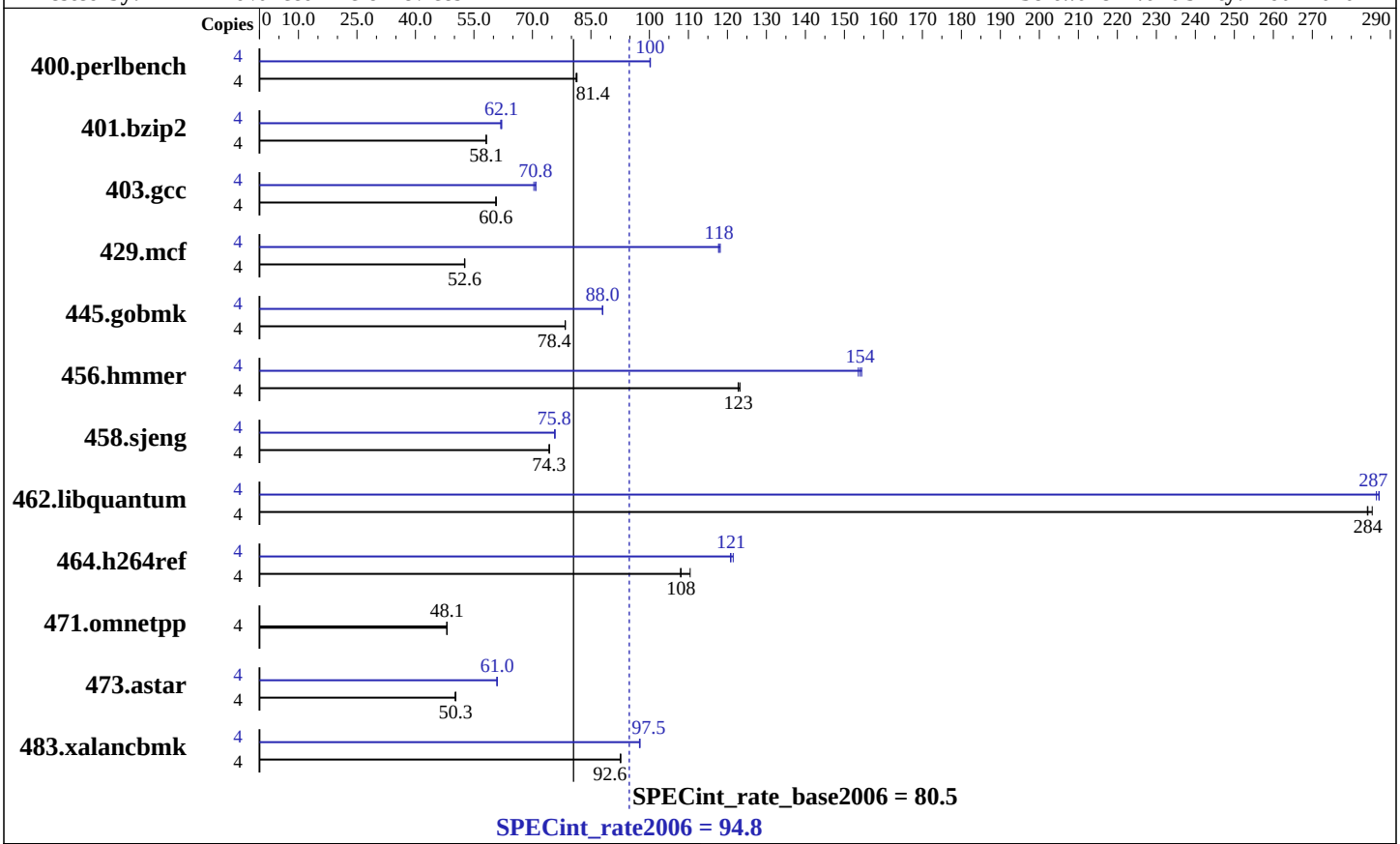
Gigabyte GA-880GMA-USB3,
AMD Phenom II X4 955

SPECint®_rate2006 = 94.8

SPECint_rate_base2006 = 80.5

CPU2006 license: 49
Test sponsor: Advanced Micro Devices
Tested by: Advanced Micro Devices

Test date: Apr-2011
Hardware Availability: Mar-2010
Software Availability: Jul-2010



Hardware

CPU Name:	AMD Phenom II X4 955
CPU Characteristics:	
CPU MHz:	3200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	6 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB 1Rx4 PC3-10600U-9)
Disk Subsystem:	1 x 2 TB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	x86 Open64 4.2.4 Compiler Suite (from AMD)
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap 8.1 32-bit Library for Linux

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**480**	**81.4**	480	81.4	481	81.2	4	**390**	**100**	390	100	390	100
401.bzip2	4	663	58.2	664	58.1	**664**	**58.1**	4	**622**	**62.1**	622	62.1	624	61.9
403.gcc	4	**531**	**60.6**	530	60.7	531	60.6	4	458	70.4	**455**	**70.8**	455	70.8
429.mcf	4	**693**	**52.6**	694	52.6	693	52.7	4	**309**	**118**	310	118	309	118
445.gobmk	4	**535**	**78.4**	535	78.4	535	78.4	4	**477**	**88.0**	477	88.0	477	88.0
456.hmmer	4	303	123	**304**	**123**	304	123	4	242	154	243	154	**242**	**154**
458.sjeng	4	**651**	**74.3**	651	74.3	651	74.3	4	639	75.7	**639**	**75.8**	639	75.8
462.libquantum	4	292	284	290	285	**292**	**284**	4	289	286	289	287	**289**	**287**
464.h264ref	4	820	108	**819**	**108**	801	110	4	729	121	733	121	**732**	**121**
471.omnetpp	4	**520**	**48.1**	520	48.1	520	48.1	4	**520**	**48.1**	520	48.1	520	48.1
473.astar	4	**558**	**50.3**	560	50.2	558	50.3	4	461	60.9	**461**	**61.0**	461	61.0
483.xalancbmk	4	298	92.6	298	92.6	**298**	**92.6**	4	**283**	**97.5**	283	97.4	283	97.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=3584 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages

Binaries were compiled on SLES10 SP2 with binutils 2.18
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "3584"
LD_LIBRARY_PATH = "/root/work/cpu2006v1.1/amd1002-rate-libs-revC/64:/root/work/cpu2006v1.1/amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-march=barcelona -mso -Ofast -CG:local_sched_alg=1
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small_pu=100
-HP:bdt=2m:heap=2m

C++ benchmarks:
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on
-CG:cmp_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

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Peak Portability Flags (Continued)

401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-CG:local_sched_alg=1 -CG:unroll_fb_req=on
-HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
-LNO:prefetch_ahead=10 -CG:cmp_peep=on -m32
-HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
-CG:gcm=off -GRA:prioritize_by_density=on -m32
-HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
-IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
-LNO:ignore_feedback=off -CG:p2align=on
-CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
-OPT:alias=disjoint -OPT:unroll_times_max=8
-OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
-CG:local_sched_alg=1 -CG:cflow=0
-CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
-HP:bdt=2m:heap=2m

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Peak Optimization Flags (Continued)

458.sjeng: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -ipa -LNO:ignore_feedback=off
-LNO:full_unroll=10 -LNO:fusion=0 -LNO:fission=2
-IPA:pu_reorder=2 -CG:ptr_load_use=0
-OPT:unroll_times_max=8 -INLINE:aggressive=on

462.libquantum: -march=barcelona -mso -Ofast -LNO:pf2=0 -CG:gcm=off
-CG:use_prefetchnta=on -CG:cmp_peep=on -WOPT:aggstr=0
-HP:bdt=2m:heap=2m -OPT:alias=disjoint
-INLINE:aggressive=on -IPA:space=1000 -IPA:plimit=20000

464.h264ref: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -IPA:plimit=20000
-OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr_load_use=0
-CG:push_pop_int_saved_regs=off

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -TENV:frame_pointer=off
-WOPT:if_conv=0 -GRA:optimize_boundary=on
-OPT:alias=disjoint -INLINE:aggressive=on
-IPA:small_pu=3000 -IPA:plimit=3000 -m32
-HP:bdt=2m:heap=2m

483.xalancbmk: -march=barcelona -mso -Ofast -INLINE:aggressive=on -m32
-CG:cmp_peep=on -GRA:unspill=on -TENV:frame_pointer=off
-fno-emit-exceptions
-L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20101109.html
http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.20110119.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20101109.xml
http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.20110119.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 20:47:17 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 10 May 2011.