SPEC® CFP2006 Result

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Intel Corporation

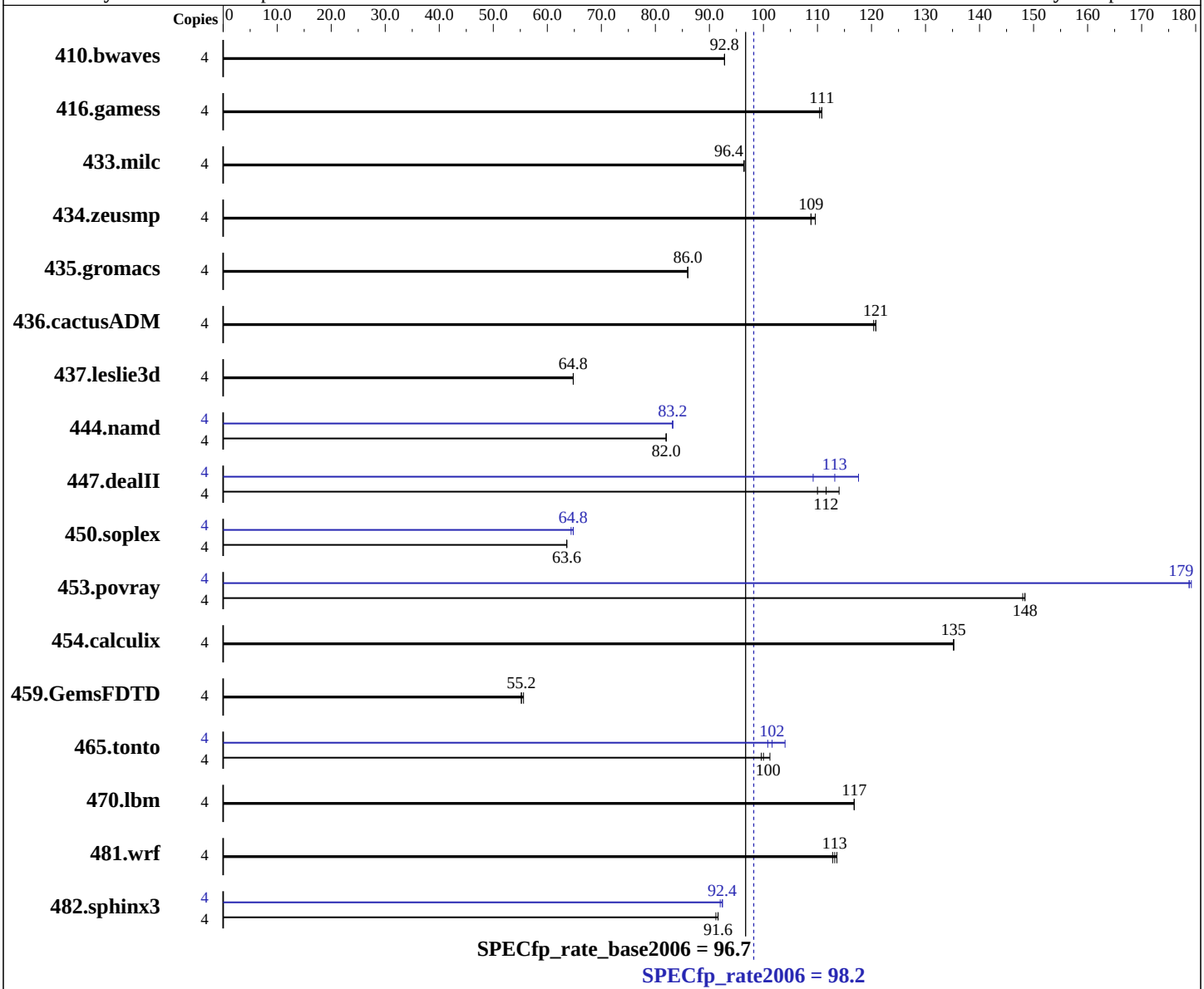
Intel DH67BLB3 Motherboard (Intel Core i5-2310)

SPECfp®_rate2006 = 98.2

SPECfp_rate_base2006 = 96.7

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Apr-2011
Hardware Availability: Jun-2011
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Core i5-2310
CPU Characteristics:	Intel Turbo Boost Technology up to 3.2 GHz
CPU MHz:	2900
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Windows 7 Ultimate (64-bit)
Compiler:	Intel C++ Compiler XE for Intel64 Version 12.0.3.163 Build 20110217 Intel Visual Fortran Compiler XE for Intel64 Version 12.0.3.163 Build 20110217 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
Other Hardware: None

System State: Default
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap Library Version 9.01 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	586	92.8	585	92.8	**585**	**92.8**	4	586	92.8	585	92.8	**585**	**92.8**
416.gamess	4	708	110	707	111	**708**	**111**	4	708	110	707	111	**708**	**111**
433.milc	4	382	96.4	382	96.4	**382**	**96.4**	4	382	96.4	382	96.4	**382**	**96.4**
434.zeusmp	4	332	110	334	109	**334**	**109**	4	332	110	334	109	**334**	**109**
435.gromacs	4	**332**	**86.0**	332	86.0	332	86.0	4	**332**	**86.0**	332	86.0	332	86.0
436.cactusADM	4	**396**	**121**	398	120	396	121	4	**396**	**121**	398	120	396	121
437.leslie3d	4	**582**	**64.8**	581	64.8	582	64.8	4	**582**	**64.8**	581	64.8	582	64.8
444.namd	4	392	82.0	392	82.0	**392**	**82.0**	4	386	83.2	386	83.2	**386**	**83.2**
447.dealII	4	**410**	**112**	415	110	402	114	4	419	109	390	118	**404**	**113**
450.soplex	4	523	63.6	**523**	**63.6**	523	63.6	4	518	64.4	515	64.8	**515**	**64.8**
453.povray	4	143	148	144	148	**143**	**148**	4	119	179	119	179	**119**	**179**
454.calculix	4	244	135	**244**	**135**	244	135	4	244	135	**244**	**135**	244	135
459.GemsFDTD	4	767	55.2	766	55.6	**766**	**55.2**	4	767	55.2	766	55.6	**766**	**55.2**
465.tonto	4	389	101	**394**	**100**	395	99.6	4	379	104	**388**	**102**	390	101
470.lbm	4	**470**	**117**	470	117	470	117	4	**470**	**117**	470	117	470	117
481.wrf	4	396	113	393	114	**394**	**113**	4	396	113	393	114	**394**	**113**
482.sphinx3	4	853	91.6	**853**	**91.6**	853	91.2	4	847	92.0	**845**	**92.4**	845	92.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
The start command with the /affinity switch was used to bind processes to cores
```

General Notes

```
Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc9

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 -names:lowercase
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 /names:lowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER /names:lowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
-link /FORCE:MULTIPLE

C++ benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qcxx-features
-Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-QxAVX -Qipo -O3 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F1000000000
-link /FORCE:MULTIPLE

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `-QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE`

C++ benchmarks:

444.namd: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

447.dealII: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qscalar-rep- -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

450.soplex: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

453.povray: `-QxAVX`(pass 2) `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32 /F1000000000 shlW64M.lib -link /FORCE:MULTIPLE`

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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