# SPEC® CINT2006 Result

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## Acer Incorporated

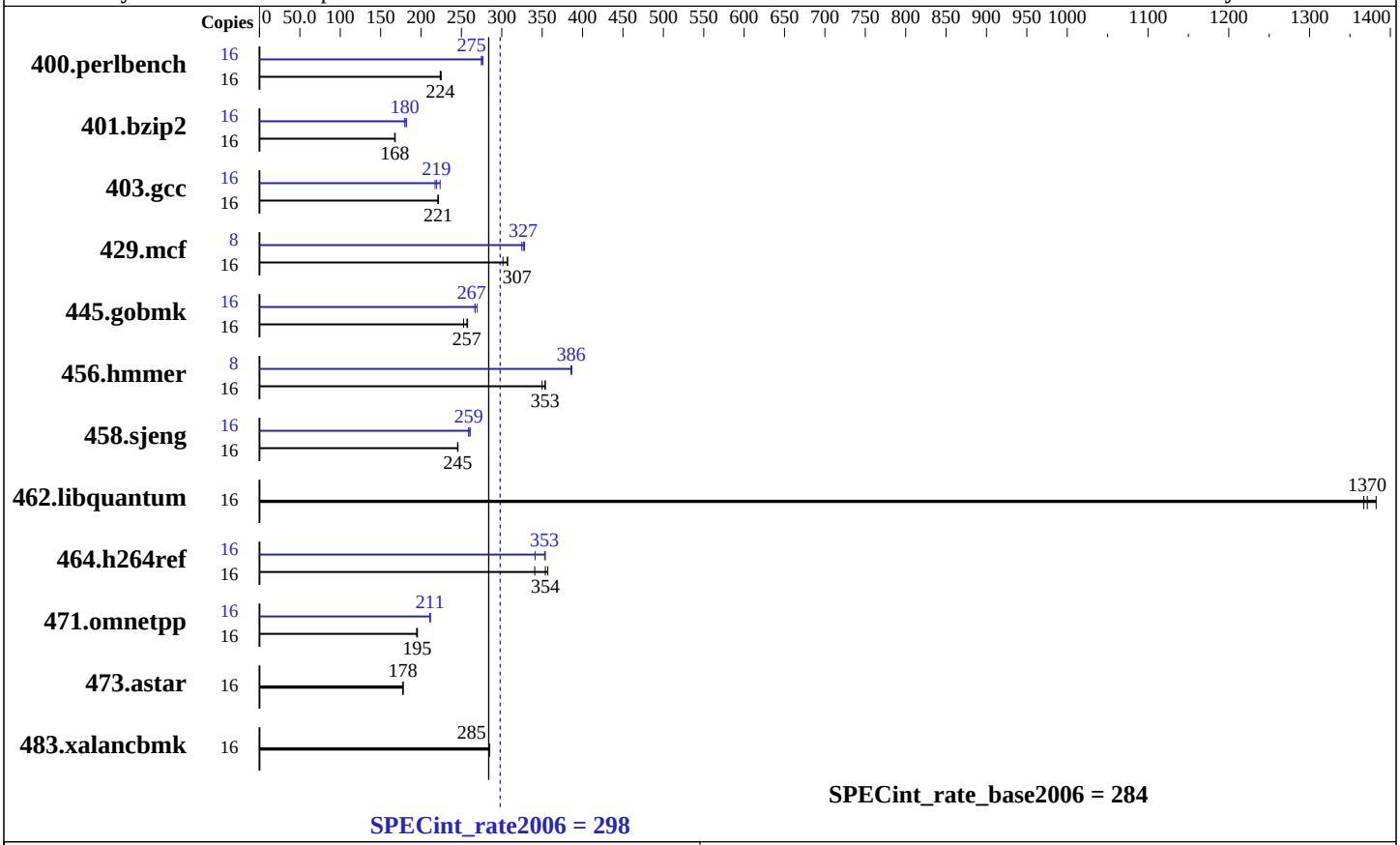
## Acer AR380 F1 (Intel Xeon X5667)

SPECint®_rate2006 = 298

SPECint_rate_base2006 = 284

**CPU2006 license:** 97
**Test sponsor:** Acer Incorporated
**Tested by:** Acer Incorporated

**Test date:** Apr-2011
**Hardware Availability:** Jun-2010
**Software Availability:** Jan-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5667 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.46 GHz |
| CPU MHz: | 3067 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 12 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC) |
| Disk Subsystem: | 1 x 300 GB SATA, 10000 RPM |
| Other Hardware: | None |

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V9.01 |

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 695 | 225 | **698** | **224** | 699 | 224 | 16 | 565 | 277 | **568** | **275** | 569 | 275 |
| 401.bzip2 | 16 | 919 | 168 | 922 | 167 | **920** | **168** | 16 | 848 | 182 | 858 | 180 | **857** | **180** |
| 403.gcc | 16 | 581 | 222 | 583 | 221 | **582** | **221** | 16 | **587** | **219** | 576 | 224 | 592 | 217 |
| 429.mcf | 16 | 484 | 301 | **476** | **307** | 475 | 307 | 8 | 225 | 325 | **223** | **327** | 222 | 328 |
| 445.gobmk | 16 | 651 | 258 | **653** | **257** | 664 | 253 | 16 | 623 | 270 | 629 | 267 | **629** | **267** |
| 456.hmmer | 16 | 422 | 354 | **422** | **353** | 427 | 350 | 8 | 194 | 386 | 193 | 387 | **193** | **386** |
| 458.sjeng | 16 | 789 | 245 | **789** | **245** | 788 | 246 | 16 | 742 | 261 | 747 | 259 | **747** | **259** |
| 462.libquantum | 16 | 242 | 1370 | 240 | 1380 | **242** | **1370** | 16 | 242 | 1370 | 240 | 1380 | **242** | **1370** |
| 464.h264ref | 16 | 992 | 357 | 1039 | 341 | **1000** | **354** | 16 | 1000 | 354 | **1003** | **353** | 1038 | 341 |
| 471.omnetpp | 16 | 511 | 196 | 514 | 195 | **512** | **195** | 16 | 474 | 211 | **473** | **211** | 473 | 212 |
| 473.astar | 16 | 633 | 178 | 632 | 178 | **632** | **178** | 16 | 633 | 178 | 632 | 178 | **632** | **178** |
| 483.xalancbmk | 16 | **388** | **285** | 388 | 285 | 388 | 284 | 16 | **388** | **285** | 388 | 285 | 388 | 284 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
  numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run
```

## Platform Notes

```
 BIOS Settings:
 Fan speed = full speed (Default = Balanced)
 Data Reuse = Disabled (Default = Enabled)
```

## General Notes

```
 Binaries compiled on RHEL 5.5
 This result was measured on the Gateway GR380 F1.
 The Acer AR380 F1, AR360 F1, Gateway GR360 F1 are electronically equivalent.
```

# Base Compiler Invocation

C benchmarks:

```
icc  -m32
```

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## Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page

## Peak Optimization Flags (Continued)

471.omnetpp (continued):
`-L/smartheap -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

## Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html`
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml`
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`


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For other inquiries, please contact webmaster@spec.org.

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