SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

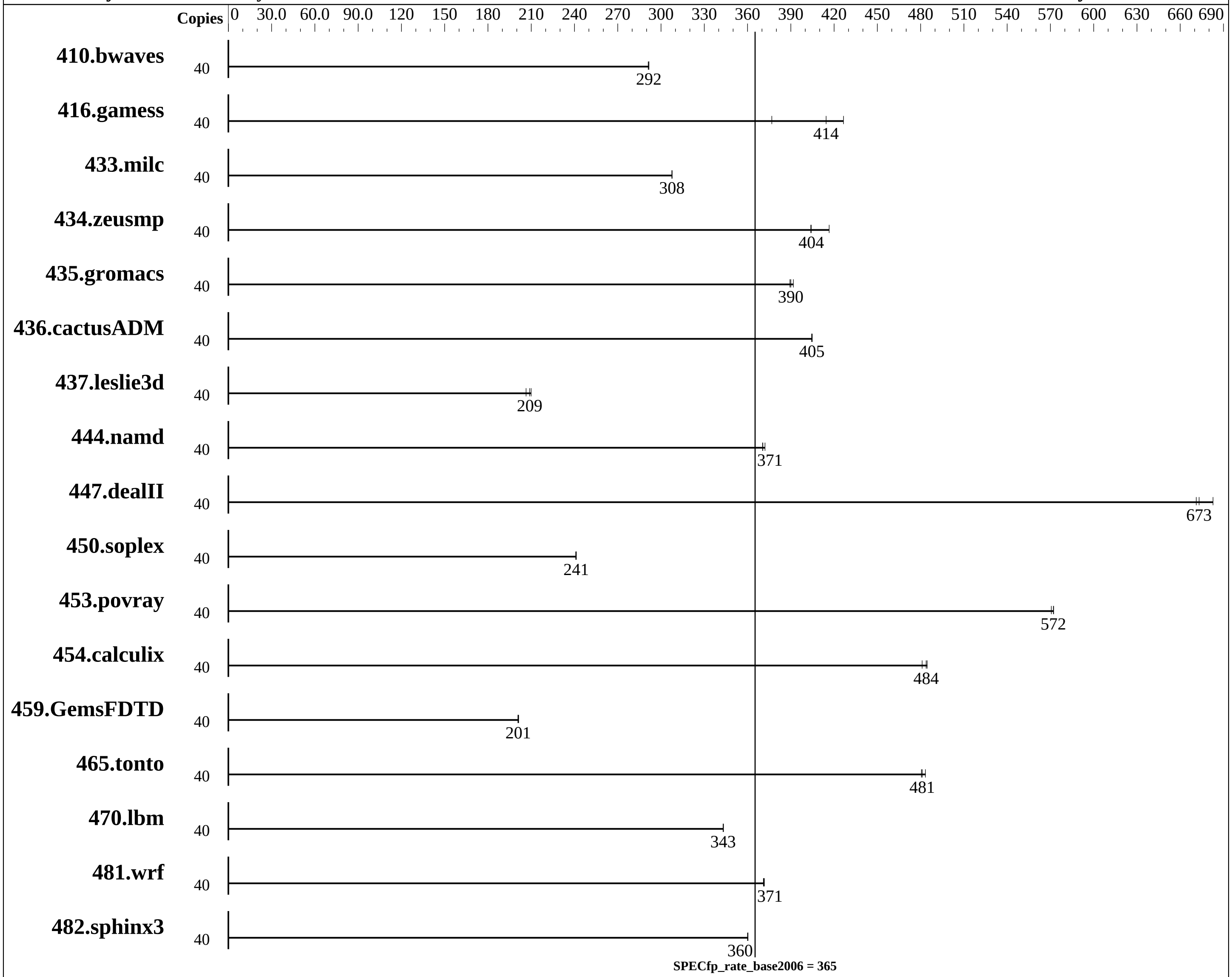
Cisco Systems

Cisco UCS C260 M2 (Intel Xeon E7-2870, 2.40 GHz)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 365

CPU2006 license:	9019	**Test date:**	Mar-2011
Test sponsor:	Cisco Systems	**Hardware Availability:**	Jun-2011
Tested by:	Cisco Systems	**Software Availability:**	Jan-2011



Hardware

CPU Name:	Intel Xeon E7-2870
CPU Characteristics:	Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Redhat Enterprise Linux 6.0, Kernel 2.6.32-71.el6.x86_64
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Cisco Systems

Cisco UCS C260 M2 (Intel Xeon E7-2870, 2.40 GHz)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 365

CPU2006 license: 9019
Test sponsor: Cisco Systems
Tested by: Cisco Systems
Test date: Mar-2011
Hardware Availability: Jun-2011
Software Availability: Jan-2011

L3 Cache: 30 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (32 x 16 GB 4Rx4 PC3-8500R-9, ECC)
Disk Subsystem: 2 x 64 GB SSD, SATA Gen2, 3Gb/s
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	40	1864	292	1867	291	**<u>1864</u>**	**<u>292</u>**							
416.gamess	40	2078	377	**<u>1890</u>**	**<u>414</u>**	1836	426							
433.milc	40	1194	307	1193	308	**<u>1194</u>**	**<u>308</u>**							
434.zeusmp	40	874	417	**<u>901</u>**	**<u>404</u>**	901	404							
435.gromacs	40	**<u>732</u>**	**<u>390</u>**	733	389	729	392							
436.cactusADM	40	**<u>1181</u>**	**<u>405</u>**	1182	404	1181	405							
437.leslie3d	40	**<u>1800</u>**	**<u>209</u>**	1822	206	1791	210							
444.namd	40	862	372	866	370	**<u>866</u>**	**<u>371</u>**							
447.dealII	40	670	683	682	671	**<u>680</u>**	**<u>673</u>**							
450.soplex	40	1383	241	**<u>1383</u>**	**<u>241</u>**	1385	241							
453.povray	40	373	571	372	572	**<u>372</u>**	**<u>572</u>**							
454.calculix	40	681	485	686	481	**<u>682</u>**	**<u>484</u>**							
459.GemsFDTD	40	**<u>2111</u>**	**<u>201</u>**	2108	201	2114	201							
465.tonto	40	814	483	**<u>818</u>**	**<u>481</u>**	819	481							
470.lbm	40	**<u>1602</u>**	**<u>343</u>**	1602	343	1601	343							
481.wrf	40	1202	372	1204	371	**<u>1203</u>**	**<u>371</u>**							
482.sphinx3	40	2164	360	2166	360	**<u>2164</u>**	**<u>360</u>**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
ulimit -s unlimited was used to set the stacksize to unlimited prior to run
Large pages were not enabled for this run
```

Platform Notes

```
BIOS Configuration : Data Reuse Optimization = Disabled
```

General Notes

```
Binaries compiled on RHEL5.5 with
binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves:	-DSPEC_CPU_LP64
416.gamess:	-DSPEC_CPU_LP64
433.milc:	-DSPEC_CPU_LP64
434.zeusmp:	-DSPEC_CPU_LP64
435.gromacs:	-DSPEC_CPU_LP64 -nofor_main
436.cactusADM:	-DSPEC_CPU_LP64 -nofor_main
437.leslie3d:	-DSPEC_CPU_LP64
444.namd:	-DSPEC_CPU_LP64
447.dealII:	-DSPEC_CPU_LP64
450.soplex:	-DSPEC_CPU_LP64
453.povray:	-DSPEC_CPU_LP64
454.calculix:	-DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD:	-DSPEC_CPU_LP64
465.tonto:	-DSPEC_CPU_LP64
470.lbm:	-DSPEC_CPU_LP64
481.wrf:	-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3:	-DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

Continued on next page

Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.02.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.02.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 19:40:11 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 May 2011.