# SPEC® CFP2006 Result

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## Bull SAS

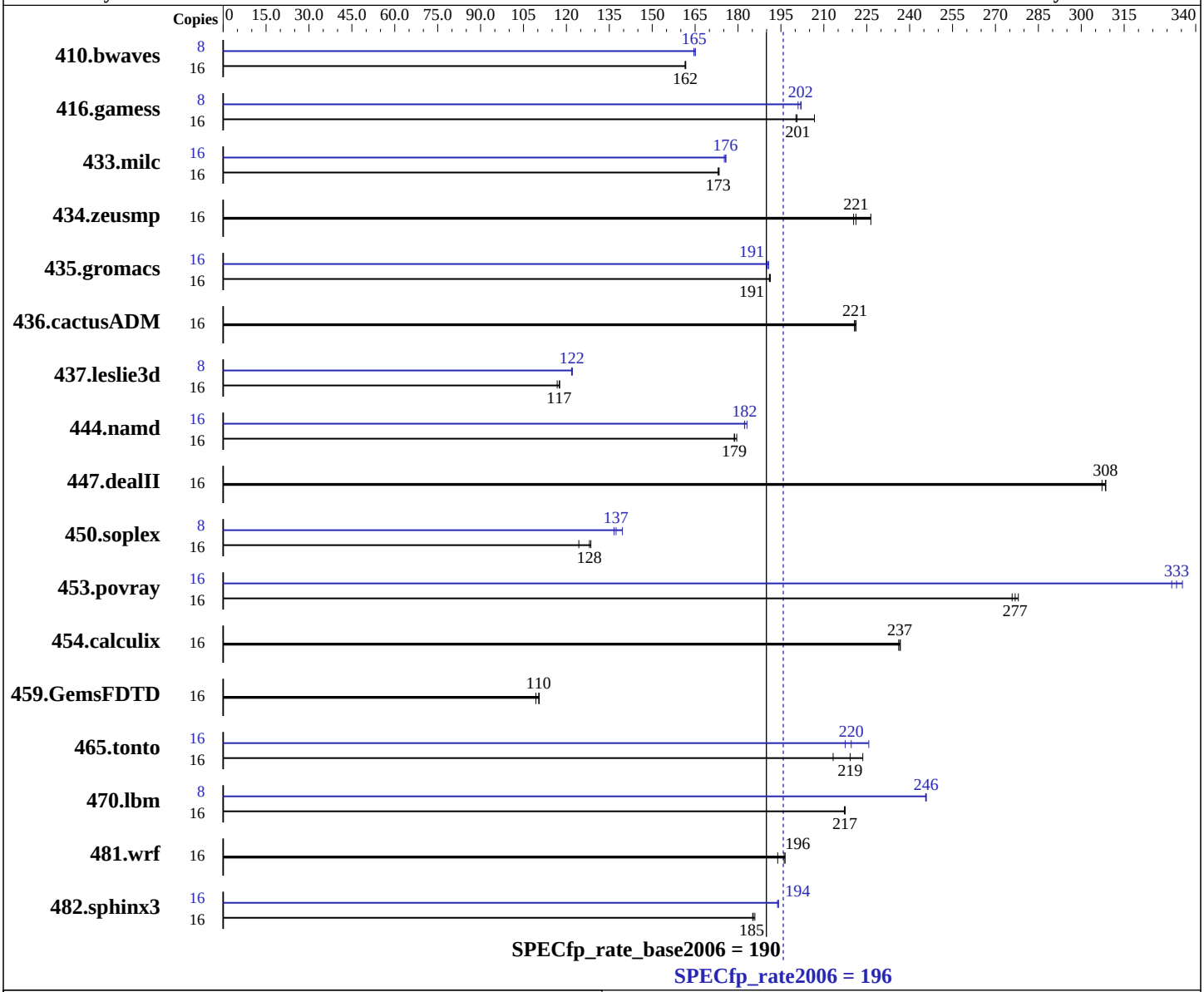
NovaScale T860 F2 (Intel Xeon X5647, 2.93 GHz)

SPECfp®_rate2006 = 196

SPECfp_rate_base2006 = 190

**CPU2006 license:** 20
**Test sponsor:** Bull SAS
**Tested by:** Dell Inc.

**Test date:** Mar-2011
**Hardware Availability:** Feb-2011
**Software Availability:** Jan-2011




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5647 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.20 GHz |
| CPU MHz: | 2933 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 146 GB 15000 RPM SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 16 | 1344 | 162 | **1346** | **162** | 1346 | 162 | 8 | 658 | 165 | **660** | **165** | 661 | 165 |
| 416.gamess | 16 | 1565 | 200 | **1561** | **201** | 1515 | 207 | 8 | 775 | 202 | **776** | **202** | 779 | 201 |
| 433.milc | 16 | 847 | 173 | 849 | 173 | **848** | **173** | 16 | 838 | 175 | **837** | **176** | 836 | 176 |
| 434.zeusmp | 16 | 643 | 226 | 661 | 220 | **658** | **221** | 16 | 643 | 226 | 661 | 220 | **658** | **221** |
| 435.gromacs | 16 | **598** | **191** | 597 | 191 | 598 | 191 | 16 | 599 | 191 | 601 | 190 | **599** | **191** |
| 436.cactusADM | 16 | 864 | 221 | **866** | **221** | 866 | 221 | 16 | 864 | 221 | **866** | **221** | 866 | 221 |
| 437.leslie3d | 16 | **1280** | **117** | 1288 | 117 | 1278 | 118 | 8 | 617 | 122 | **617** | **122** | 616 | 122 |
| 444.namd | 16 | 718 | 179 | **718** | **179** | 715 | 180 | 16 | 701 | 183 | **704** | **182** | 704 | 182 |
| 447.dealII | 16 | 593 | 309 | **593** | **308** | 596 | 307 | 16 | 593 | 309 | **593** | **308** | 596 | 307 |
| 450.soplex | 16 | 1038 | 129 | 1073 | 124 | **1042** | **128** | 8 | 488 | 137 | **486** | **137** | 478 | 140 |
| 453.povray | 16 | **307** | **277** | 309 | 276 | 306 | 278 | 16 | **255** | **333** | 257 | 332 | 254 | 335 |
| 454.calculix | 16 | 558 | 237 | **558** | **237** | 559 | 236 | 16 | 558 | 237 | **558** | **237** | 559 | 236 |
| 459.GemsFDTD | 16 | 1553 | 109 | **1539** | **110** | 1536 | 111 | 16 | 1553 | 109 | **1539** | **110** | 1536 | 111 |
| 465.tonto | 16 | 738 | 213 | **718** | **219** | 704 | 224 | 16 | **717** | **220** | 697 | 226 | 724 | 217 |
| 470.lbm | 16 | 1011 | 217 | **1011** | **217** | 1012 | 217 | 8 | **447** | **246** | 447 | 246 | 448 | 246 |
| 481.wrf | 16 | 922 | 194 | **911** | **196** | 910 | 196 | 16 | 922 | 194 | **911** | **196** | 910 | 196 |
| 482.sphinx3 | 16 | 1678 | 186 | 1684 | 185 | **1683** | **185** | 16 | 1606 | 194 | 1609 | 194 | **1606** | **194** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 7200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
Data Reuse = Disabled (Default = Enabled)
```

## General Notes

```
The Dell PowerEdge T710 and
the Bull NovaScale T860 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge T710 model.
Binaries were compiled on RHEL5.5
```

# Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

# Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

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# Peak Optimization Flags (Continued)

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110426.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110426.xml`


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