



SPEC® CINT2006 Result

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Intel Corporation

SPECint®_rate2006 = 156

Intel DH67BLB3 Motherboard (Intel Core i7-2600)

SPECint_rate_base2006 = 148

CPU2006 license: 13

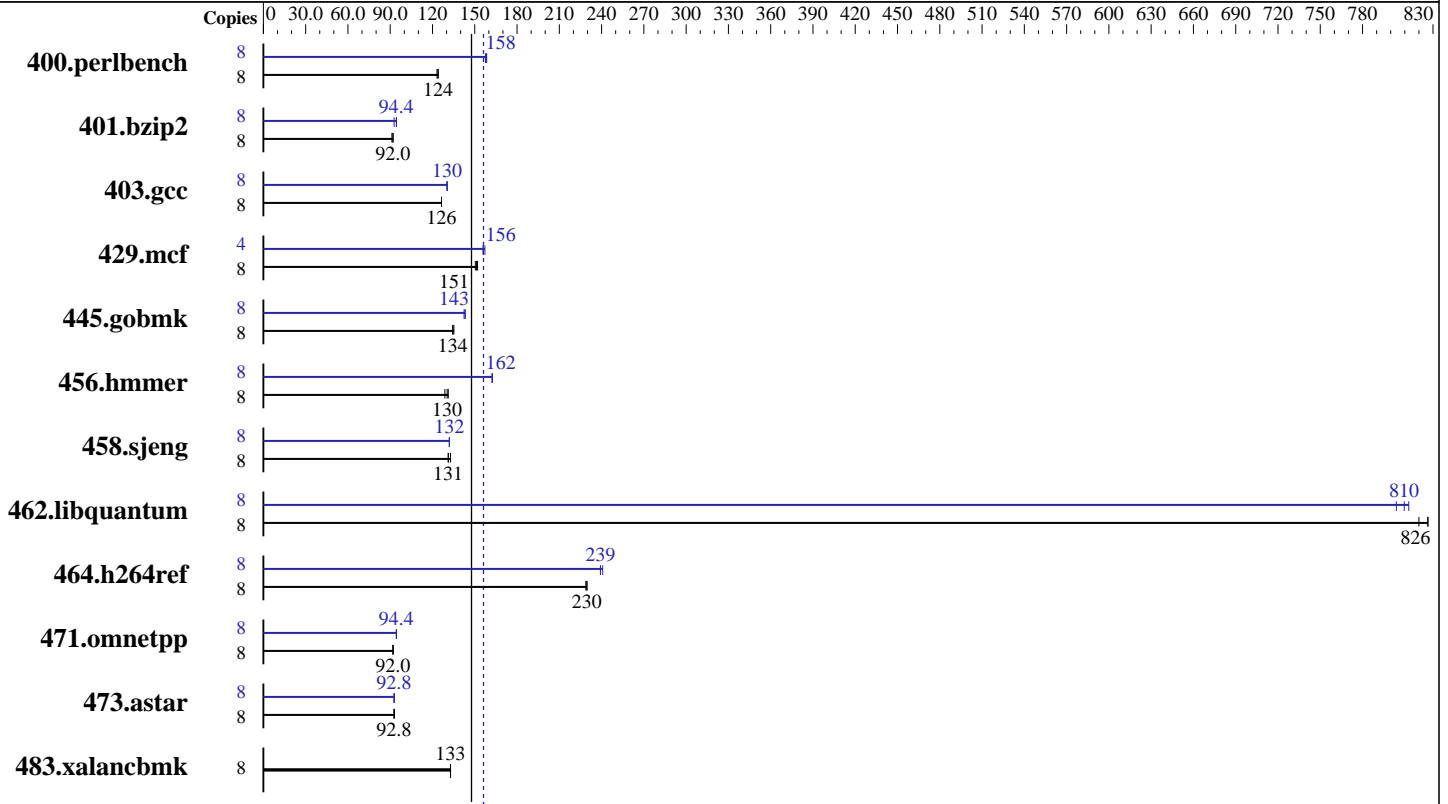
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2011

Hardware Availability: Mar-2011

Software Availability: Apr-2011



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SPECint_rate_base2006 = 148

Hardware

CPU Name: Intel Core i7-2600
 CPU Characteristics: Intel Turbo Boost Technology up to 3.8 GHz
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for IA32 and Intel 64 Version 12.0.3.163 Build 20110217
 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|-------------|------------|------------|------------|-------------|--------|------------|------------|------------|-------------|------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 632 | 124 | 630 | 124 | 632 | 123 | 8 | 494 | 158 | 492 | 158 | 496 | 158 |
| 401.bzip2 | 8 | 844 | 91.2 | 840 | 92.0 | 841 | 92.0 | 8 | 817 | 94.4 | 818 | 94.4 | 831 | 92.8 |
| 403.gcc | 8 | 509 | 126 | 509 | 126 | 509 | 126 | 8 | 494 | 130 | 493 | 130 | 493 | 130 |
| 429.mcf | 8 | 483 | 151 | 479 | 152 | 484 | 150 | 4 | 232 | 157 | 234 | 156 | 234 | 156 |
| 445.gobmk | 8 | 624 | 134 | 624 | 134 | 622 | 135 | 8 | 586 | 143 | 587 | 143 | 589 | 142 |
| 456.hammer | 8 | 570 | 131 | 578 | 129 | 571 | 130 | 8 | 459 | 162 | 459 | 162 | 459 | 162 |
| 458.sjeng | 8 | 738 | 131 | 738 | 131 | 730 | 133 | 8 | 734 | 132 | 733 | 132 | 734 | 132 |
| 462.libquantum | 8 | 201 | 826 | 202 | 820 | 201 | 826 | 8 | 204 | 813 | 205 | 810 | 206 | 804 |
| 464.h264ref | 8 | 772 | 230 | 772 | 230 | 774 | 229 | 8 | 736 | 241 | 740 | 239 | 740 | 239 |
| 471.omnetpp | 8 | 546 | 92.0 | 545 | 92.0 | 546 | 92.0 | 8 | 529 | 94.4 | 529 | 94.4 | 529 | 94.4 |
| 473.astar | 8 | 604 | 92.8 | 605 | 92.8 | 604 | 92.8 | 8 | 606 | 92.8 | 605 | 92.8 | 603 | 92.8 |
| 483.xalancbmk | 8 | 415 | 133 | 415 | 133 | 415 | 133 | 8 | 415 | 133 | 415 | 133 | 415 | 133 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

The start command with the /affinity switch was used to bind processes to cores

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
```

```
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
```

```
483.xalancbmk: -Qoption, cpp, --no_wchar_t_keyword
```



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Base Optimization Flags

C benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icl -Qvc9 -Qstd=c99

456.hmmer: C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe

-link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
-link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

458.sjeng: C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe

-link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
-link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

462.libquantum: C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe

-Qstd=c99
-link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
-link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

C++ benchmarks (except as noted below):

icl -Qvc9

473.astar: C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe

-link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
-link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
-link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64



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Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 456.hmmer: -DSPEC_CPU_P64
 458.sjeng: -DSPEC_CPU_P64
 462.libquantum: -DSPEC_CPU_P64
 464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
 473.astar: -DSPEC_CPU_P64
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
 -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000
 shlW32M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
 -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias /F512000000

403.gcc: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
 -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
 -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qauto-ilp32 -QxAVX(pass 2) -Qprof_gen(pass 1)
 -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch
 /F512000000

458.sjeng: -Qauto-ilp32 -QxAVX(pass 2) -Qprof_gen(pass 1)
 -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4
 /F512000000

462.libquantum: -Qauto-ilp32 -QxSSE4.2 -Qipo -O3 -Qprec-div-
 -Qopt-prefetch /F512000000

464.h264ref: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
 -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
 -O3 -Qprec-div- -Qansi-alias
 -Qopt-ra-region-strategy=block /F512000000 shlW32M.lib
 -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

```
473.astar: -Qauto-ilp32 -QxSSE4.2 -Qipo -O3 -Qprec-div-
          -Qopt-prefetch /F512000000 shlw64M.lib
          -link /FORCE:MULTIPLE
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-win32-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-win32-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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