



# SPEC® CINT2006 Result

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## Supermicro

### SPECint®\_rate2006 = 120

Motherboard X8SIL (Intel Xeon X3460, 2.80 GHz)

### SPECint\_rate\_base2006 = 113

CPU2006 license: 001176

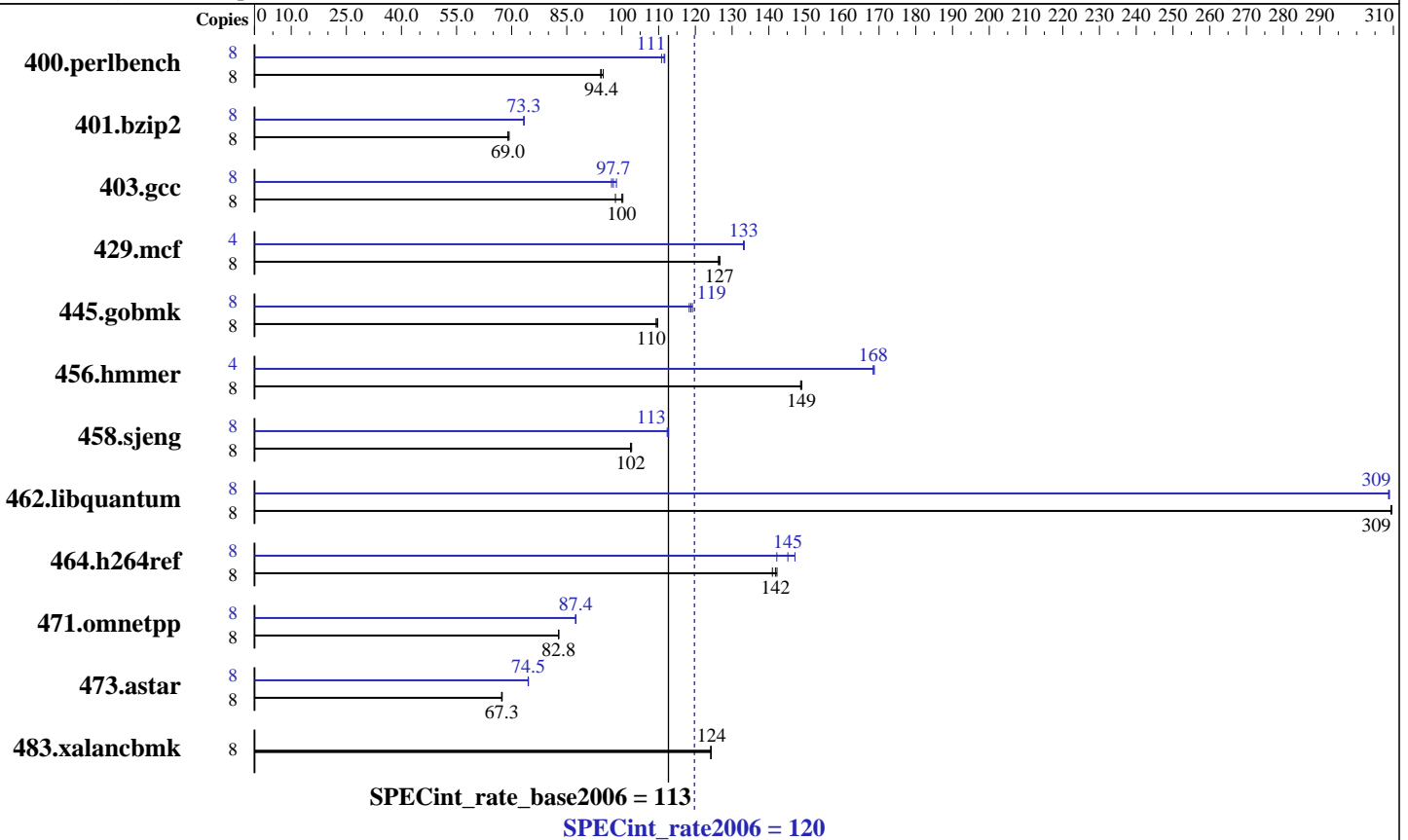
Test date: Mar-2011

Test sponsor: Supermicro

Hardware Availability: Jan-2010

Tested by: Supermicro

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X3460  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	823	94.9	<b>828</b>	<b>94.4</b>	830	94.2	8	<b>701</b>	<b>111</b>	700	112	705	111
401.bzip2	8	1114	69.3	<b>1119</b>	<b>69.0</b>	1119	69.0	8	1052	73.4	1053	73.3	<b>1053</b>	<b>73.3</b>
403.gcc	8	<b>644</b>	<b>100</b>	656	98.2	642	100	8	663	97.2	654	98.5	<b>659</b>	<b>97.7</b>
429.mcf	8	<b>577</b>	<b>127</b>	576	127	578	126	4	274	133	<b>274</b>	<b>133</b>	274	133
445.gobmk	8	<b>766</b>	<b>110</b>	768	109	765	110	8	704	119	709	118	<b>707</b>	<b>119</b>
456.hammer	8	502	149	<b>501</b>	<b>149</b>	501	149	4	222	168	<b>222</b>	<b>168</b>	221	169
458.sjeng	8	943	103	945	102	<b>945</b>	<b>102</b>	8	859	113	<b>860</b>	<b>113</b>	862	112
462.libquantum	8	535	310	536	309	<b>536</b>	<b>309</b>	8	537	309	537	309	<b>537</b>	<b>309</b>
464.h264ref	8	1245	142	1256	141	<b>1248</b>	<b>142</b>	8	1204	147	1245	142	<b>1219</b>	<b>145</b>
471.omnetpp	8	<b>604</b>	<b>82.8</b>	604	82.8	604	82.8	8	572	87.4	571	87.5	<b>572</b>	<b>87.4</b>
473.astar	8	833	67.4	<b>834</b>	<b>67.3</b>	835	67.3	8	<b>754</b>	<b>74.5</b>	754	74.5	753	74.6
483.xalancbmk	8	444	124	444	124	<b>444</b>	<b>124</b>	8	444	124	444	124	<b>444</b>	<b>124</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## Platform Notes

Fan speed set to Full Speed in BIOS Setup.  
As tested, the system used a Supermicro CSE-111T-560CB chassis.  
The system includes a PWS-562-1H power supply, SNK-P0046P heatsink,  
and 4 FAN-0106L4 cooling fans.

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

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## Peak Optimization Flags (Continued)

```

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

```

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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