# SPEC® CFP2006 Result

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## Fujitsu

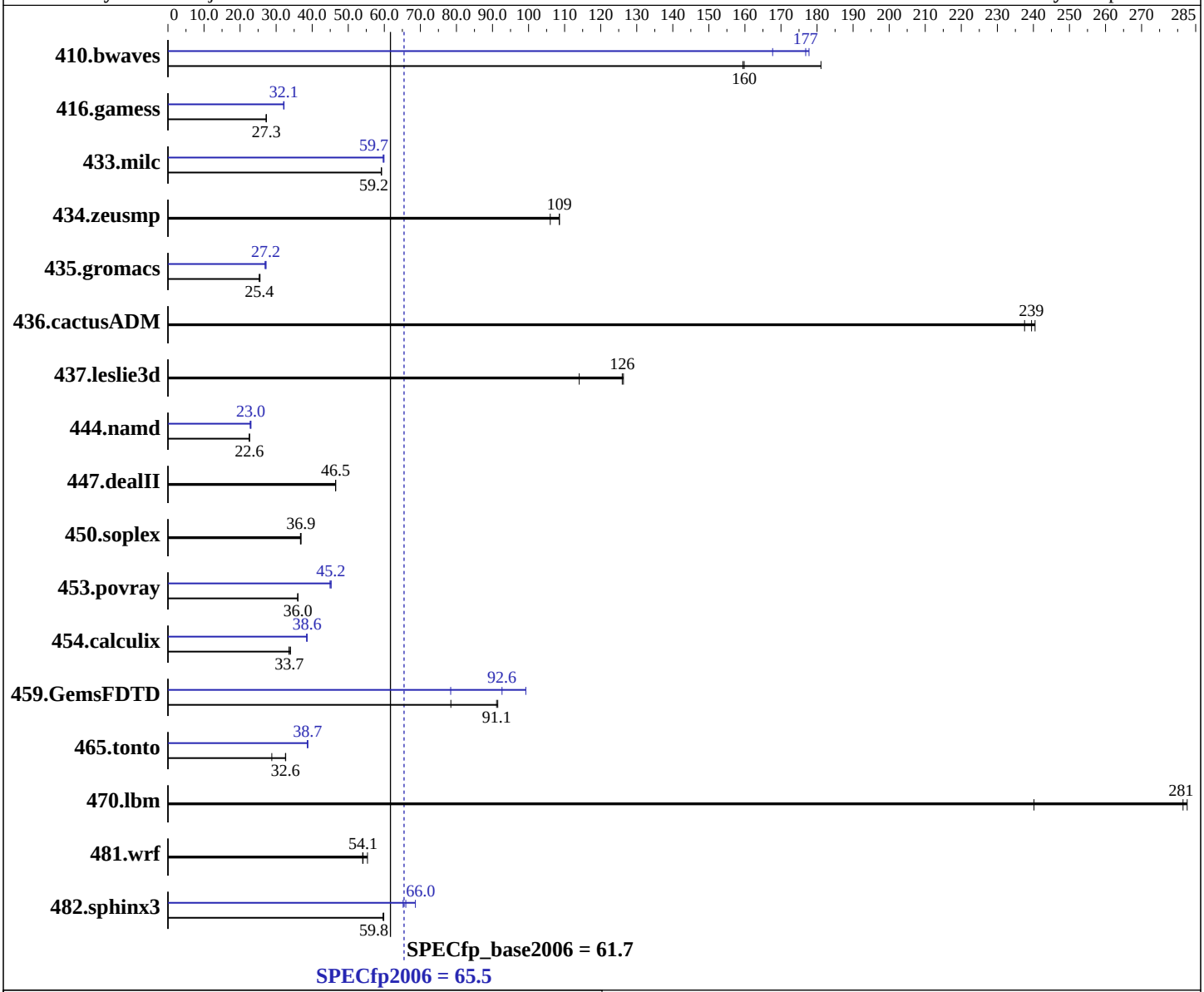
PRIMERGY RX200 S6, Intel Xeon X5687, 3.60 GHz

SPECfp®2006 = 65.5

SPECfp_base2006 = 61.7

**CPU2006 license:** 19
**Test sponsor:** Fujitsu
**Tested by:** Fujitsu

**Test date:** Mar-2011
**Hardware Availability:** Feb-2011
**Software Availability:** Apr-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5687 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.87 GHz |
| CPU MHz: | 3600 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

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## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3 |
| Auto Parallel: | Yes |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 75.0 | 181 | **85.0** | **160** | 85.2 | 159 | **76.8** | **177** | 81.0 | 168 | 76.4 | 178 |
| 416.gamess | **718** | **27.3** | 720 | 27.2 | 717 | 27.3 | **610** | **32.1** | 610 | 32.1 | 608 | 32.2 |
| 433.milc | **155** | **59.2** | 155 | 59.2 | 155 | 59.2 | **154** | **59.7** | 153 | 59.9 | 154 | 59.7 |
| 434.zeusmp | 85.8 | 106 | **83.8** | **109** | 83.8 | 109 | 85.8 | 106 | **83.8** | **109** | 83.8 | 109 |
| 435.gromacs | 282 | 25.3 | 280 | 25.5 | **281** | **25.4** | **263** | **27.2** | 263 | 27.2 | 265 | 26.9 |
| 436.cactusADM | **49.9** | **239** | 49.7 | 240 | 50.3 | 238 | **49.9** | **239** | 49.7 | 240 | 50.3 | 238 |
| 437.leslie3d | 82.4 | 114 | **74.6** | **126** | 74.4 | 126 | 82.4 | 114 | **74.6** | **126** | 74.4 | 126 |
| 444.namd | 355 | 22.6 | 355 | 22.6 | **355** | **22.6** | 349 | 23.0 | **349** | **23.0** | 352 | 22.8 |
| 447.dealII | 246 | 46.5 | **246** | **46.5** | 246 | 46.5 | 246 | 46.5 | **246** | **46.5** | 246 | 46.5 |
| 450.soplex | 226 | 36.9 | **226** | **36.9** | 227 | 36.7 | 226 | 36.9 | **226** | **36.9** | 227 | 36.7 |
| 453.povray | 148 | 36.1 | **148** | **36.0** | 148 | 36.0 | **118** | **45.2** | 118 | 45.2 | 118 | 44.9 |
| 454.calculix | 246 | 33.6 | 243 | 34.0 | **245** | **33.7** | 214 | 38.6 | **214** | **38.6** | 215 | 38.4 |
| 459.GemsFDTD | 116 | 91.5 | **116** | **91.1** | 135 | 78.5 | 107 | 99.2 | **115** | **92.6** | 135 | 78.4 |
| 465.tonto | 301 | 32.7 | 342 | 28.8 | **302** | **32.6** | **254** | **38.7** | 254 | 38.8 | 255 | 38.6 |
| 470.lbm | 48.6 | 283 | 57.2 | 240 | **48.8** | **281** | 48.6 | 283 | 57.2 | 240 | **48.8** | **281** |
| 481.wrf | 207 | 54.0 | **206** | **54.1** | 202 | 55.3 | 207 | 54.0 | **206** | **54.1** | 202 | 55.3 |
| 482.sphinx3 | **326** | **59.8** | 326 | 59.9 | 327 | 59.6 | 284 | 68.6 | 299 | 65.2 | **295** | **66.0** |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
BIOS configuration:
Data Reuse Optimization = Disable
Intel HT Technology = Disable
```

## General Notes

```
OMP_NUM_THREADS set to number of cores
For information about Fujitsu please visit: http://www.fujitsu.com
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5
```

## Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

## Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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## Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

## Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

`Same as Base Portability Flags`

## Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-static -auto-ilp32 -ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel`

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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