



SPEC[®] CINT2006 Result

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Acer Incorporated

SPECint[®]_rate2006 = 390

Gateway GR380 F1 (Intel Xeon X5675)

SPECint_rate_base2006 = 364

CPU2006 license: 97

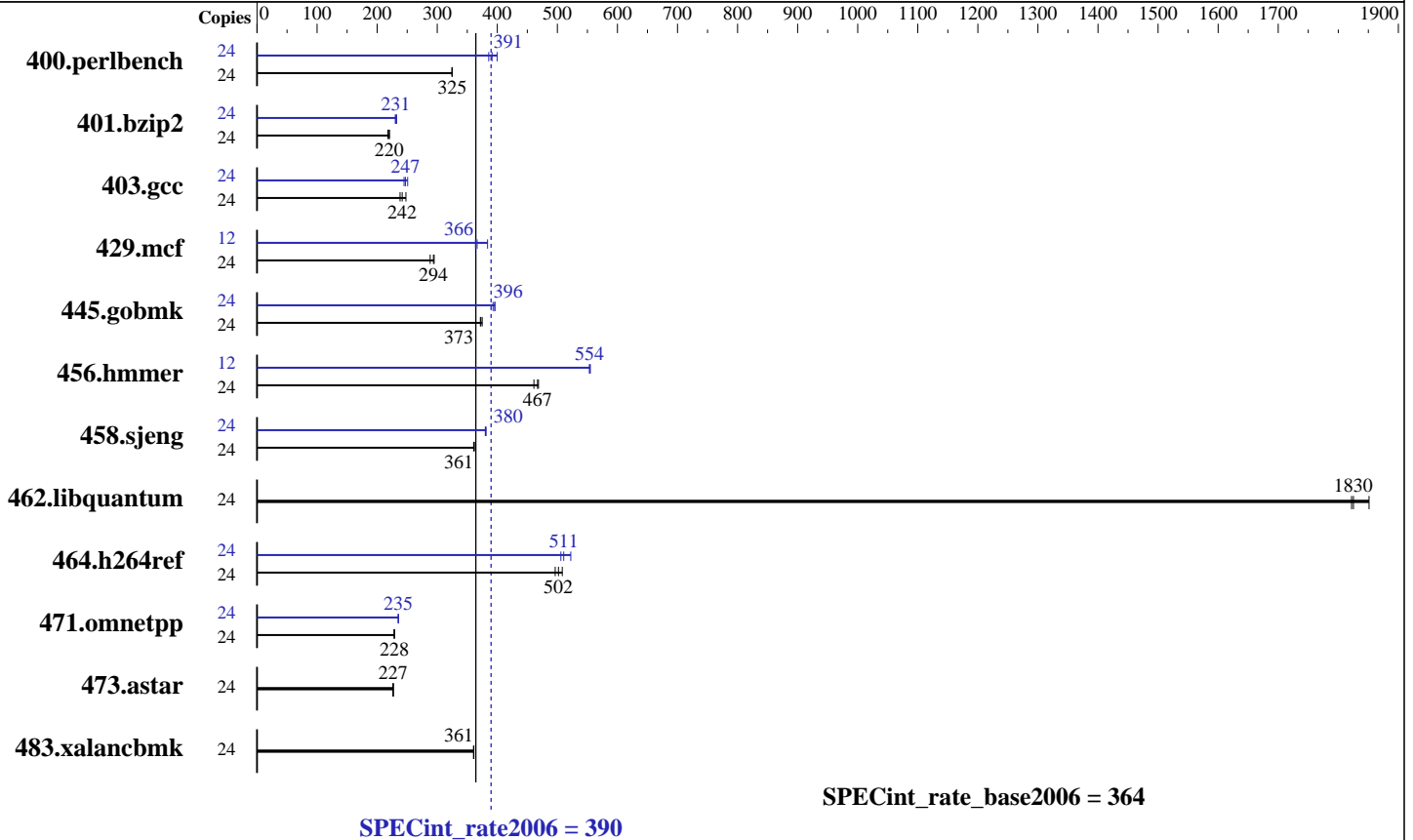
Test date: Mar-2011

Test sponsor: Acer Incorporated

Hardware Availability: Feb-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5675
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9,ECC)
 Disk Subsystem: 1 x 300 GB SATA, 10000 RPM SATA HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|------------|-------------|-------------|------------|------------|--------|-------------|------------|-------------|-------------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 723 | 324 | 721 | 325 | <u>722</u> | <u>325</u> | 24 | <u>599</u> | <u>391</u> | 608 | 386 | 587 | 400 |
| 401.bzip2 | 24 | 1063 | 218 | <u>1053</u> | <u>220</u> | 1051 | 220 | 24 | <u>1002</u> | <u>231</u> | 1006 | 230 | 996 | 232 |
| 403.gcc | 24 | 779 | 248 | 811 | 238 | <u>799</u> | <u>242</u> | 24 | 770 | 251 | <u>782</u> | <u>247</u> | 789 | 245 |
| 429.mcf | 24 | 759 | 288 | <u>745</u> | <u>294</u> | 742 | 295 | 12 | <u>299</u> | <u>366</u> | 301 | 364 | 285 | 384 |
| 445.gobmk | 24 | 672 | 375 | <u>676</u> | <u>373</u> | 677 | 372 | 24 | <u>636</u> | <u>396</u> | 636 | 396 | 641 | 393 |
| 456.hammer | 24 | <u>480</u> | <u>467</u> | 486 | 461 | 478 | 469 | 12 | <u>202</u> | <u>554</u> | 202 | 555 | 202 | 553 |
| 458.sjeng | 24 | 805 | 361 | 803 | 362 | <u>804</u> | <u>361</u> | 24 | 761 | 381 | <u>763</u> | <u>380</u> | 764 | 380 |
| 462.libquantum | 24 | 269 | 1850 | <u>272</u> | <u>1830</u> | 273 | 1820 | 24 | 269 | 1850 | <u>272</u> | <u>1830</u> | 273 | 1820 |
| 464.h264ref | 24 | 1045 | 508 | <u>1058</u> | <u>502</u> | 1070 | 496 | 24 | 1017 | 522 | <u>1040</u> | <u>511</u> | 1050 | 506 |
| 471.omnetpp | 24 | <u>656</u> | <u>228</u> | 656 | 229 | 657 | 228 | 24 | <u>638</u> | <u>235</u> | 637 | 235 | 638 | 235 |
| 473.astar | 24 | <u>743</u> | <u>227</u> | 744 | 226 | 743 | 227 | 24 | <u>743</u> | <u>227</u> | 744 | 226 | 743 | 227 |
| 483.xalancbmk | 24 | 460 | 360 | <u>459</u> | <u>361</u> | 459 | 361 | 24 | 460 | 360 | <u>459</u> | <u>361</u> | 459 | 361 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

Platform Notes

BIOS Settings:
Fan speed = full speed (Default = Balanced)
Data Reuse = Disabled (Default = Enabled)

General Notes

Binaries compiled on RHEL5.5
This result was measured on the Gateway GR380 F1.
The Acer AR380 F1 is electronically equivalent

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page



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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page



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Peak Optimization Flags (Continued)

471.omnetpp (continued):
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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Page 5