SPEC® CINT2006 Result
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Acer Incorporated

SPECint®_rate2006 = 348

Gateway GT350 F1 (Intel Xeon X5670)

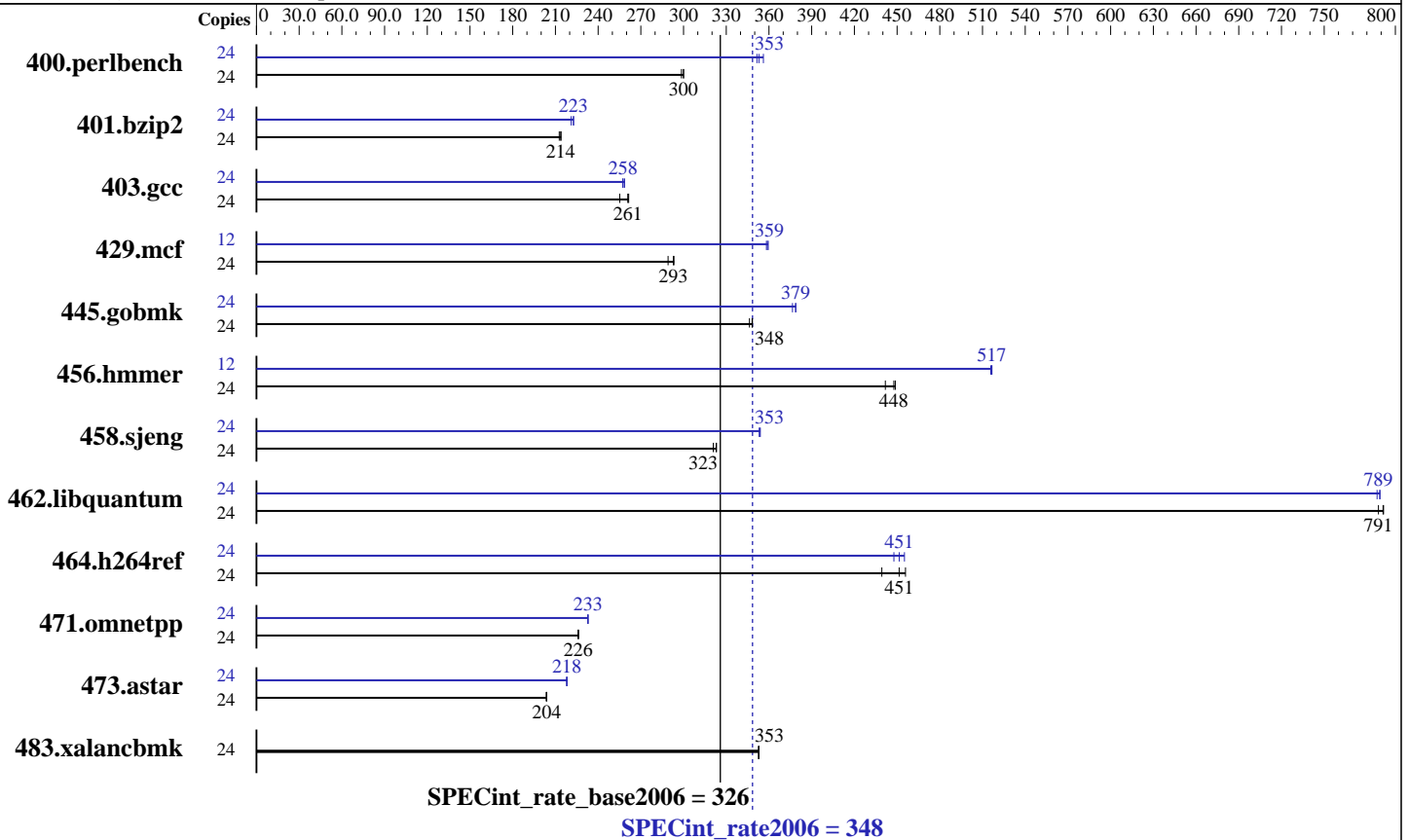SPECint_rate_base2006 = 326

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Dec-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010

Benchmark	Copies	Value
400.perlbench	24 / 24	353 / 300
401.bzip2	24 / 24	223 / 214
403.gcc	24 / 24	258 / 261
429.mcf	12 / 24	359 / 293
445.gobmk	24 / 24	379 / 348
456.hmmer	12 / 24	517 / 448
458.sjeng	24 / 24	353 / 323
462.libquantum	24 / 24	789 / 791
464.h264ref	24 / 24	451 / 451
471.omnetpp	24 / 24	233 / 226
473.astar	24 / 24	218 / 204
483.xalancbmk	24	353

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Hardware

CPU Name:	Intel Xeon X5670
CPU Characteristics:	Intel Turbo Boost Technology up to 3.33 GHz
CPU MHz:	2993
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 300 GB SATA, 10000RPM SATA HDD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	**782**	**300**	786	299	782	300		24	658	356	667	352	**664**	**353**
401.bzip2	24	1089	213	**1084**	**214**	1082	214		24	1048	221	**1040**	**223**	1040	223
403.gcc	24	**741**	**261**	757	255	739	261		24	747	259	751	257	**750**	**258**
429.mcf	24	757	289	746	293	**747**	**293**		12	306	358	304	360	**305**	**359**
445.gobmk	24	**723**	**348**	723	348	727	346		24	**665**	**379**	669	377	665	379
456.hmmer	24	**500**	**448**	507	442	499	449		12	217	517	**217**	**517**	217	516
458.sjeng	24	905	321	899	323	**899**	**323**		24	822	353	821	354	**822**	**353**
462.libquantum	24	631	788	**628**	**791**	628	792		24	632	787	630	789	**630**	**789**
464.h264ref	24	1165	456	**1176**	**451**	1209	439		24	1167	455	1186	448	**1176**	**451**
471.omnetpp	24	663	226	664	226	**664**	**226**		24	644	233	644	233	**644**	**233**
473.astar	24	826	204	828	203	**827**	**204**		24	773	218	772	218	**773**	**218**
483.xalancbmk	24	**469**	**353**	469	353	470	353		24	**469**	**353**	469	353	470	353

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
This result was measured on the Gateway GT350 F1.
The Acer AT350 F1 is electronically equivalent.

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

462.libquantum: `icc -m64`

C++ benchmarks (except as noted below):
```
icpc -m32
```

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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Peak Optimization Flags

C benchmarks:

   400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
         `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
         `-prof-use(pass 2) -ansi-alias`

     401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
         `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
         `-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

       403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

       429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

   445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
         `-ipo -no-prec-div -ansi-alias`

   456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
         `-ansi-alias -auto-ilp32`

    458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
         `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
         `-prof-use(pass 2) -unroll4 -auto-ilp32`

 462.libquantum: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32`
         `-opt-prefetch`

  464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
         `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
         `-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

  471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
         `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
         `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
         `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

    473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
         `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
         `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
         `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

 483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revH.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revH.xml`

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