



# SPEC<sup>®</sup> CFP2006 Result

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## Hewlett-Packard Company

**SPECfp<sup>®</sup>2006 = 60.3**

ProLiant DL380 G7  
(3.07 GHz, Intel Xeon X5675)

**SPECfp\_base2006 = 56.9**

CPU2006 license: 3

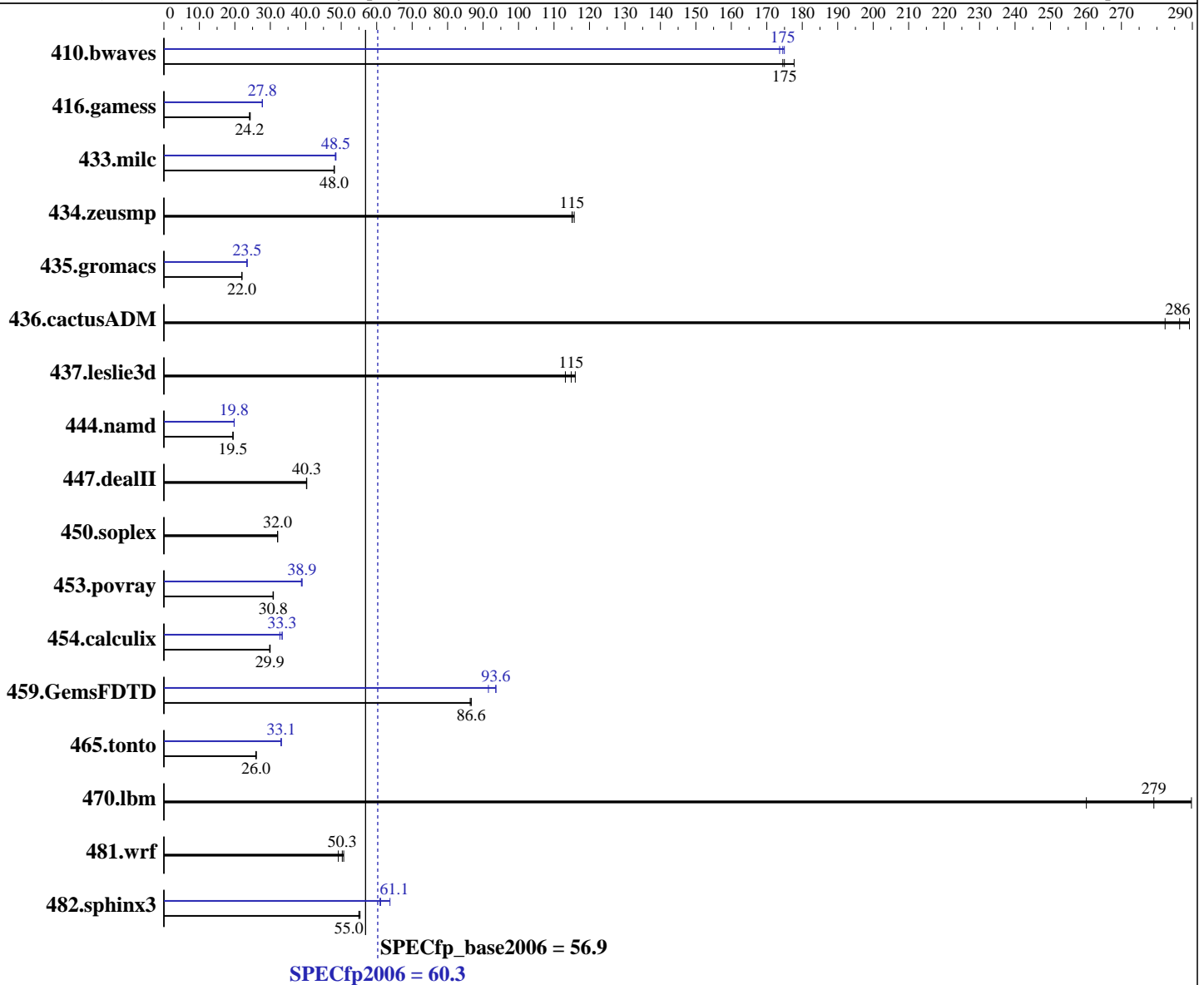
Test date: Feb-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Feb-2011

Tested by: Hewlett-Packard Company

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon X5675  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 3066  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 300 GB 10 K SAS  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	76.5	178	77.9	175	<u>77.7</u>	<u>175</u>	77.7	175	78.3	174	<u>77.9</u>	<u>175</u>
416.gamess	811	24.1	<u>810</u>	<u>24.2</u>	804	24.4	<u>705</u>	<u>27.8</u>	706	27.7	705	27.8
433.milc	191	48.0	191	48.1	<u>191</u>	<u>48.0</u>	190	48.3	<u>189</u>	<u>48.5</u>	189	48.5
434.zeusmp	<u>79.0</u>	<u>115</u>	79.1	115	78.7	116	<u>79.0</u>	<u>115</u>	79.1	115	78.7	116
435.gromacs	325	22.0	<u>325</u>	<u>22.0</u>	324	22.0	304	23.5	305	23.4	<u>304</u>	<u>23.5</u>
436.cactusADM	42.3	282	<u>41.7</u>	<u>286</u>	41.3	289	42.3	282	<u>41.7</u>	<u>286</u>	41.3	289
437.leslie3d	83.0	113	81.0	116	<u>81.8</u>	<u>115</u>	83.0	113	81.0	116	<u>81.8</u>	<u>115</u>
444.namd	412	19.5	412	19.5	<u>412</u>	<u>19.5</u>	<u>405</u>	<u>19.8</u>	405	19.8	405	19.8
447.dealII	<u>284</u>	<u>40.3</u>	284	40.3	285	40.2	<u>284</u>	<u>40.3</u>	284	40.3	285	40.2
450.soplex	<u>260</u>	<u>32.0</u>	260	32.1	260	32.0	<u>260</u>	<u>32.0</u>	260	32.1	260	32.0
453.povray	<u>173</u>	<u>30.8</u>	172	30.9	173	30.8	<u>137</u>	<u>38.9</u>	137	38.9	137	38.8
454.calculix	<u>276</u>	<u>29.9</u>	277	29.8	275	30.0	252	32.7	247	33.3	<u>247</u>	<u>33.3</u>
459.GemsFDTD	123	86.4	122	86.6	<u>122</u>	<u>86.6</u>	<u>113</u>	<u>93.6</u>	116	91.5	113	93.6
465.tonto	<u>379</u>	<u>26.0</u>	377	26.1	380	25.9	297	33.1	<u>298</u>	<u>33.1</u>	298	33.0
470.lbm	52.8	260	<u>49.2</u>	<u>279</u>	47.4	290	52.8	260	<u>49.2</u>	<u>279</u>	47.4	290
481.wrf	<u>222</u>	<u>50.3</u>	227	49.2	220	50.7	<u>222</u>	<u>50.3</u>	227	49.2	220	50.7
482.sphinx3	355	55.0	352	55.3	<u>354</u>	<u>55.0</u>	<u>319</u>	<u>61.1</u>	306	63.8	320	60.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS configuration:  
Intel HyperThreading Options set to Disabled  
HP Power Profile set to Maximum Performance  
Thermal Configuration set to Increased Cooling  
Data Reuse set to Disabled



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## General Notes

Binaries were compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

C++ benchmarks:

444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32`

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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110316.xml>



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