SPEC® CFP2006 Result

Bull SAS

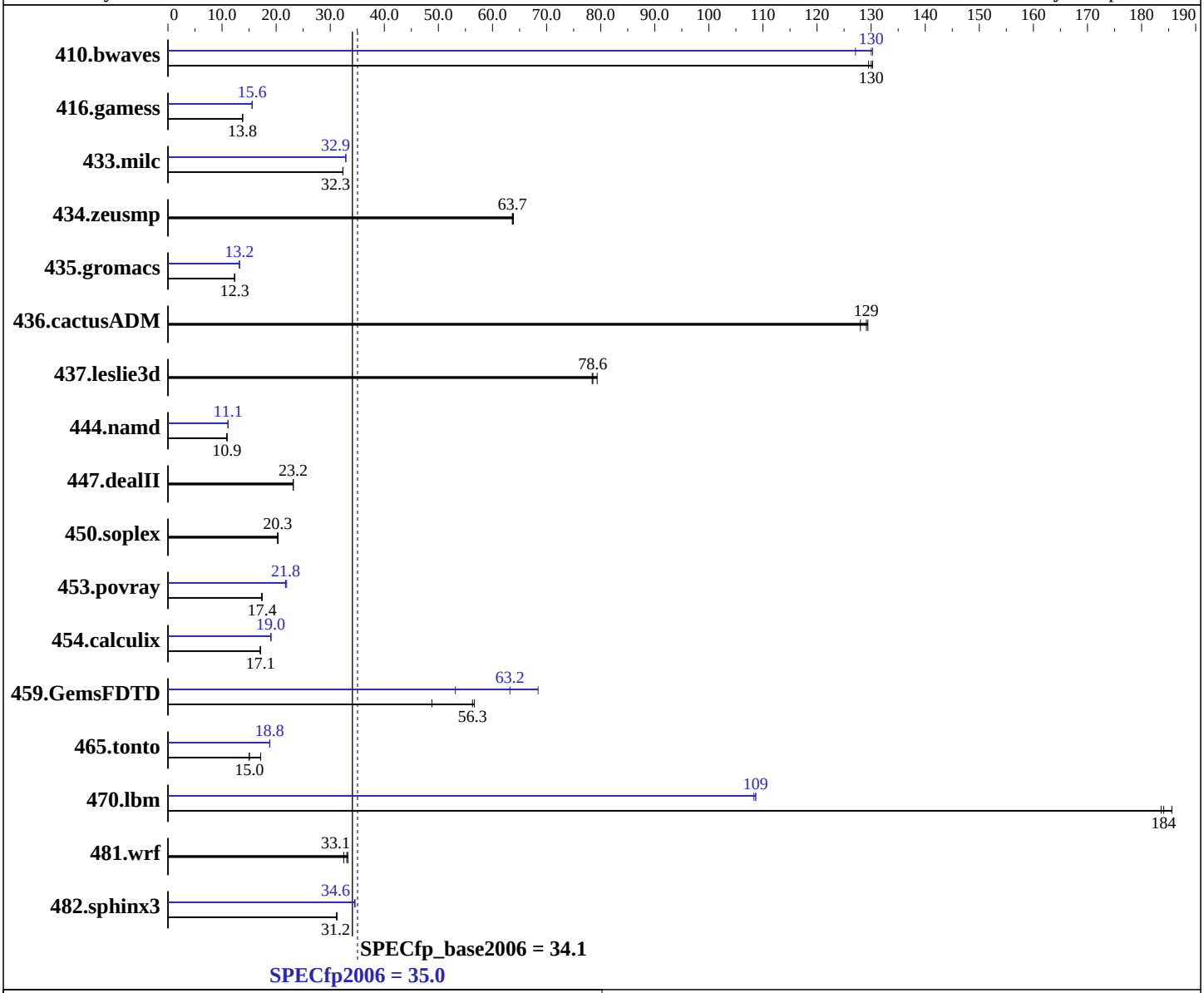
BL265 (Intel Xeon L5609, 1.86 GHz)

SPECfp®2006 = 35.0

SPECfp_base2006 = 34.1

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Jan-2011
Hardware Availability: Mar-2010
Software Availability: Apr-2011



Hardware

CPU Name:	Intel Xeon L5609
CPU Characteristics:	
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Upadte 3
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)
Disk Subsystem: 2 x 50 GB SATA, SSD
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	105	130	**105**	**130**	104	130	104	130	107	127	**105**	**130**
416.gamess	**1419**	**13.8**	1419	13.8	1419	13.8	**1257**	**15.6**	1259	15.5	1257	15.6
433.milc	284	32.3	284	32.4	**284**	**32.3**	279	32.9	**279**	**32.9**	279	32.9
434.zeusmp	142	63.9	**143**	**63.7**	143	63.7	142	63.9	**143**	**63.7**	143	63.7
435.gromacs	579	12.3	**579**	**12.3**	578	12.3	541	13.2	540	13.2	**541**	**13.2**
436.cactusADM	**92.5**	**129**	93.3	128	92.3	129	**92.5**	**129**	93.3	128	92.3	129
437.leslie3d	120	78.4	118	79.4	**120**	**78.6**	120	78.4	118	79.4	**120**	**78.6**
444.namd	736	10.9	734	10.9	**735**	**10.9**	722	11.1	**722**	**11.1**	723	11.1
447.dealII	494	23.2	**494**	**23.2**	494	23.2	494	23.2	**494**	**23.2**	494	23.2
450.soplex	**411**	**20.3**	410	20.3	411	20.3	**411**	**20.3**	410	20.3	411	20.3
453.povray	306	17.4	**306**	**17.4**	307	17.3	242	21.9	245	21.7	**244**	**21.8**
454.calculix	484	17.0	482	17.1	**482**	**17.1**	**433**	**19.0**	433	19.0	433	19.0
459.GemsFDTD	**188**	**56.3**	217	48.8	187	56.7	155	68.4	200	53.1	**168**	**63.2**
465.tonto	**654**	**15.0**	574	17.1	654	15.0	523	18.8	**523**	**18.8**	522	18.8
470.lbm	**74.6**	**184**	74.8	184	74.0	186	127	108	126	109	**126**	**109**
481.wrf	344	32.5	336	33.3	**338**	**33.1**	344	32.5	336	33.3	**338**	**33.1**
482.sphinx3	**625**	**31.2**	625	31.2	624	31.2	**564**	**34.6**	564	34.6	565	34.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Hugepages was enabled with the following:
  'nodev   /mnt/hugepages   hugetlbfs  defaults   0 0' added to /etc/fstab
   echo 900 > /proc/sys/vm/nr_hugepages
   export HUGETLB_MORECORE=yes
   export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
Power C-states enabled in BIOS
Demand Scrub disabled in BIOS
```

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias
```

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -parallel -ansi-alias -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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