SPEC® CFP2006 Result

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Oracle Corporation

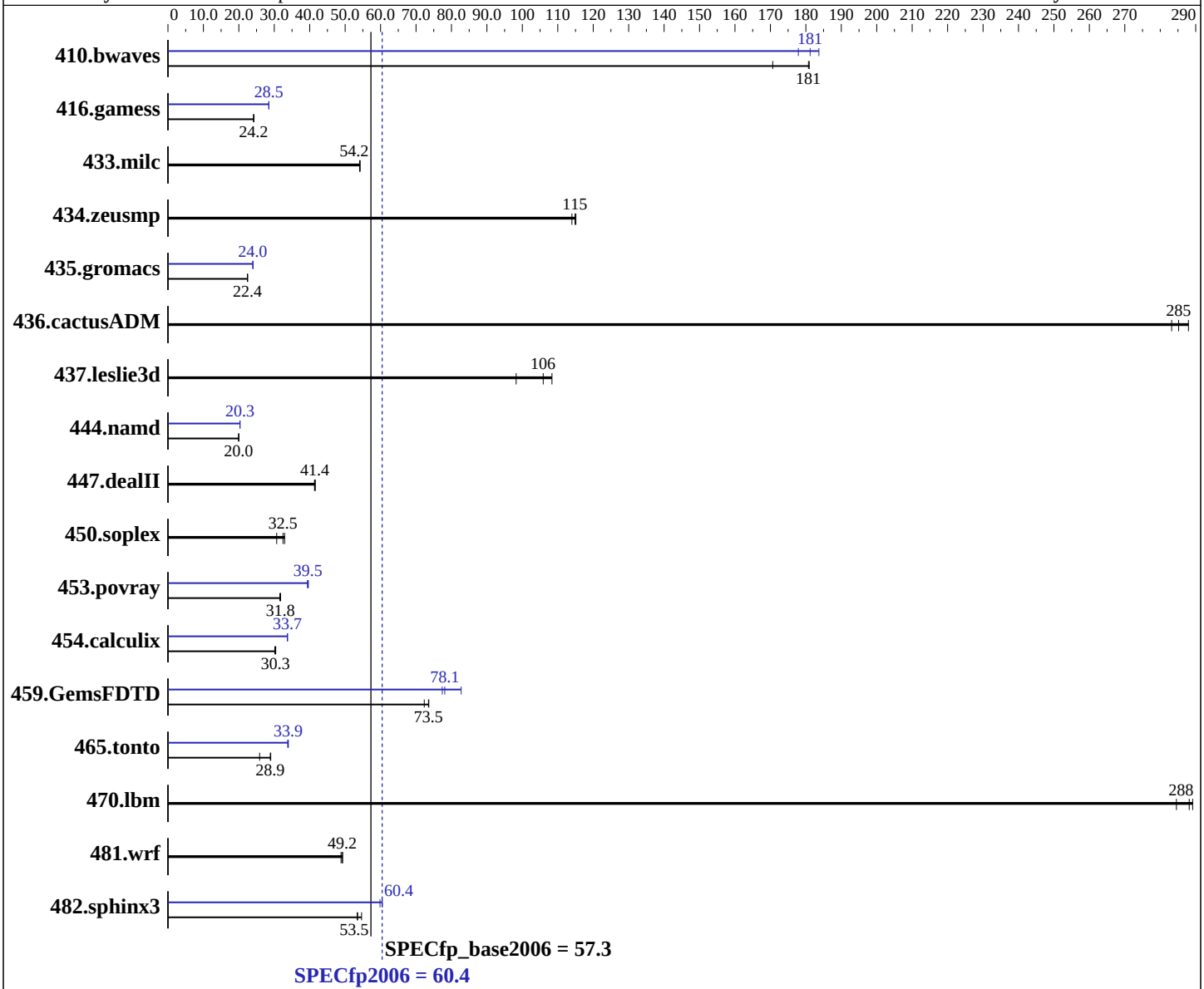
Sun Blade X6275 M2 Server Module (Intel Xeon X5675 3.06 GHz)

SPECfp®2006 = 60.4

SPECfp_base2006 = 57.3

CPU2006 license: 6
Test sponsor: Oracle Corporation
Tested by: Oracle Corporation

Test date: Jan-2011
Hardware Availability: Mar-2011
Software Availability: Nov-2010



Hardware

CPU Name:	Intel Xeon X5675
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	3067
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1 or 2 chips per Sun Blade X6275 M2 node
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software

Operating System:	Oracle Linux 5.5 kernel 2.6.18-194.el5
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12 Alpha Build 20110105
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, per node)
Disk Subsystem: 1 x 24 GB, SAS, Sun FlashDisk
Other Hardware: None

Peak Pointers: 64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	79.6	171	**75.2**	**181**	75.1	181	74.0	184	**75.0**	**181**	76.4	178
416.gamess	**810**	**24.2**	807	24.3	810	24.2	688	28.4	**687**	**28.5**	687	28.5
433.milc	170	54.1	169	54.2	**170**	**54.2**	170	54.1	169	54.2	**170**	**54.2**
434.zeusmp	**79.2**	**115**	79.1	115	79.8	114	**79.2**	**115**	79.1	115	79.8	114
435.gromacs	317	22.5	318	22.4	**318**	**22.4**	297	24.0	**298**	**24.0**	298	24.0
436.cactusADM	41.5	288	42.2	283	**41.9**	**285**	41.5	288	42.2	283	**41.9**	**285**
437.leslie3d	95.7	98.3	**88.8**	**106**	86.7	108	95.7	98.3	**88.8**	**106**	86.7	108
444.namd	402	20.0	**402**	**20.0**	401	20.0	394	20.3	395	20.3	**395**	**20.3**
447.dealII	275	41.6	277	41.4	**276**	**41.4**	275	41.6	277	41.4	**276**	**41.4**
450.soplex	272	30.7	**257**	**32.5**	253	32.9	272	30.7	**257**	**32.5**	253	32.9
453.povray	168	31.6	**168**	**31.8**	167	31.8	134	39.6	**135**	**39.5**	135	39.3
454.calculix	271	30.4	274	30.1	**272**	**30.3**	**245**	**33.7**	245	33.7	245	33.7
459.GemsFDTD	144	73.6	**144**	**73.5**	147	72.3	128	82.7	137	77.4	**136**	**78.1**
465.tonto	380	25.9	**341**	**28.9**	339	29.0	290	34.0	**290**	**33.9**	291	33.8
470.lbm	**47.7**	**288**	48.3	285	47.5	289	**47.7**	**288**	48.3	285	47.5	289
481.wrf	227	49.3	229	48.8	**227**	**49.2**	227	49.3	229	48.8	**227**	**49.2**
482.sphinx3	365	53.4	**364**	**53.5**	357	54.6	322	60.5	326	59.8	**323**	**60.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
Load Default BIOS Settings and then change the following
  Hardware Prefetch Enabled
  Adjacent Cache Line Prefetch Enabled
  L1 Data Prefetch Enabled
```

General Notes

```
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M
 Though binaries were linked with hugepages, this result did not use hugepages.
Binaries were compiled on RHEL5.5 with Binutils binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
    435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
    437.leslie3d: -DSPEC_CPU_LP64
     444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
    454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
      465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias
```

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch -ansi-alias`

Peak Compiler Invocation

C benchmarks:
`icc -m64`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias -parallel`

C++ benchmarks:

444.namd: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-fno-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32 -ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.html
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.xml
http://www.spec.org/cpu2006/flags/Oracle-platform-x86_64.20101027.xml

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For other inquiries, please contact webmaster@spec.org.

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