# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

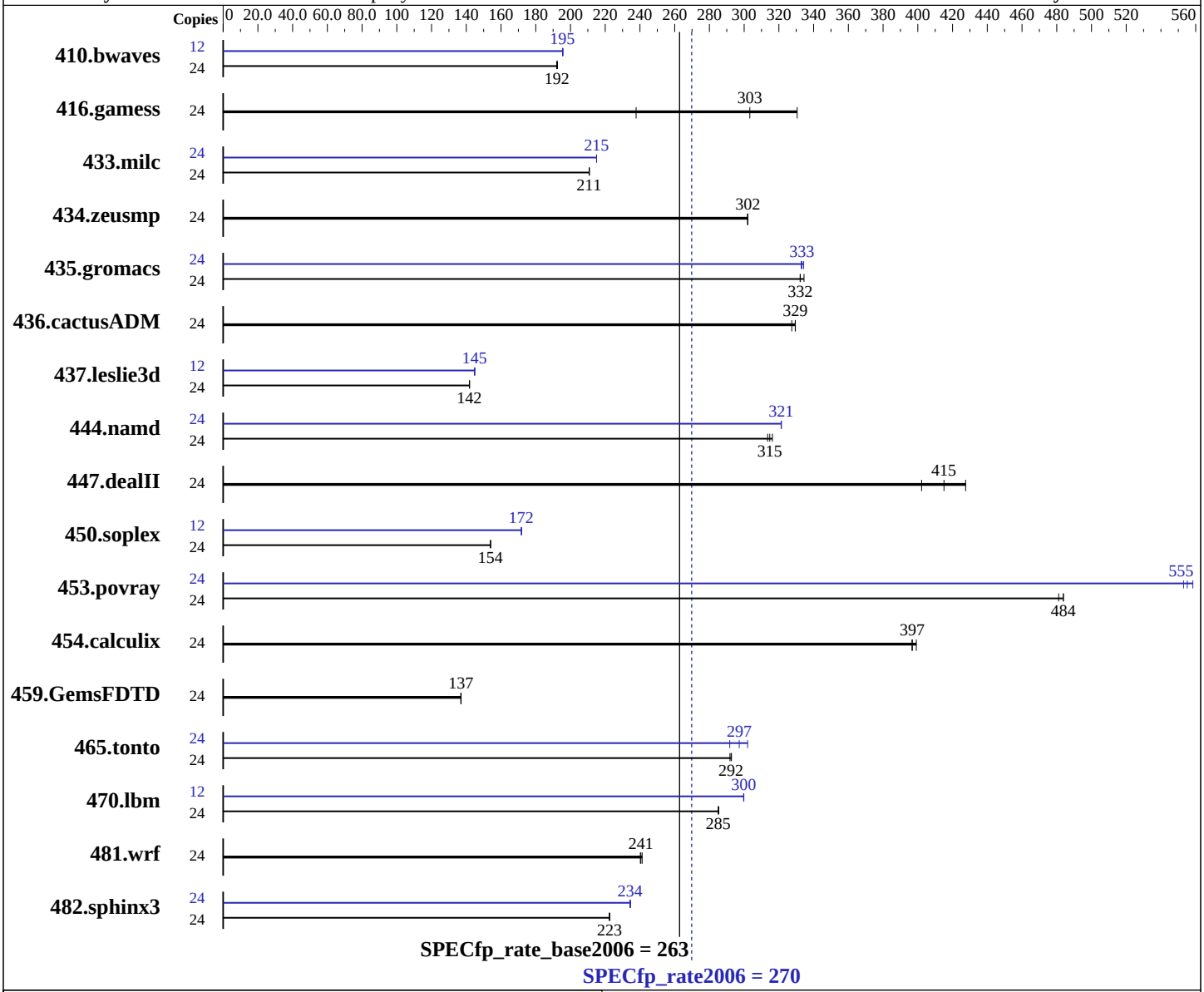
ProLiant DL380 G7
(3.47 GHz, Intel Xeon X5690)

SPECfp®_rate2006 = 270

SPECfp_rate_base2006 = 263

**CPU2006 license:** 3
**Test sponsor:** Hewlett-Packard Company
**Tested by:** Hewlett-Packard Company

**Test date:** Jan-2011
**Hardware Availability:** Feb-2011
**Software Availability:** Dec-2010




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon X5690 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.73 GHz |
| CPU MHz: | 3467 |
| FPU: | Integrated |
| CPU(s) enabled: | 12 cores, 2 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

Continued on next page

### Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant DL380 G7
(3.47 GHz, Intel Xeon X5690)

SPECfp_rate2006 = 270

SPECfp_rate_base2006 = 263

**CPU2006 license:** 3
**Test sponsor:** Hewlett-Packard Company
**Tested by:** Hewlett-Packard Company

**Test date:** Jan-2011
**Hardware Availability:** Feb-2011
**Software Availability:** Dec-2010

L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 300 GB 10 K SAS
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 24 | 1694 | 193 | **1697** | **192** | 1699 | 192 | 12 | **834** | **195** | 833 | 196 | 834 | 195 |
| 416.gamess | 24 | 1422 | 331 | **1550** | **303** | 1976 | 238 | 24 | 1422 | 331 | **1550** | **303** | 1976 | 238 |
| 433.milc | 24 | 1045 | 211 | 1045 | 211 | **1045** | **211** | 24 | 1025 | 215 | 1025 | 215 | **1025** | **215** |
| 434.zeusmp | 24 | **723** | **302** | 723 | 302 | 723 | 302 | 24 | **723** | **302** | 723 | 302 | 723 | 302 |
| 435.gromacs | 24 | 516 | 332 | 512 | 334 | **516** | **332** | 24 | **514** | **333** | 513 | 334 | 515 | 333 |
| 436.cactusADM | 24 | 870 | 329 | **871** | **329** | 876 | 327 | 24 | 870 | 329 | **871** | **329** | 876 | 327 |
| 437.leslie3d | 24 | **1591** | **142** | 1591 | 142 | 1589 | 142 | 12 | **779** | **145** | 778 | 145 | 779 | 145 |
| 444.namd | 24 | **612** | **315** | 609 | 316 | 614 | 314 | 24 | 599 | 321 | **599** | **321** | 599 | 321 |
| 447.dealII | 24 | 642 | 428 | 683 | 402 | **661** | **415** | 24 | 642 | 428 | 683 | 402 | **661** | **415** |
| 450.soplex | 24 | 1300 | 154 | 1300 | 154 | **1300** | **154** | 12 | 582 | 172 | **583** | **172** | 583 | 172 |
| 453.povray | 24 | 264 | 484 | 265 | 481 | **264** | **484** | 24 | 229 | 558 | **230** | **555** | 231 | 553 |
| 454.calculix | 24 | 499 | 397 | **499** | **397** | 496 | 399 | 24 | 499 | 397 | **499** | **397** | 496 | 399 |
| 459.GemsFDTD | 24 | 1861 | 137 | 1858 | 137 | **1859** | **137** | 24 | 1861 | 137 | 1858 | 137 | **1859** | **137** |
| 465.tonto | 24 | 807 | 293 | 809 | 292 | **808** | **292** | 24 | **795** | **297** | 782 | 302 | 810 | 292 |
| 470.lbm | 24 | 1157 | 285 | 1155 | 285 | **1156** | **285** | 12 | **550** | **300** | 550 | 300 | 550 | 300 |
| 481.wrf | 24 | 1111 | 241 | 1116 | 240 | **1115** | **241** | 24 | 1111 | 241 | 1116 | 240 | **1115** | **241** |
| 482.sphinx3 | 24 | 2101 | 223 | 2104 | 222 | **2102** | **223** | 24 | 1994 | 235 | **1996** | **234** | 1997 | 234 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
numactl was used to bind copies to the cores
```

## Operating System Notes

```
'nodev   /mnt/hugepages   hugetlbfs   defaults   0 0' added to /etc/fstab
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 10800 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
BIOS configuration:
 HP Power Profile set to Maximum Performance
 Thermal Configuration set to Increased Cooling
 Data Reuse set to Disabled
```

## General Notes

```
Binaries were compiled on RHEL5.5 with Binutils binutils-2.17.50.0.6-14.el5
```

# Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

# Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
`icpc -m64`

450.soplex: `icpc -m32 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

453.povray: `icpc -m64 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Fortran benchmarks (except as noted below):
`ifort -m64`

437.leslie3d: `ifort -m64 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

465.tonto: `ifort -m64 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`

Continued on next page

## Peak Portability Flags (Continued)

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

Continued on next page

# Peak Optimization Flags (Continued)

437.leslie3d: `-xSSE4.2 -ipo -O3 -no-prec-div`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-unroll4 -auto -inline-calloc -opt-malloc-options=3`

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch -static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.02.html`
`http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110216.00.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.02.xml`
`http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20110216.00.xml`


SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 16:14:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 March 2011.