



SPEC[®] CFP2006 Result

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IBM Corporation

SPECfp[®]2006 = **63.4**

IBM BladeCenter HS22V (Intel Xeon X5690)

SPECfp_base2006 = **59.1**

CPU2006 license: 11

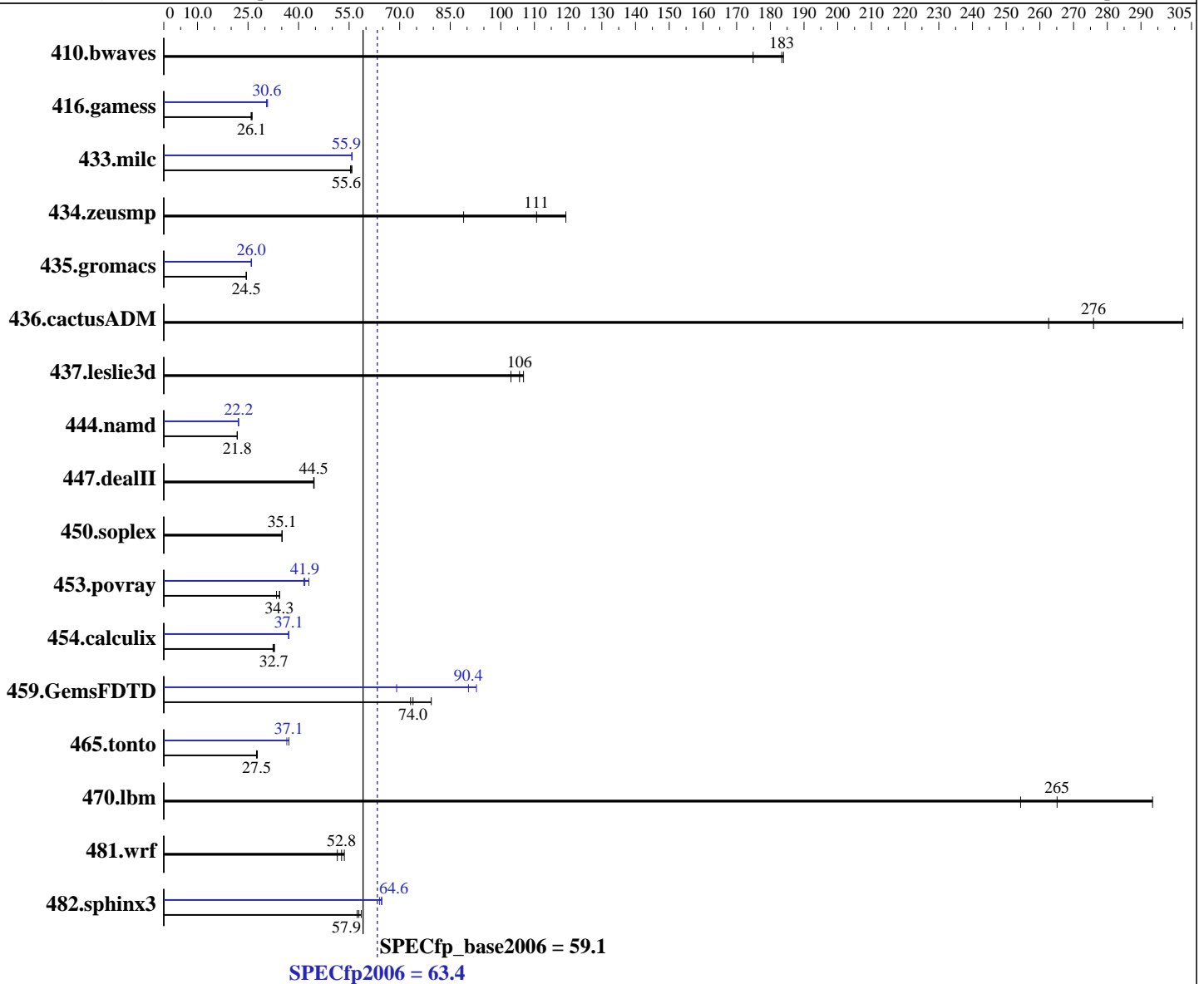
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon X5690
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
 CPU MHz: 3467
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SuSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
Disk Subsystem: 2 x 50 GB SATA, SSD, RAID 0
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binaries compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	74.1	183	73.9	184	77.7	175	74.1	183	73.9	184	77.7	175
416.gamess	751	26.1	756	25.9	747	26.2	636	30.8	641	30.6	642	30.5
433.milc	164	55.8	165	55.6	166	55.4	164	55.9	165	55.8	164	55.9
434.zeusmp	102	89.0	82.3	111	76.3	119	102	89.0	82.3	111	76.3	119
435.gromacs	292	24.5	292	24.5	291	24.5	275	26.0	275	26.0	274	26.0
436.cactusADM	45.5	263	39.5	302	43.3	276	45.5	263	39.5	302	43.3	276
437.leslie3d	88.0	107	91.2	103	89.0	106	88.0	107	91.2	103	89.0	106
444.namd	368	21.8	368	21.8	368	21.8	362	22.1	362	22.2	362	22.2
447.dealII	257	44.5	257	44.5	256	44.6	257	44.5	257	44.5	256	44.6
450.soplex	237	35.1	237	35.1	238	35.1	237	35.1	237	35.1	238	35.1
453.povray	159	33.5	155	34.3	155	34.4	127	41.9	128	41.6	124	43.1
454.calculix	252	32.7	254	32.4	251	32.9	223	37.1	223	37.1	223	37.1
459.GemsFDTD	145	73.2	134	79.4	143	74.0	154	69.1	117	90.4	114	92.8
465.tonto	358	27.5	358	27.5	355	27.8	265	37.1	265	37.1	270	36.5
470.lbm	54.0	254	51.8	265	46.8	293	54.0	254	51.8	265	46.8	293
481.wrf	217	51.5	212	52.8	208	53.6	217	51.5	212	52.8	208	53.6
482.sphinx3	340	57.4	333	58.6	337	57.9	304	64.0	302	64.6	301	64.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

Load Default BIOS Settings and then change the following
Turbo Mode enabled
Turbo Boost set to Traditional
Power C-states enabled
Demand Scrub disabled



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General Notes

OMP_NUM_THREADS set to number of cores

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias`

470.lbm: `basepeak = yes`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel`

C++ benchmarks:

444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32`

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Peak Optimization Flags (Continued)

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.01.xml>



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