SPEC® CINT2006 Result
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Fujitsu

PRIMERGY BX922 S2, Intel Xeon X5687, 3.60 GHz

$SPECint^{®}_rate2006 = $ 165

$SPECint_rate_base2006 = $ 157

CPU2006 license:	19	**Test date:**	Feb-2011
Test sponsor:	Fujitsu	**Hardware Availability:**	Feb-2011
Tested by:	Fujitsu	**Software Availability:**	Jan-2011

Copies 0 30.0 60.0 90.0 120 150 180 210 240 270 300 330 360 390 420 450 480 510 540 570 600 630 660 690 720 780

400.perlbench 8 / 151 ; 8 / 124

401.bzip2 8 / 101 ; 8 / 94.0

403.gcc 8 / 121 ; 8 / 121

429.mcf 4 / 176 ; 8 / 164

445.gobmk 8 / 149 ; 8 / 141

456.hmmer 4 / 216 ; 8 / 198

458.sjeng 8 / 145 ; 8 / 137

462.libquantum 8 / 772

464.h264ref 8 / 196

471.omnetpp 8 / 114 ; 8 / 106

473.astar 8 / 97.9

483.xalancbmk 8 / 158

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Hardware

CPU Name:	Intel Xeon X5687
CPU Characteristics:	Intel Turbo Boost Technology up to 3.87 GHz
CPU MHz:	3600
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1 x SAS, 300 GB, 10000 RPM
Other Hardware:	--

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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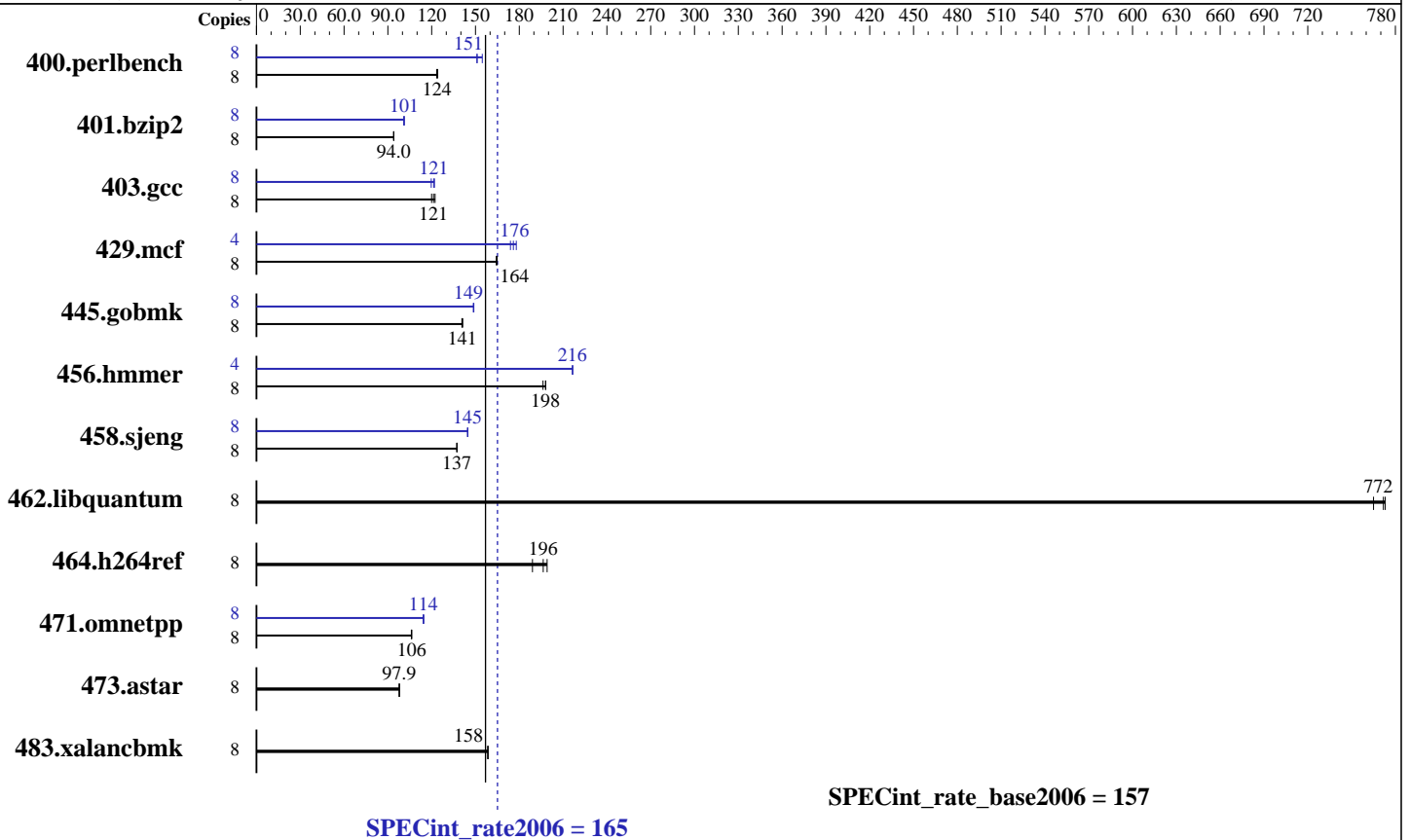SPECint_rate_base2006 = 157

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**632**	**124**	630	124	633	123	8	518	151	505	155	**517**	**151**
401.bzip2	8	821	94.0	822	94.0	**821**	**94.0**	8	763	101	765	101	**763**	**101**
403.gcc	8	537	120	526	122	**531**	**121**	8	528	122	**531**	**121**	538	120
429.mcf	8	**444**	**164**	443	165	444	164	4	210	174	205	178	**207**	**176**
445.gobmk	8	**595**	**141**	594	141	597	141	8	564	149	**564**	**149**	565	149
456.hmmer	8	380	196	377	198	**377**	**198**	4	172	217	**172**	**216**	172	216
458.sjeng	8	**705**	**137**	705	137	705	137	8	670	145	669	145	**669**	**145**
462.libquantum	8	**215**	**772**	214	773	217	765	8	**215**	**772**	214	773	217	765
464.h264ref	8	889	199	**902**	**196**	937	189	8	889	199	**902**	**196**	937	189
471.omnetpp	8	**471**	**106**	471	106	470	106	8	438	114	436	115	**437**	**114**
473.astar	8	574	97.9	**574**	**97.9**	574	97.8	8	574	97.9	**574**	**97.9**	574	97.8
483.xalancbmk	8	**348**	**158**	348	159	348	158	8	**348**	**158**	348	159	348	158

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
 'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 Hugepages were not configured on the system
```

Platform Notes

```
 BIOS configuration:
 Data Reuse Optimization = Disable
 Performance/Power Setting = Traditional
```

General Notes

```
 For information about Fujitsu please visit: http://www.fujitsu.com
 Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/smartheap -lsmartheap
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

      401.bzip2: icc -m64

    456.hmmer: icc -m64

      458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

   401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

     403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
               -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

     429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

   445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

   456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

   458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

  464.h264ref: basepeak = yes
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/smartheap -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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