



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = 204

PRIMERGY BX620 S6, Intel Xeon X5675, 3.07 GHz

SPECint\_rate\_base2006 = 191

CPU2006 license: 19

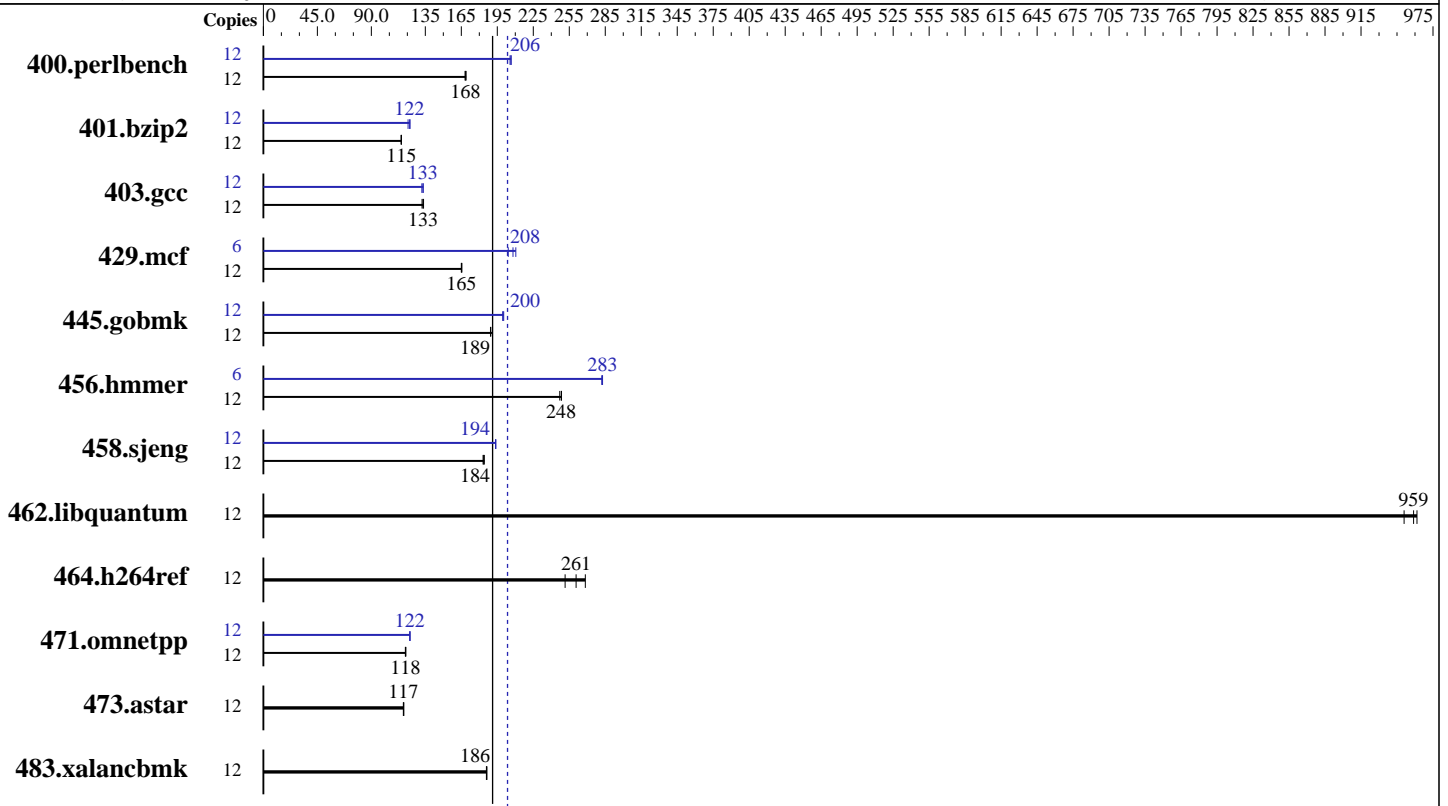
Test date: Jan-2011

Test sponsor: Fujitsu

Hardware Availability: Feb-2011

Tested by: Fujitsu

Software Availability: Jan-2011



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### Hardware

CPU Name: Intel Xeon X5675  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.47 GHz  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM  
 Other Hardware: --

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	694	169	697	168	<b>696</b>	<b>168</b>	12	<b>568</b>	<b>206</b>	570	206	568	206
401.bzip2	12	1006	115	1008	115	<b>1007</b>	<b>115</b>	12	<b>950</b>	<b>122</b>	947	122	960	121
403.gcc	12	724	134	<b>726</b>	<b>133</b>	730	132	12	<b>726</b>	<b>133</b>	731	132	725	133
429.mcf	12	<b>662</b>	<b>165</b>	662	165	663	165	6	<b>263</b>	<b>208</b>	260	210	268	204
445.gobmk	12	<b>664</b>	<b>189</b>	660	191	665	189	12	628	200	631	200	<b>630</b>	<b>200</b>
456.hammer	12	453	247	<b>451</b>	<b>248</b>	450	249	6	198	283	198	282	<b>198</b>	<b>283</b>
458.sjeng	12	<b>791</b>	<b>184</b>	793	183	788	184	12	<b>750</b>	<b>194</b>	750	194	749	194
462.libquantum	12	259	962	261	951	<b>259</b>	<b>959</b>	12	259	962	261	951	<b>259</b>	<b>959</b>
464.h264ref	12	<b>1019</b>	<b>261</b>	1056	252	990	268	12	<b>1019</b>	<b>261</b>	1056	252	990	268
471.omnetpp	12	<b>633</b>	<b>118</b>	633	118	631	119	12	614	122	<b>614</b>	<b>122</b>	614	122
473.astar	12	722	117	<b>721</b>	<b>117</b>	720	117	12	722	117	<b>721</b>	<b>117</b>	720	117
483.xalancbmk	12	445	186	444	186	<b>445</b>	<b>186</b>	12	445	186	444	186	<b>445</b>	<b>186</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Hugepages were not configured on the system

## Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable  
Performance/Power Setting = Traditional

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

## Base Compiler Invocation

C benchmarks:  
icc -m32 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m32 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: icc -m32  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: icc -m32

445.gobmk: icc -m32

462.libquantum: icc -m32  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

464.h264ref: icc -m32  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

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## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m32 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

471.omnetpp: icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

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## Peak Optimization Flags (Continued)

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110303.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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