



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 75.4

PRIMERGY TX200 S6, Intel Xeon E5607, 2.27 GHz

SPECfp_rate_base2006 = 72.8

CPU2006 license: 19

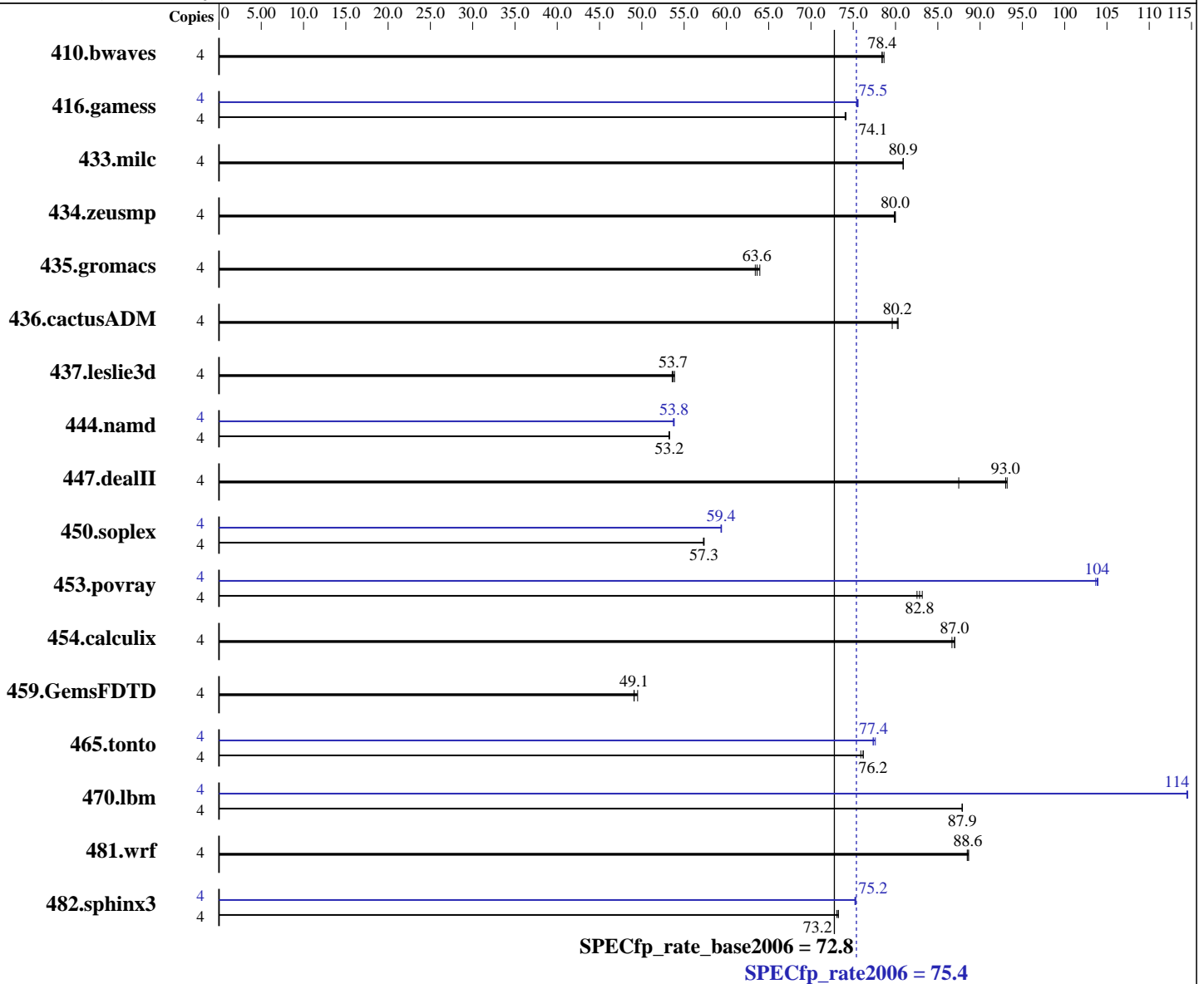
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2010

Hardware Availability: Jul-2010

Software Availability: Nov-2010



Hardware

CPU Name: Intel Xeon E5607
 CPU Characteristics:
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.0.082 Build 20101006
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz and CL7)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: none

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	693	78.4	691	78.6	693	78.4	4	693	78.4	691	78.6	693	78.4
416.gamess	4	1057	74.1	1058	74.0	1056	74.1	4	1036	75.6	1038	75.4	1037	75.5
433.milc	4	454	80.9	454	80.9	454	80.9	4	454	80.9	454	80.9	454	80.9
434.zeusmp	4	455	80.0	456	79.9	455	80.0	4	455	80.0	456	79.9	455	80.0
435.gromacs	4	447	64.0	450	63.4	449	63.6	4	447	64.0	450	63.4	449	63.6
436.cactusADM	4	600	79.6	596	80.2	595	80.3	4	600	79.6	596	80.2	595	80.3
437.leslie3d	4	702	53.6	700	53.7	698	53.9	4	702	53.6	700	53.7	698	53.9
444.namd	4	603	53.2	602	53.3	603	53.2	4	596	53.8	597	53.7	596	53.8
447.dealII	4	492	93.0	491	93.2	523	87.5	4	492	93.0	491	93.2	523	87.5
450.soplex	4	582	57.3	582	57.3	582	57.4	4	562	59.4	562	59.4	561	59.4
453.povray	4	256	83.2	258	82.5	257	82.8	4	205	104	205	104	205	104
454.calculix	4	381	86.7	379	87.0	379	87.0	4	381	86.7	379	87.0	379	87.0
459.GemsFDTD	4	864	49.1	865	49.1	857	49.5	4	864	49.1	865	49.1	857	49.5
465.tonto	4	517	76.2	519	75.9	517	76.2	4	508	77.4	507	77.6	509	77.3
470.lbm	4	625	87.9	626	87.9	625	87.9	4	480	115	480	114	480	114
481.wrf	4	505	88.5	504	88.6	505	88.6	4	505	88.5	504	88.6	505	88.6
482.sphinx3	4	1065	73.2	1064	73.2	1067	73.1	4	1035	75.3	1036	75.2	1037	75.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable



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General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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