



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 380

Servidor Itaotec MX224 (Intel Xeon X5680)

SPECint\_rate\_base2006 = 354

CPU2006 license: 9001

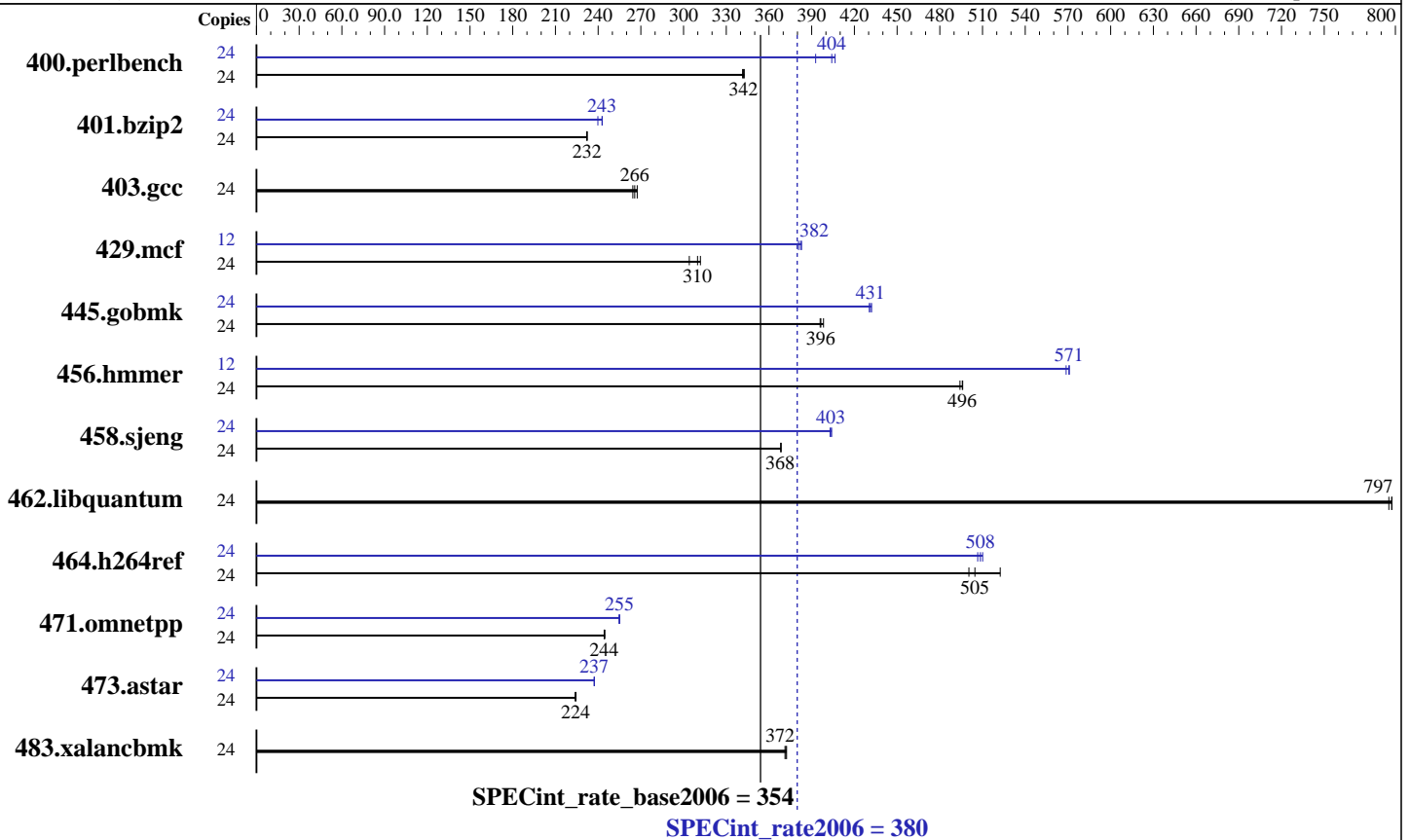
Test date: Dec-2010

Test sponsor: Itaotec

Hardware Availability: Feb-2011

Tested by: Itaotec

Software Availability: Apr-2010



## Hardware

CPU Name: Intel Xeon X5680  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l\_cproc\_p\_11.1.072  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	687	341	<b>685</b>	<b>342</b>	684	343	24	597	393	577	406	<b>580</b>	<b>404</b>
401.bzip2	24	998	232	<b>997</b>	<b>232</b>	997	232	24	966	240	954	243	<b>954</b>	<b>243</b>
403.gcc	24	722	267	<b>727</b>	<b>266</b>	731	264	24	722	267	<b>727</b>	<b>266</b>	731	264
429.mcf	24	720	304	<b>707</b>	<b>310</b>	702	312	12	286	383	287	381	<b>286</b>	<b>382</b>
445.gobmk	24	632	398	636	396	<b>635</b>	<b>396</b>	24	<b>584</b>	<b>431</b>	583	432	585	430
456.hammer	24	<b>452</b>	<b>496</b>	451	496	453	494	12	196	571	197	569	<b>196</b>	<b>571</b>
458.sjeng	24	<b>788</b>	<b>368</b>	788	368	788	368	24	721	403	719	404	<b>720</b>	<b>403</b>
462.libquantum	24	625	795	624	798	<b>624</b>	<b>797</b>	24	625	795	624	798	<b>624</b>	<b>797</b>
464.h264ref	24	1017	522	1061	500	<b>1052</b>	<b>505</b>	24	<b>1045</b>	<b>508</b>	1048	507	1041	510
471.omnetpp	24	613	245	614	244	<b>614</b>	<b>244</b>	24	589	255	588	255	<b>588</b>	<b>255</b>
473.astar	24	<b>752</b>	<b>224</b>	753	224	751	224	24	710	237	710	237	<b>710</b>	<b>237</b>
483.xalancbmk	24	446	371	445	372	<b>445</b>	<b>372</b>	24	446	371	445	372	<b>445</b>	<b>372</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## General Notes

This result was measured on the Servidor Itaotec MX223+.  
The Servidor Itaotec MX203+, the Servidor Itaotec MX224 and the Servidor Itaotec MX223+ are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8/lib -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101123.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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