



SPEC® CINT2006 Result

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HITACHI

SPECint®_rate2006 = 78.3

BladeSymphony BS2000 (Intel Xeon E5503)

SPECint_rate_base2006 = 72.9

CPU2006 license: 872

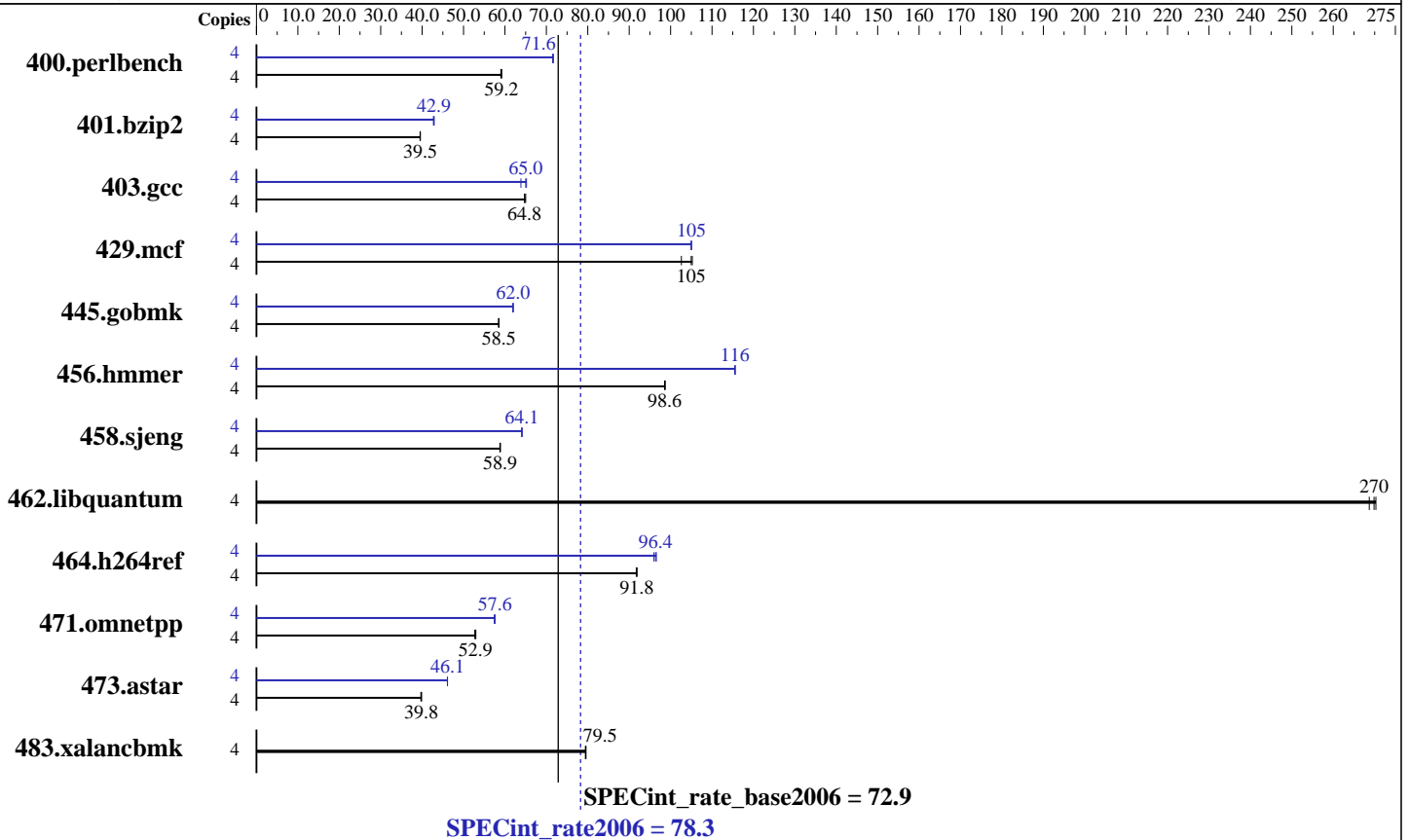
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Oct-2010

Hardware Availability: Apr-2010

Software Availability: Dec-2009



Hardware

CPU Name: Intel Xeon E5503
 CPU Characteristics: 2000
 CPU MHz: Integrated
 FPU: 4 cores, 2 chips, 2 cores/chip
 CPU(s) enabled: 1, 2 chips
 CPU(s) orderable: 32 KB I + 32 KB D on chip per core
 Primary Cache: 256 KB I+D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC, running at 800 MHz)
 Memory: 2 x 146 GB 10000 rpm SAS RAID1 configuration
 Disk Subsystem: None
 Other Hardware:

Software

Operating System: Red Hat Enterprise Linux Server release 5.4.3, Advanced Platform, Kernel 2.6.18-164.9.1.el5 on an x86_64
 Compiler: Intel C++ Compiler 11.1 for Linux Build 20091012 Package ID: l_cproc_p_11.1.059
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	<u>661</u>	<u>59.2</u>	660	59.2	662	59.0	4	<u>546</u>	<u>71.6</u>	546	71.5	545	71.7
401.bzip2	4	975	39.6	<u>976</u>	<u>39.5</u>	977	39.5	4	900	42.9	<u>900</u>	<u>42.9</u>	903	42.7
403.gcc	4	497	64.7	<u>497</u>	<u>64.8</u>	495	65.0	4	504	63.9	494	65.2	<u>495</u>	<u>65.0</u>
429.mcf	4	356	103	<u>348</u>	<u>105</u>	347	105	4	347	105	<u>347</u>	<u>105</u>	348	105
445.gobmk	4	717	58.5	717	58.5	<u>717</u>	<u>58.5</u>	4	677	62.0	<u>677</u>	<u>62.0</u>	677	61.9
456.hammer	4	379	98.6	378	98.7	<u>379</u>	<u>98.6</u>	4	323	116	323	115	<u>323</u>	<u>116</u>
458.sjeng	4	<u>822</u>	<u>58.9</u>	824	58.8	822	58.9	4	<u>755</u>	<u>64.1</u>	755	64.1	756	64.1
462.libquantum	4	307	270	<u>307</u>	<u>270</u>	308	269	4	307	270	<u>307</u>	<u>270</u>	308	269
464.h264ref	4	963	91.9	<u>964</u>	<u>91.8</u>	965	91.7	4	917	96.6	922	96.0	<u>919</u>	<u>96.4</u>
471.omnetpp	4	474	52.7	<u>472</u>	<u>52.9</u>	472	53.0	4	434	57.6	<u>434</u>	<u>57.6</u>	435	57.4
473.astar	4	705	39.8	<u>705</u>	<u>39.8</u>	707	39.7	4	609	46.1	<u>609</u>	<u>46.1</u>	609	46.1
483.xalancbmk	4	347	79.6	347	79.4	<u>347</u>	<u>79.5</u>	4	347	79.6	347	79.4	<u>347</u>	<u>79.5</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'/usr/bin/numactl' used to bind processes to CPUs

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/bsc/smartheap/lib -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

`401.bzip2: icc -m64`

`456.hmmer: icc -m64`

`458.sjeng: icc -m64`

C++ benchmarks (except as noted below):

`icpc -m32`

`473.astar: icpc -m64`

Peak Portability Flags

`400.perlbench: -DSPEC_CPU_LINUX_IA32`

`401.bzip2: -DSPEC_CPU_LP64`

`456.hmmer: -DSPEC_CPU_LP64`

`458.sjeng: -DSPEC_CPU_LP64`

`462.libquantum: -DSPEC_CPU_LINUX`

`473.astar: -DSPEC_CPU_LP64`

`483.xalancbmk: -DSPEC_CPU_LINUX`



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Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/home/bsc/smartheap/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
 -L/home/bsc/smartheap/lib -lsmartheap64

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

Continued on next page



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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.03.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.03.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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