SPEC® CINT2006 Result

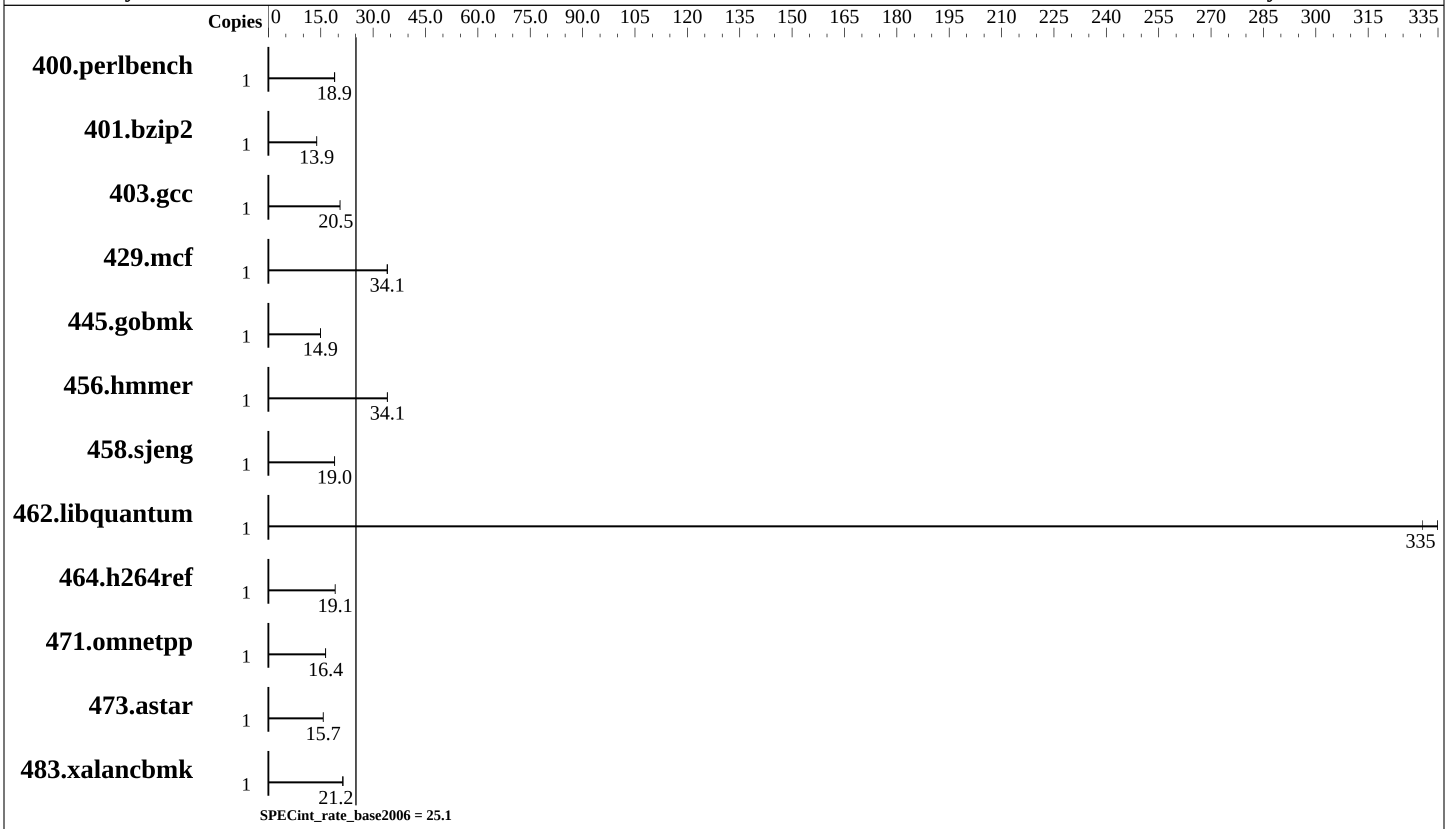
J.P Sa Couto S.A.

TSUNAMI Tidal S6026RF server system (Intel Xeon X5620)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 25.1

CPU2006 license:	4105	**Test date:**	Dec-2010
Test sponsor:	J.P Sa Couto S.A.	**Hardware Availability:**	Nov-2010
Tested by:	J.P Sa Couto S.A.	**Software Availability:**	Nov-2010



Hardware

CPU Name:	Intel Xeon E5620
CPU Characteristics:	Intel Turbo Boost Technology up to 2.66 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB PC3-10600R-9, ECC)
Disk Subsystem:	INTEL X25-M SATA SSD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ CompilerXE for Intel 64, Version 12.0 Package ID: l_ccompxe_intel64_2011.0.084
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	Not Applicable
Other Software:	None

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1	**517**	**18.9**	517	18.9	513	19.0							
401.bzip2	1	698	13.8	696	13.9	**696**	**13.9**							
403.gcc	1	392	20.6	**392**	**20.5**	392	20.5							
429.mcf	1	267	34.2	**268**	**34.1**	268	34.0							
445.gobmk	1	702	15.0	704	14.9	**703**	**14.9**							
456.hmmer	1	274	34.1	**274**	**34.1**	274	34.0							
458.sjeng	1	637	19.0	639	18.9	**637**	**19.0**							
462.libquantum	1	61.9	335	**61.9**	**335**	62.7	331							
464.h264ref	1	1156	19.1	1155	19.2	**1156**	**19.1**							
471.omnetpp	1	382	16.4	**381**	**16.4**	381	16.4							
473.astar	1	**447**	**15.7**	446	15.7	447	15.7							
483.xalancbmk	1	**325**	**21.2**	326	21.2	322	21.4							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-DSPEC_CPU_LP64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

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Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20100609.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20100609.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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