SPEC® CINT2006 Result

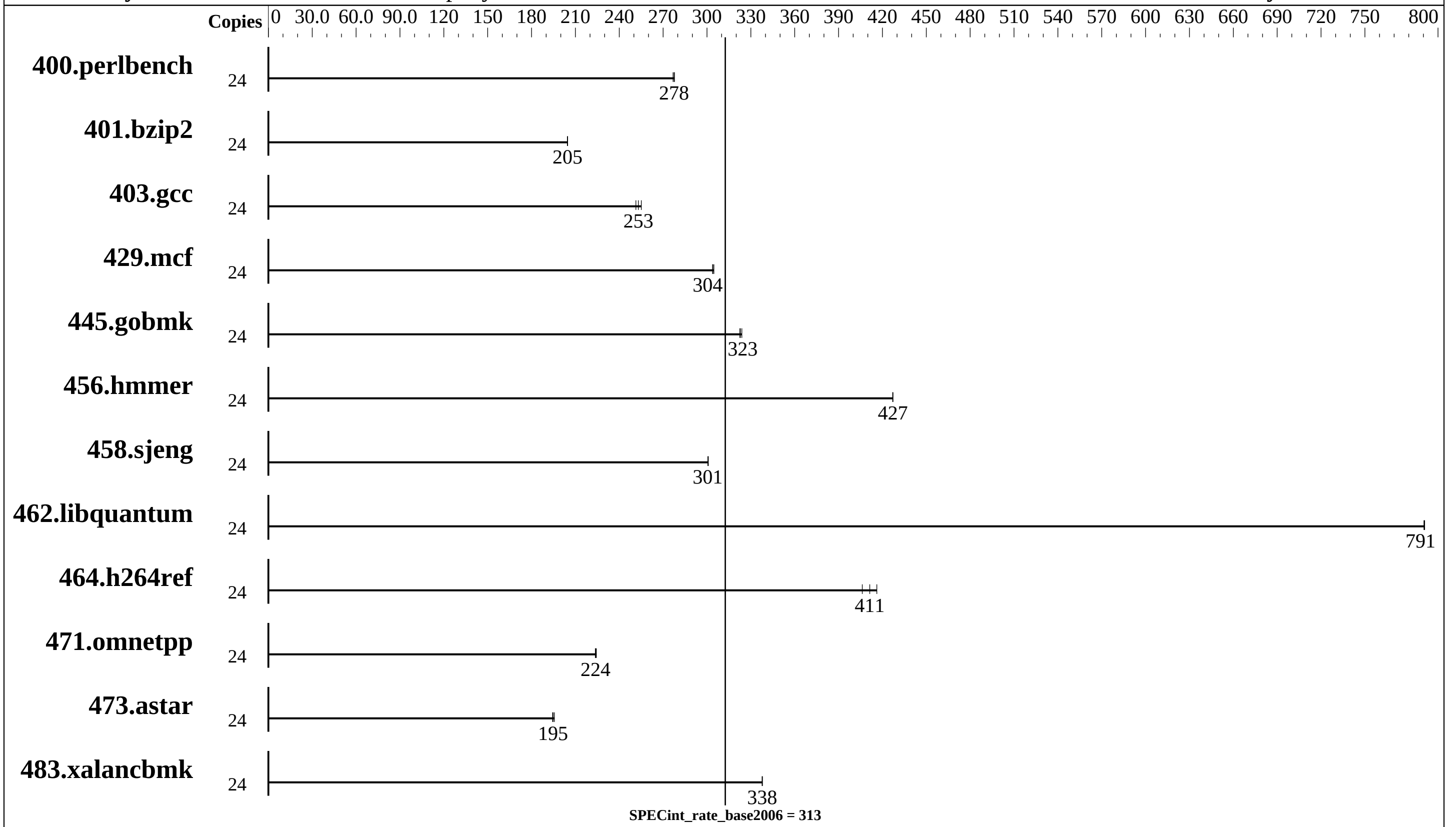
Hewlett-Packard Company

ProLiant DL360 G7
(2.67 GHz, Intel Xeon X5650)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 313

CPU2006 license:	3	**Test date:**	Sep-2010
Test sponsor:	Hewlett-Packard Company	**Hardware Availability:**	Jun-2010
Tested by:	Hewlett-Packard Company	**Software Availability:**	Mar-2010



Hardware

CPU Name:	Intel Xeon X5650
CPU Characteristics:	Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	2x146 GB 10 K SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.5, Advanced Platform, Kernel 2.6.18-194.el5
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1

Hewlett-Packard Company

ProLiant DL360 G7
(2.67 GHz, Intel Xeon X5650)

SPECint_rate2006 = Not Run

SPECint_rate_base2006 = 313

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company
Test date: Sep-2010
Hardware Availability: Jun-2010
Software Availability: Mar-2010

Results Table

	Base							Peak						
Benchmark	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Copies**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	24	**845**	**278**	844	278	847	277							
401.bzip2	24	1132	205	**1132**	**205**	1131	205							
403.gcc	24	**763**	**253**	769	251	757	255							
429.mcf	24	**720**	**304**	720	304	718	305							
445.gobmk	24	777	324	781	323	**780**	**323**							
456.hmmer	24	524	427	524	427	**524**	**427**							
458.sjeng	24	966	301	**966**	**301**	965	301							
462.libquantum	24	629	790	**629**	**791**	629	791							
464.h264ref	24	1307	406	1276	416	**1291**	**411**							
471.omnetpp	24	671	224	669	224	**670**	**224**							
473.astar	24	861	196	**865**	**195**	867	194							
483.xalancbmk	24	490	338	**490**	**338**	490	338							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
BIOS configuration:
 HP Power Profile set to Maximum Performance
 Thermal Configuration set to Increased Cooling
 Data Reuse set to Disabled
```

General Notes

```
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.html
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.20101118.xml
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 14:57:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 17 November 2010.