SPEC® CINT2006 Result

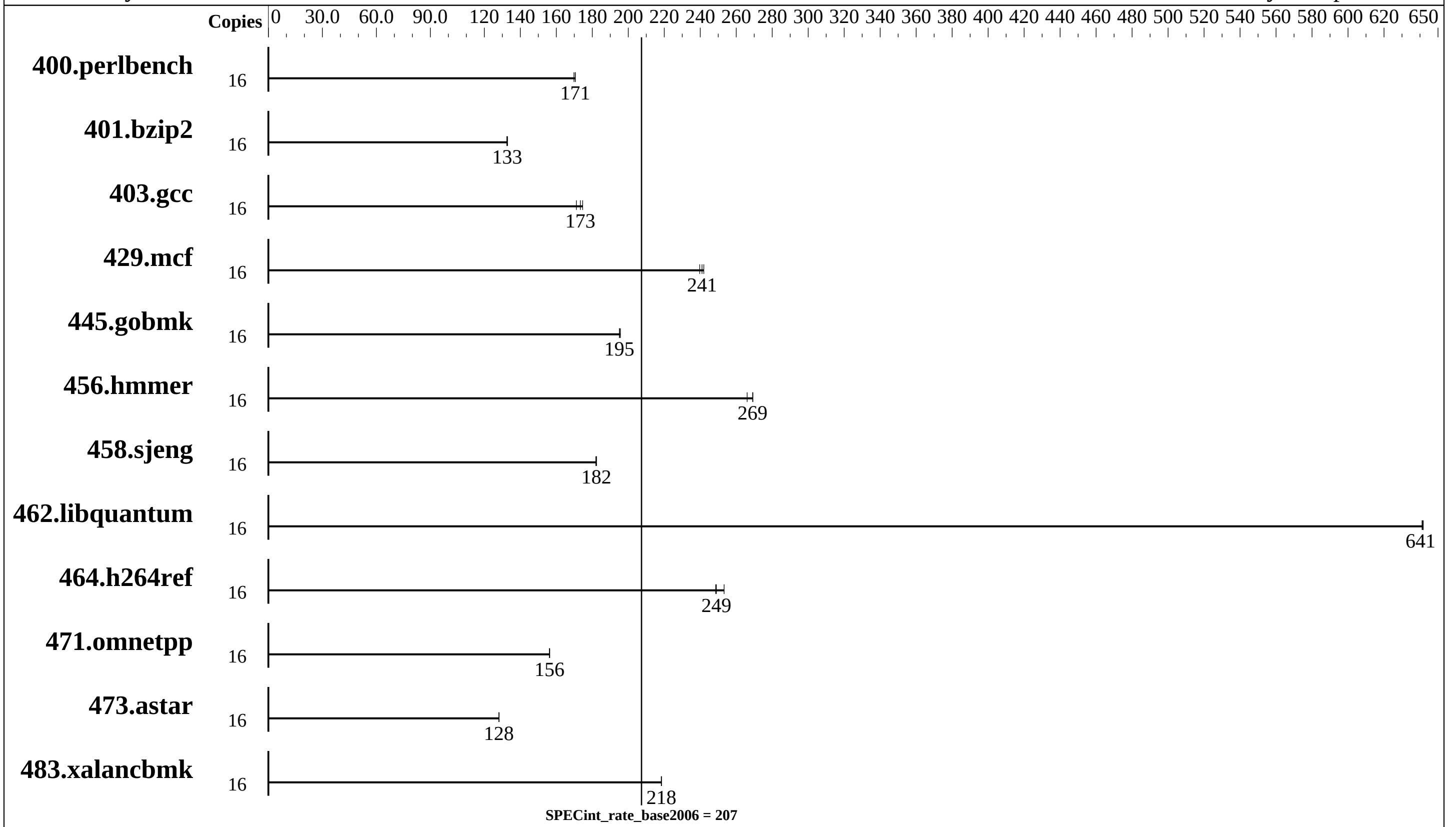
Itautec

Servidor Itautec MX203+ (Intel Xeon E5620)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 207

CPU2006 license:	9001	**Test date:**	Oct-2010
Test sponsor:	Itautec	**Hardware Availability:**	Apr-2010
Tested by:	Itautec	**Software Availability:**	Apr-2010



Hardware

CPU Name:	Intel Xeon E5620
CPU Characteristics:	Intel Turbo Boost Technology up to 2.66 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB 2Rx4 PC3-8500R-7 ECC)
Disk Subsystem:	1 x 160 GB SATA-2, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler 11.1 for Linux Build 20100414 Package ID: l_cproc_p_11.1.072
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	916	171	**917**	**171**	920	170							
401.bzip2	16	1162	133	**1163**	**133**	1165	133							
403.gcc	16	**743**	**173**	753	171	737	175							
429.mcf	16	609	240	**605**	**241**	603	242							
445.gobmk	16	**860**	**195**	861	195	858	196							
456.hmmer	16	561	266	554	269	**555**	**269**							
458.sjeng	16	1064	182	1062	182	**1062**	**182**							
462.libquantum	16	**517**	**641**	517	641	517	642							
464.h264ref	16	**1422**	**249**	1424	249	1398	253							
471.omnetpp	16	**640**	**156**	640	156	640	156							
473.astar	16	**877**	**128**	876	128	877	128							
483.xalancbmk	16	506	218	**505**	**218**	505	218							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 numactl was used to bind copies to the cores
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
```

General Notes

```
This result was measured on the Servidor Itautec MX223+.
The Servidor Itautec MX223+, the Servidor Itautec MX224 and the Servidor Itautec MX203+
are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/opt/sh/SmartHeap_8.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.20101110.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.20101110.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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