SPEC® CFP2006 Result

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Acer Incorporated

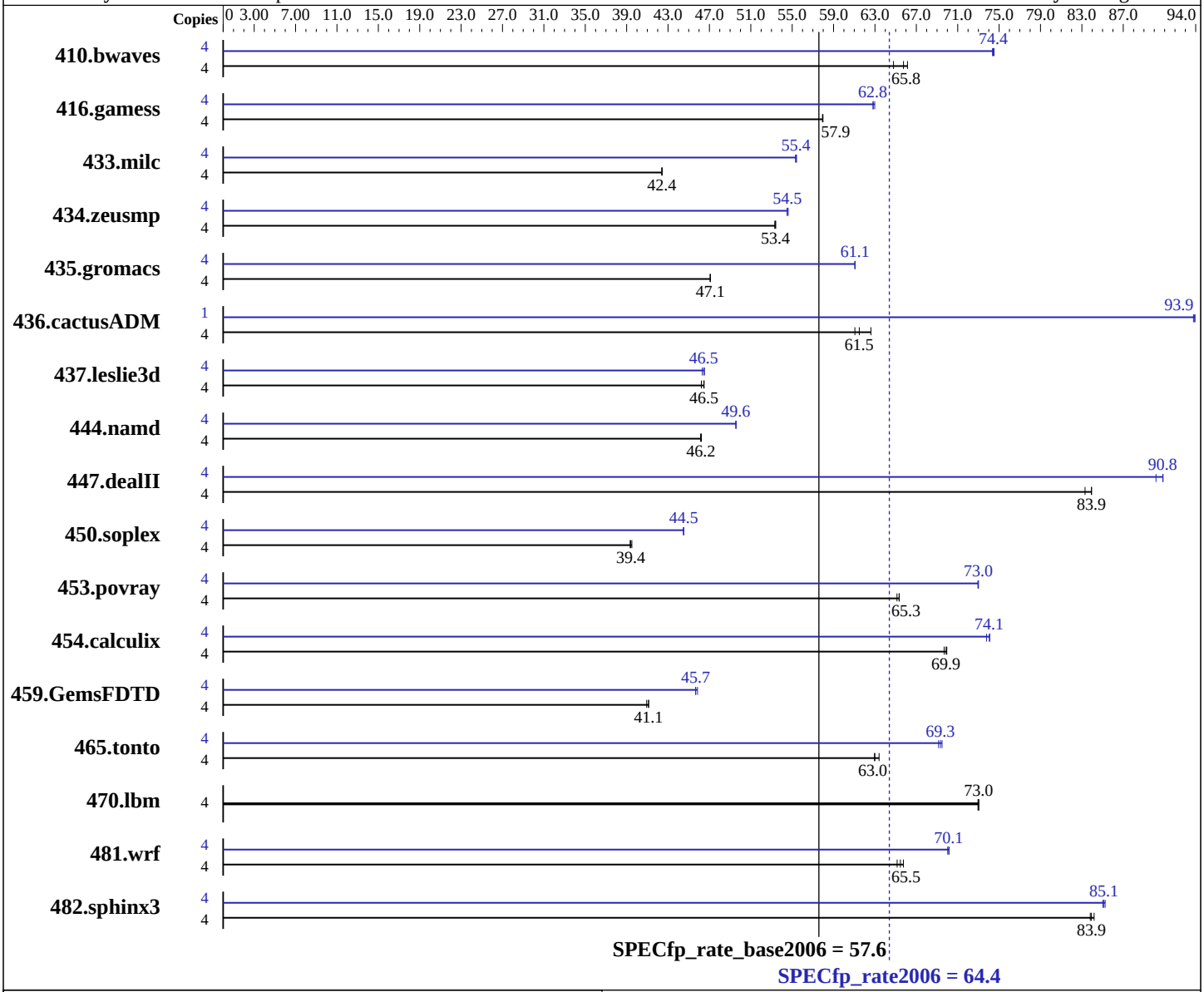
Gateway GT115 F1 (AMD Opteron 4122, 2.20 GHz)

SPECfp®_rate2006 = 64.4

SPECfp_rate_base2006 = 57.6

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Oct-2010
Hardware Availability: Oct-2010
Software Availability: Aug-2010



Hardware

CPU Name:	AMD Opteron 4122
CPU Characteristics:	
CPU MHz:	2200
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	x86 Open64 4.2.4 Compiler Suite (from AMD)
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	binutils 2.18

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L3 Cache: 6 MB I+D on chip per chip
Other Cache: None
Memory: 16 GB (2 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 640 GB SATA, 7200 RPM
Other Hardware: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	822	66.1	839	64.8	**827**	**65.8**	4	731	74.4	730	74.5	**730**	**74.4**
416.gamess	4	**1352**	**57.9**	1352	57.9	1351	58.0	4	**1247**	**62.8**	1247	62.8	1244	63.0
433.milc	4	865	42.4	**866**	**42.4**	866	42.4	4	664	55.3	**663**	**55.4**	663	55.4
434.zeusmp	4	682	53.4	**682**	**53.4**	683	53.3	4	667	54.6	**667**	**54.5**	668	54.5
435.gromacs	4	606	47.1	607	47.1	**607**	**47.1**	4	468	61.1	468	61.1	**468**	**61.1**
436.cactusADM	4	**777**	**61.5**	783	61.1	763	62.6	1	**127**	**93.9**	127	93.9	127	93.8
437.leslie3d	4	813	46.2	809	46.5	**809**	**46.5**	4	812	46.3	**809**	**46.5**	808	46.5
444.namd	4	695	46.2	694	46.2	**694**	**46.2**	4	647	49.6	**647**	**49.6**	647	49.6
447.dealII	4	**545**	**83.9**	545	83.9	549	83.3	4	**504**	**90.8**	504	90.8	508	90.2
450.soplex	4	848	39.3	**847**	**39.4**	844	39.5	4	**750**	**44.5**	749	44.5	750	44.5
453.povray	4	326	65.3	**326**	**65.3**	327	65.1	4	**292**	**73.0**	292	73.0	292	73.0
454.calculix	4	**472**	**69.9**	474	69.7	472	69.9	4	447	73.8	**446**	**74.1**	445	74.1
459.GemsFDTD	4	1032	41.1	1037	40.9	**1032**	**41.1**	4	926	45.9	929	45.7	**929**	**45.7**
465.tonto	4	621	63.4	625	63.0	**625**	**63.0**	4	**568**	**69.3**	569	69.1	566	69.5
470.lbm	4	**753**	**73.0**	753	73.0	753	73.0	4	**753**	**73.0**	753	73.0	753	73.0
481.wrf	4	686	65.1	680	65.7	**683**	**65.5**	4	637	70.2	638	70.0	**637**	**70.1**
482.sphinx3	4	930	83.8	**929**	**83.9**	926	84.2	4	914	85.3	**916**	**85.1**	917	85.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152'  was used to set environment locked pages in memory limit

Set vm/nr_hugepages=3584 in /etc/sysctl.conf
mount -t hugetlbfs nodev /mnt/hugepages
```

General Notes

```
Environment variables set by runspec before the start of the run:
HUGETLB_LIMIT = "896"
LD_LIBRARY_PATH = "/usr/cpu2006/amd1002-rate-libs-revC/64:/usr/cpu2006/amd1002-rate-libs-revC/32"
OMP_NUM_THREADS = "4"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at
http://developer.amd.com/cpu/open64
Binaries were compiled on SLES10 SP2 with binutils 2.18
The Acer AT115 F1, and Gateway GT115 F1 are electronically equivalent.
This result was measured on Gateway GT115 F1.
```

Base Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Fortran benchmarks:
openf95

Benchmarks using both Fortran and C:
opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
-fno-second-underscore
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
`-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m`

C++ benchmarks:
`-march=barcelona -mso -Ofast -static -INLINE:aggressive=on -OPT:malloc_alg=1 -HP:bdt=2m`

Fortran benchmarks:
`-march=barcelona -mso -Ofast -HP`

Benchmarks using both Fortran and C:
`-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP`

Peak Compiler Invocation

C benchmarks:
`opencc`

C++ benchmarks:
`openCC`

Fortran benchmarks:
`openf95`

Benchmarks using both Fortran and C:
`opencc openf95`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64`
436.cactusADM: `-DSPEC_CPU_LP64 -fno-second-underscore`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG -fno-second-underscore`
482.sphinx3: `-DSPEC_CPU_LP64`

Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.dealII: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m
-CG:cmp_peep=on

416.gamess: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll_size=256
-CG:cmp_peep=on -GRA:prioritize_by_density=on -HP

Continued on next page

Peak Optimization Flags (Continued)

437.leslie3d: `-march=barcelona -mso -Ofast -HP:bdt=2m:heap=2m`

459.GemsFDTD: `-march=barcelona -mso -Ofast -LNO:fission=2 -LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1 -HP`

465.tonto: `-march=barcelona -mso -Ofast -OPT:alias=no_f90_pointer_alias -LNO:blocking=off -CG:load_exe=1 -IPA:plimit=525 -HP`

Benchmarks using both Fortran and C:

435.gromacs: `-march=barcelona -mso -Ofast -OPT:rsqrt=2 -HP:bdt=2m:heap=2m`

436.cactusADM: `-march=barcelona -mso -fb_create fbdata`(pass 1) `-fb_opt fbdata`(pass 2) `-Ofast -apo -LNO:prefetch_ahead=1 -HP:bdt=2m:heap=2m -LANG:heap_allocation_threshold=100`

454.calculix: `-march=barcelona -mso -Ofast -CG:load_exe=0 -CG:ptr_load_use=0 -CG:local_sched_alg=2 -CG:compute_to=on -LNO:prefetch_ahead=30 -WOPT:unroll=2 -GRA:optimize_boundary=on -HP:bdt=2m:heap=2m`

481.wrf: `-march=barcelona -mso -Ofast -LNO:blocking=off -LNO:prefetch_ahead=10 -LANG:copyinout=off -IPA:callee_limit=5000 -GRA:prioritize_by_density=on -m3dnow -HP`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html
http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml
http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml

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